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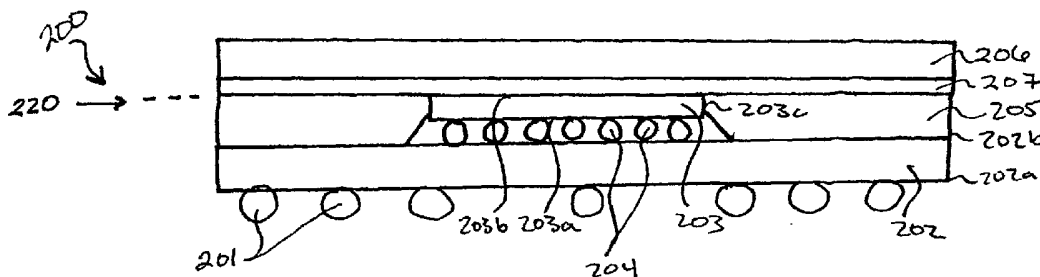
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(54) Title: MOLDED HIGH DENSITY ELECTRONIC PACKAGING STRUCTURE FOR HIGH PERFORMANCE APPLICATIONS



(57) Abstract: The invention discloses a thermally enhanced ball grid array package (200) including a base layer element (202) and a flip chip die (203) mounted on the base layer element (202). The die (203) has a first surface (203a) electrically coupled to the base layer element (202), a second surface (203b) opposite to the first surface (203a), and lateral sides (203c). A molding compound (205) encapsulates the base layer element (202) and the lateral sides (203c) of the die (203). A surface (220) is formed of the second surface (203b) of the die (203) and an upper surface of the molding compound. A material (207) is disposed on the surface (220), and a heat spreader (206) is mounted on the material.

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<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 20020074669 A1 (Watanabe et al) 20 June 2002 (20.06.2002), figure 12.	1-6, 12-15, 22, 23, 25, 27, 28
X	US 6,359,335 B1 (Distefano et al) 19 March 2002 (19.03.2002), figure 15A.	19-23, 26, 29, 30
X	US 5,909,056 A (Mertol) 01 June 1999 (01.06.1999), figure 4C.	22, 24
X	US 20020008316 A1 (Miyazaki) 24 January 2002 (24.01.2002), figure 3G.	1-5, 7-11, 22, 23
A	US 6,444,498 B1 (Huang et al) 03 September 2002 (03.09.2002), figures 2-7.	1-30
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