



(51) International Patent Classification:
H04N 19/149 (2014.01)

(21) International Application Number:
PCT/CN2024/101758

(22) International Filing Date:
26 June 2024 (26.06.2024)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
PCT/CN2023/102887
27 June 2023 (27.06.2023) CN

(71) Applicants: **DOUYIN VISION CO., LTD.** [CN/CN]; Room B-0035, 2/F, No.3 Building, No. 30, Shixing Road, Shijingshan District, Beijing 100041 (CN). **BYTEDANCE INC.** [US/US]; 12655 West Jefferson Boulevard, Sixth Floor, Suite No. 137, Los Angeles, California 90066 (US).

(72) Inventors: **WANG, Yang**; Jinritoutiao Post Office, China Satellite Communications Tower, No. 63, Zhichun Road, Haidian District, Beijing 100080 (CN). **ZHANG, Kai**; 12655 West Jefferson Boulevard, Sixth Floor, Suite No. 137, Los Angeles, California 90066 (US). **HE, Yuwen**; 12655 West Jefferson Boulevard, Sixth Floor, Suite No. 137, Los Angeles, California 90066 (US). **LIU, Hongbin**;

Jinritoutiao Post Office, China Satellite Communications Tower, No. 63, Zhichun Road, Haidian District, Beijing 100080 (CN). **ZHANG, Li**; 12655 West Jefferson Boulevard, Sixth Floor, Suite No. 137, Los Angeles, California 90066 (US).

(74) Agent: **SHIHUI PARTNERS**; 42/F, Tower C, Beijing Yintai Centre, No. 2 Jianguomenwai Avenue, Chaoyang District, Beijing 100022 (CN).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CV, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IQ, IR, IS, IT, JM, JO, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, MG, MK, MN, MU, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, WS, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, CV, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SC, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ,

(54) Title: METHOD, APPARATUS, AND MEDIUM FOR VIDEO PROCESSING

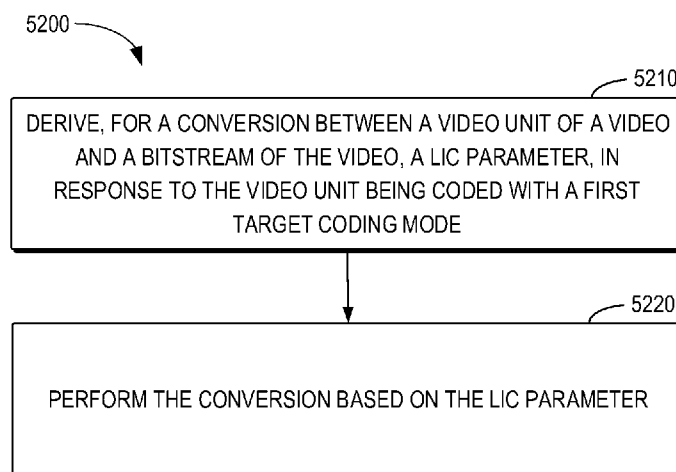


Fig. 52

(57) Abstract: Embodiments of the present disclosure provide a solution for video processing. A method for video processing is proposed. The method comprises: deriving, for a conversion between a video unit of a video and a bitstream of the video, a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to the video unit being coded with a first target coding mode; and performing the conversion based on the LIC parameter.

DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT,
LU, LV, MC, ME, MK, MT, NL, NO, PL, PT, RO, RS, SE,
SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN,
GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Published:

— *with international search report (Art. 21(3))*

METHOD, APPARATUS, AND MEDIUM FOR VIDEO PROCESSING

FIELDS

[0001] Embodiments of the present disclosure relates generally to video processing techniques, and more particularly, to local illumination compensation with multiple templates.

BACKGROUND

[0002] In nowadays, digital video capabilities are being applied in various aspects of peoples' lives. Multiple types of video compression technologies, such as MPEG-2, MPEG-4, ITU-TH.263, ITU-TH.264/MPEG-4 Part 10 Advanced Video Coding (AVC), ITU-TH.265 high efficiency video coding (HEVC) standard, versatile video coding (VVC) standard, have been proposed for video encoding/decoding. However, the coding performance of video coding techniques is generally expected to be further improved.

SUMMARY

[0003] Embodiments of the present disclosure provide a solution for video processing.

[0004] In a first aspect, a method for video processing is proposed. The method comprises: deriving, for a conversion between a video unit of a video and a bitstream of the video, a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to the video unit being coded with a first target coding mode; and performing the conversion based on the LIC parameter. Compared with the conventional solution, the method in accordance with the first aspect of the present disclosure can improve the coding performance of LIC by increasing the diversity of derived parameters.

[0005] In a second aspect, another method for video processing is proposed. The method comprises: applying, for a conversion between a video unit of a video and a bitstream of the video, a plurality of local illumination compensation (LIC) models to the video unit,

in response to the video unit being coded with a second target coding mode; and performing the conversion based on the plurality of LIC models. Compared with the conventional solution, the method in accordance with the second aspect of the present disclosure can improve the coding performance of LIC by applying a plurality of LIC models.

[0006] In a third aspect, an apparatus for video processing is proposed. The apparatus comprises a processor and a non-transitory memory with instructions thereon. The instructions upon execution by the processor, cause the processor to perform a method in accordance with the first, or second aspect of the present disclosure.

[0007] In a fourth aspect, a non-transitory computer-readable storage medium is proposed. The non-transitory computer-readable storage medium stores instructions that cause a processor to perform a method in accordance with the first, or second aspect of the present disclosure.

[0008] In a fifth aspect, another non-transitory computer-readable recording medium is proposed. The non-transitory computer-readable recording medium stores a bitstream of a video which is generated by a method performed by an apparatus for video processing. The method comprises: deriving a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to a video unit of the video being coded with a first target coding mode; and generating the bitstream based on the LIC parameter.

[0009] In a sixth aspect, another non-transitory computer-readable recording medium is proposed. The non-transitory computer-readable recording medium stores a bitstream of a video which is generated by a method performed by an apparatus for video processing. The method comprises: applying a plurality of local illumination compensation (LIC) models to a video unit of the video, in response to the video unit of the video being coded with a second target coding mode; and generating the bitstream based on the plurality of

LIC models.

[0010] In a seventh aspect, a method for storing a bitstream of a video is proposed. The method comprises: deriving a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to a video unit of the video being coded with a first target coding mode; generating the bitstream based on the LIC parameter; and storing the bitstream in a non-transitory computer-readable recording medium.

[0011] In an eighth aspect, a method for storing a bitstream of a video is proposed. The method comprises: applying a plurality of local illumination compensation (LIC) models to a video unit of the video, in response to the video unit of the video being coded with a second target coding mode; generating the bitstream based on the plurality of LIC models; and storing the bitstream in a non-transitory computer-readable recording medium.

[0012] This Summary is provided to introduce a selection of concepts in a simplified form that are further described below in the Detailed Description. This Summary is not intended to identify key features or essential features of the claimed subject matter, nor is it intended to be used to limit the scope of the claimed subject matter.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] Through the following detailed description with reference to the accompanying drawings, the above and other objectives, features, and advantages of example embodiments of the present disclosure will become more apparent. In the example embodiments of the present disclosure, the same reference numerals usually refer to the same components.

[0014] Fig. 1 illustrates a block diagram that illustrates an example video coding system, in accordance with some embodiments of the present disclosure;

[0015] Fig. 2 illustrates a block diagram that illustrates a first example video encoder, in

accordance with some embodiments of the present disclosure;

[0016] Fig. 3 illustrates a block diagram that illustrates an example video decoder, in accordance with some embodiments of the present disclosure;

[0017] Fig. 4 illustrates an example of encoder block diagram;

[0018] Fig. 5 illustrates 67 intra prediction modes;

[0019] Fig. 6A and Fig. 6B illustrate reference samples for wide-angular intra prediction;

[0020] Fig. 7 illustrates problem of discontinuity in case of directions beyond 45° ;

[0021] Fig. 8A and Fig. 8B illustrate MMVD Search Point;

[0022] Fig. 9 illustrates Illustration for symmetrical MVD mode;

[0023] Fig. 10 illustrates extended CU region used in BDOF;

[0024] Fig. 11 illustrates top and left neighboring blocks used in CIIP weight derivation;

[0025] Fig. 12 illustrates control point based affine motion model;

[0026] Fig. 13 illustrates Affine MVF per subblock;

[0027] Fig. 14 illustrates locations of inherited affine motion predictors;

[0028] Fig. 15 illustrates control point motion vector inheritance;

[0029] Fig. 16 illustrates locations of candidates position for constructed affine merge mode;

[0030] Fig. 17 illustrates illustration of motion vector usage for proposed combined method;

[0031] Fig. 18 illustrates Subblock MV VSB and pixel $\Delta v(i,j)$ (arrow);

[0032] Fig. 19A and Fig. 19B illustrate the SbTMVP process in VVC;

[0033] Fig. 20 illustrates local illumination compensation;

- [0034] Fig. 21 illustrates no subsampling for the short side;
- [0035] Fig. 22 illustrates decoding side motion vector refinement;
- [0036] Fig. 23 illustrates diamond regions in the search area;
- [0037] Fig. 24 illustrates positions of spatial merge candidate;
- [0038] Fig. 25 illustrates candidate pairs considered for redundancy check of spatial merge candidates;
- [0039] Fig. 26 illustrates illustration of motion vector scaling for temporal merge candidate;
- [0040] Fig. 27 illustrates candidate positions for temporal merge candidate, C0 and C1;
- [0041] Fig. 28 illustrates VVC spatial neighboring blocks of the current block;
- [0042] Fig. 29 illustrates illustration of virtual block in the i-th search round;
- [0043] Fig. 30 illustrates examples of the GPM splits grouped by identical angles;
- [0044] Fig. 31 illustrates uni-prediction MV selection for geometric partitioning mode;
- [0045] Fig. 32 illustrates exemplified generation of a bending weight w_0 using geometric partitioning mode;
- [0046] Fig. 33 illustrates spatial neighboring blocks used to derive the spatial merge candidates;
- [0047] Fig. 34 illustrates template matching performs on a search area around initial MV;
- [0048] Fig. 35 illustrates illustration of sub-blocks where OBMC applies;
- [0049] Fig. 36 illustrates SBT position, type and transform type;
- [0050] Fig. 37 illustrates neighbouring samples used for calculating SAD;
- [0051] Fig. 38 illustrates neighbouring samples used for calculating SAD for sub-CU level motion information;

- [0052] Fig. 39 illustrates the sorting process;
- [0053] Fig. 40 illustrates reorder process in encoder;
- [0054] Fig. 41 illustrates reorder process in decoder;
- [0055] Fig. 42 illustrates IBC reference region depending on current CU position;
- [0056] Fig. 43 illustrates examples of symmetry in screen content pictures;
- [0057] Fig. 44A illustrates an illustration of BV adjustment for horizontal flip;
- [0058] Fig. 44B illustrates an illustration of BV adjustment for vertical flip;
- [0059] Fig. 45 illustrates an intra template matching search area used;
- [0060] Fig. 46A- Fig. 46E illustrate template used to derive the parameters of the filter, respectively;
- [0061] Fig. 47A- Fig. 47F illustrate combined template used to derive the parameters of the filter, respectively;
- [0062] Fig. 48 illustrates an L-shape template used to derive the parameters of the filter;
- [0063] Fig. 49 illustrates a non-adjacent template used to derive the parameters of the filter;
- [0064] Fig. 50 illustrates samples of a specific line used to derive LIC parameters;
- [0065] Fig. 51 illustrates samples of a specific line used to derive LIC parameters including left-above samples;
- [0066] Fig. 52 illustrates a flowchart of a method for video processing in accordance with embodiments of the present disclosure;
- [0067] Fig. 53 illustrates a flowchart of a method for video processing in accordance with embodiments of the present disclosure;
- [0068] Fig. 54 illustrates a block diagram of a computing device in which various

embodiments of the present disclosure can be implemented.

[0069] Throughout the drawings, the same or similar reference numerals usually refer to the same or similar elements.

DETAILED DESCRIPTION

[0070] Principle of the present disclosure will now be described with reference to some embodiments. It is to be understood that these embodiments are described only for the purpose of illustration and help those skilled in the art to understand and implement the present disclosure, without suggesting any limitation as to the scope of the disclosure. The disclosure described herein can be implemented in various manners other than the ones described below.

[0071] In the following description and claims, unless defined otherwise, all technical and scientific terms used herein have the same meaning as commonly understood by one of ordinary skills in the art to which this disclosure belongs.

[0072] References in the present disclosure to “one embodiment,” “an embodiment,” “an example embodiment,” and the like indicate that the embodiment described may include a particular feature, structure, or characteristic, but it is not necessary that every embodiment includes the particular feature, structure, or characteristic. Moreover, such phrases are not necessarily referring to the same embodiment. Further, when a particular feature, structure, or characteristic is described in connection with an example embodiment, it is submitted that it is within the knowledge of one skilled in the art to affect such feature, structure, or characteristic in connection with other embodiments whether or not explicitly described.

[0073] It shall be understood that although the terms “first” and “second” etc. may be used herein to describe various elements, these elements should not be limited by these terms. These terms are only used to distinguish one element from another. For example, a first element could be termed a second element, and similarly, a second element could be

termed a first element, without departing from the scope of example embodiments. As used herein, the term “and/or” includes any and all combinations of one or more of the listed terms.

[0074] The terminology used herein is for the purpose of describing particular embodiments only and is not intended to be limiting of example embodiments. As used herein, the singular forms “a”, “an” and “the” are intended to include the plural forms as well, unless the context clearly indicates otherwise. It will be further understood that the terms “comprises”, “comprising”, “has”, “having”, “includes” and/or “including”, when used herein, specify the presence of stated features, elements, and/or components etc., but do not preclude the presence or addition of one or more other features, elements, components and/ or combinations thereof.

Example Environment

[0075] Fig. 1 is a block diagram that illustrates an example video coding system 100 that may utilize the techniques of this disclosure. As shown, the video coding system 100 may include a source device 110 and a destination device 120. The source device 110 can be also referred to as a video encoding device, and the destination device 120 can be also referred to as a video decoding device. In operation, the source device 110 can be configured to generate encoded video data and the destination device 120 can be configured to decode the encoded video data generated by the source device 110. The source device 110 may include a video source 112, a video encoder 114, and an input/output (I/O) interface 116.

[0076] The video source 112 may include a source such as a video capture device. Examples of the video capture device include, but are not limited to, an interface to receive video data from a video content provider, a computer graphics system for generating video data, and/or a combination thereof.

[0077] The video data may comprise one or more pictures. The video encoder 114

encodes the video data from the video source 112 to generate a bitstream. The bitstream may include a sequence of bits that form a coded representation of the video data. The bitstream may include coded pictures and associated data. The coded picture is a coded representation of a picture. The associated data may include sequence parameter sets, picture parameter sets, and other syntax structures. The I/O interface 116 may include a modulator/demodulator and/or a transmitter. The encoded video data may be transmitted directly to destination device 120 via the I/O interface 116 through the network 130A. The encoded video data may also be stored onto a storage medium/server 130B for access by destination device 120.

[0078] The destination device 120 may include an I/O interface 126, a video decoder 124, and a display device 122. The I/O interface 126 may include a receiver and/or a modem. The I/O interface 126 may acquire encoded video data from the source device 110 or the storage medium/server 130B. The video decoder 124 may decode the encoded video data. The display device 122 may display the decoded video data to a user. The display device 122 may be integrated with the destination device 120, or may be external to the destination device 120 which is configured to interface with an external display device.

[0079] The video encoder 114 and the video decoder 124 may operate according to a video compression standard, such as the High Efficiency Video Coding (HEVC) standard, Versatile Video Coding (VVC) standard and other current and/or further standards.

[0080] Fig. 2 is a block diagram illustrating an example of a video encoder 200, which may be an example of the video encoder 114 in the system 100 illustrated in Fig. 1, in accordance with some embodiments of the present disclosure.

[0081] The video encoder 200 may be configured to implement any or all of the techniques of this disclosure. In the example of Fig. 2, the video encoder 200 includes a plurality of functional components. The techniques described in this disclosure may be shared among the various components of the video encoder 200. In some examples, a processor may be configured to perform any or all of the techniques described in this

disclosure.

[0082] In some embodiments, the video encoder 200 may include a partition unit 201, a predication unit 202 which may include a mode select unit 203, a motion estimation unit 204, a motion compensation unit 205 and an intra-prediction unit 206, a residual generation unit 207, a transform unit 208, a quantization unit 209, an inverse quantization unit 210, an inverse transform unit 211, a reconstruction unit 212, a buffer 213, and an entropy encoding unit 214.

[0083] In other examples, the video encoder 200 may include more, fewer, or different functional components. In an example, the predication unit 202 may include an intra block copy (IBC) unit. The IBC unit may perform predication in an IBC mode in which at least one reference picture is a picture where the current video block is located.

[0084] Furthermore, although some components, such as the motion estimation unit 204 and the motion compensation unit 205, may be integrated, but are represented in the example of Fig. 2 separately for purposes of explanation.

[0085] The partition unit 201 may partition a picture into one or more video blocks. The video encoder 200 and the video decoder 300 may support various video block sizes.

[0086] The mode select unit 203 may select one of the coding modes, intra or inter, e.g., based on error results, and provide the resulting intra-coded or inter-coded block to a residual generation unit 207 to generate residual block data and to a reconstruction unit 212 to reconstruct the encoded block for use as a reference picture. In some examples, the mode select unit 203 may select a combination of intra and inter predication (CIIP) mode in which the predication is based on an inter predication signal and an intra predication signal. The mode select unit 203 may also select a resolution for a motion vector (e.g., a sub-pixel or integer pixel precision) for the block in the case of inter-predication.

[0087] To perform inter prediction on a current video block, the motion estimation unit 204 may generate motion information for the current video block by comparing one or

more reference frames from buffer 213 to the current video block. The motion compensation unit 205 may determine a predicted video block for the current video block based on the motion information and decoded samples of pictures from the buffer 213 other than the picture associated with the current video block.

[0088] The motion estimation unit 204 and the motion compensation unit 205 may perform different operations for a current video block, for example, depending on whether the current video block is in an I-slice, a P-slice, or a B-slice. As used herein, an “I-slice” may refer to a portion of a picture composed of macroblocks, all of which are based upon macroblocks within the same picture. Further, as used herein, in some aspects, “P-slices” and “B-slices” may refer to portions of a picture composed of macroblocks that are not dependent on macroblocks in the same picture.

[0089] In some examples, the motion estimation unit 204 may perform uni-directional prediction for the current video block, and the motion estimation unit 204 may search reference pictures of list 0 or list 1 for a reference video block for the current video block. The motion estimation unit 204 may then generate a reference index that indicates the reference picture in list 0 or list 1 that contains the reference video block and a motion vector that indicates a spatial displacement between the current video block and the reference video block. The motion estimation unit 204 may output the reference index, a prediction direction indicator, and the motion vector as the motion information of the current video block. The motion compensation unit 205 may generate the predicted video block of the current video block based on the reference video block indicated by the motion information of the current video block.

[0090] Alternatively, in other examples, the motion estimation unit 204 may perform bi-directional prediction for the current video block. The motion estimation unit 204 may search the reference pictures in list 0 for a reference video block for the current video block and may also search the reference pictures in list 1 for another reference video block for the current video block. The motion estimation unit 204 may then generate reference

indexes that indicate the reference pictures in list 0 and list 1 containing the reference video blocks and motion vectors that indicate spatial displacements between the reference video blocks and the current video block. The motion estimation unit 204 may output the reference indexes and the motion vectors of the current video block as the motion information of the current video block. The motion compensation unit 205 may generate the predicted video block of the current video block based on the reference video blocks indicated by the motion information of the current video block.

[0091] In some examples, the motion estimation unit 204 may output a full set of motion information for decoding processing of a decoder. Alternatively, in some embodiments, the motion estimation unit 204 may signal the motion information of the current video block with reference to the motion information of another video block. For example, the motion estimation unit 204 may determine that the motion information of the current video block is sufficiently similar to the motion information of a neighboring video block.

[0092] In one example, the motion estimation unit 204 may indicate, in a syntax structure associated with the current video block, a value that indicates to the video decoder 300 that the current video block has the same motion information as the another video block.

[0093] In another example, the motion estimation unit 204 may identify, in a syntax structure associated with the current video block, another video block and a motion vector difference (MVD). The motion vector difference indicates a difference between the motion vector of the current video block and the motion vector of the indicated video block. The video decoder 300 may use the motion vector of the indicated video block and the motion vector difference to determine the motion vector of the current video block.

[0094] As discussed above, video encoder 200 may predictively signal the motion vector. Two examples of predictive signaling techniques that may be implemented by video encoder 200 include advanced motion vector predication (AMVP) and merge mode signaling.

[0095] The intra prediction unit 206 may perform intra prediction on the current video

block. When the intra prediction unit 206 performs intra prediction on the current video block, the intra prediction unit 206 may generate prediction data for the current video block based on decoded samples of other video blocks in the same picture. The prediction data for the current video block may include a predicted video block and various syntax elements.

[0096] The residual generation unit 207 may generate residual data for the current video block by subtracting (e.g., indicated by the minus sign) the predicted video block (s) of the current video block from the current video block. The residual data of the current video block may include residual video blocks that correspond to different sample components of the samples in the current video block.

[0097] In other examples, there may be no residual data for the current video block for the current video block, for example in a skip mode, and the residual generation unit 207 may not perform the subtracting operation.

[0098] The transform processing unit 208 may generate one or more transform coefficient video blocks for the current video block by applying one or more transforms to a residual video block associated with the current video block.

[0099] After the transform processing unit 208 generates a transform coefficient video block associated with the current video block, the quantization unit 209 may quantize the transform coefficient video block associated with the current video block based on one or more quantization parameter (QP) values associated with the current video block.

[0100] The inverse quantization unit 210 and the inverse transform unit 211 may apply inverse quantization and inverse transforms to the transform coefficient video block, respectively, to reconstruct a residual video block from the transform coefficient video block. The reconstruction unit 212 may add the reconstructed residual video block to corresponding samples from one or more predicted video blocks generated by the predication unit 202 to produce a reconstructed video block associated with the current video block for storage in the buffer 213.

[0101] After the reconstruction unit 212 reconstructs the video block, loop filtering operation may be performed to reduce video blocking artifacts in the video block.

[0102] The entropy encoding unit 214 may receive data from other functional components of the video encoder 200. When the entropy encoding unit 214 receives the data, the entropy encoding unit 214 may perform one or more entropy encoding operations to generate entropy encoded data and output a bitstream that includes the entropy encoded data.

[0103] Fig. 3 is a block diagram illustrating an example of a video decoder 300, which may be an example of the video decoder 124 in the system 100 illustrated in Fig. 1, in accordance with some embodiments of the present disclosure.

[0104] The video decoder 300 may be configured to perform any or all of the techniques of this disclosure. In the example of Fig. 3, the video decoder 300 includes a plurality of functional components. The techniques described in this disclosure may be shared among the various components of the video decoder 300. In some examples, a processor may be configured to perform any or all of the techniques described in this disclosure.

[0105] In the example of Fig. 3, the video decoder 300 includes an entropy decoding unit 301, a motion compensation unit 302, an intra prediction unit 303, an inverse quantization unit 304, an inverse transformation unit 305, and a reconstruction unit 306 and a buffer 307. The video decoder 300 may, in some examples, perform a decoding pass generally reciprocal to the encoding pass described with respect to video encoder 200.

[0106] The entropy decoding unit 301 may retrieve an encoded bitstream. The encoded bitstream may include entropy coded video data (e.g., encoded blocks of video data). The entropy decoding unit 301 may decode the entropy coded video data, and from the entropy decoded video data, the motion compensation unit 302 may determine motion information including motion vectors, motion vector precision, reference picture list indexes, and other motion information. The motion compensation unit 302 may, for example, determine such information by performing the AMVP and merge mode. AMVP is used, including

derivation of several most probable candidates based on data from adjacent PBs and the reference picture. Motion information typically includes the horizontal and vertical motion vector displacement values, one or two reference picture indices, and, in the case of prediction regions in B slices, an identification of which reference picture list is associated with each index. As used herein, in some aspects, a “merge mode” may refer to deriving the motion information from spatially or temporally neighboring blocks.

[0107] The motion compensation unit 302 may produce motion compensated blocks, possibly performing interpolation based on interpolation filters. Identifiers for interpolation filters to be used with sub-pixel precision may be included in the syntax elements.

[0108] The motion compensation unit 302 may use the interpolation filters as used by the video encoder 200 during encoding of the video block to calculate interpolated values for sub-integer pixels of a reference block. The motion compensation unit 302 may determine the interpolation filters used by the video encoder 200 according to the received syntax information and use the interpolation filters to produce predictive blocks.

[0109] The motion compensation unit 302 may use at least part of the syntax information to determine sizes of blocks used to encode frame(s) and/or slice(s) of the encoded video sequence, partition information that describes how each macroblock of a picture of the encoded video sequence is partitioned, modes indicating how each partition is encoded, one or more reference frames (and reference frame lists) for each inter-encoded block, and other information to decode the encoded video sequence. As used herein, in some aspects, a “slice” may refer to a data structure that can be decoded independently from other slices of the same picture, in terms of entropy coding, signal prediction, and residual signal reconstruction. A slice can either be an entire picture or a region of a picture.

[0110] The intra prediction unit 303 may use intra prediction modes for example received in the bitstream to form a prediction block from spatially adjacent blocks. The inverse quantization unit 304 inverse quantizes, i.e., de-quantizes, the quantized video block

coefficients provided in the bitstream and decoded by entropy decoding unit 301. The inverse transform unit 305 applies an inverse transform.

[0111] The reconstruction unit 306 may obtain the decoded blocks, e.g., by summing the residual blocks with the corresponding prediction blocks generated by the motion compensation unit 302 or intra-prediction unit 303. If desired, a deblocking filter may also be applied to filter the decoded blocks in order to remove blockiness artifacts. The decoded video blocks are then stored in the buffer 307, which provides reference blocks for subsequent motion compensation/intra prediction and also produces decoded video for presentation on a display device.

[0112] Some exemplary embodiments of the present disclosure will be described in detailed hereinafter. It should be understood that section headings are used in the present document to facilitate ease of understanding and do not limit the embodiments disclosed in a section to only that section. Furthermore, while certain embodiments are described with reference to Versatile Video Coding or other specific video codecs, the disclosed techniques are applicable to other video coding technologies also. Furthermore, while some embodiments describe video coding steps in detail, it will be understood that corresponding steps decoding that undo the coding will be implemented by a decoder. Furthermore, the term video processing encompasses video coding or compression, video decoding or decompression and video transcoding in which video pixels are represented from one compressed format into another compressed format or at a different compressed bitrate.

1. Brief Summary

[0113] This disclosure is related to video coding technologies. Specifically, it is related to local illumination compensation (LIC), how to improve LIC with multiple templates or multiple models, and other coding tools in image/video coding. It may be applied to the existing video coding standard like HEVC, or Versatile Video Coding (VVC). It may be also applicable to future video coding standards or video codec.

2. Introduction

[0114] Video coding standards have evolved primarily through the development of the well-known ITU-T and ISO/IEC standards. The ITU-T produced H.261 and H.263, ISO/IEC produced MPEG-1 and MPEG-4 Visual, and the two organizations jointly produced the H.262/MPEG-2 Video and H.264/MPEG-4 Advanced Video Coding (AVC) and H.265/HEVC standards. Since H.262, the video coding standards are based on the hybrid video coding structure wherein temporal prediction plus transform coding are utilized. To explore the future video coding technologies beyond HEVC, Joint Video Exploration Team (JVET) was founded by VCEG and MPEG jointly in 2015. Since then, many new methods have been adopted by JVET and put into the reference software named Joint Exploration Model (JEM). In April 2018, the Joint Video Expert Team (JVET) between VCEG (Q6/16) and ISO/IEC JTC1 SC29/WG11 (MPEG) was created to work on the VVC standard targeting at 50% bitrate reduction compared to HEVC.

2.1. Coding flow of a typical video codec

[0115] Fig. 4 shows an example of encoder block diagram of VVC, which contains three in-loop filtering blocks: deblocking filter (DF), sample adaptive offset (SAO) and ALF. Unlike DF, which uses predefined filters, SAO and ALF utilize the original samples of the current picture to reduce the mean square errors between the original samples and the reconstructed samples by adding an offset and by applying a finite impulse response (FIR) filter, respectively, with coded side information signalling the offsets and filter coefficients. ALF is located at the last processing stage of each picture and can be regarded as a tool trying to catch and fix artifacts created by the previous stages.

2.2. Intra mode coding with 67 intra prediction modes

[0116] To capture the arbitrary edge directions presented in natural video, the number of directional intra modes is extended from 33, as used in HEVC, to 65, as shown in Fig. 5, and the planar and DC modes remain the same. These denser directional intra prediction modes apply for all block sizes and for both luma and chroma intra predictions.

[0117] In the HEVC, every intra-coded block has a square shape and the length of each of its side is a power of 2. Thus, no division operations are required to generate an intra-predictor using DC mode. In VVC, blocks can have a rectangular shape that necessitates the use of a division operation per block in the general case. To avoid division operations for DC prediction, only the longer side is used to compute the average for non-square blocks.

2.2.1. Wide angle intra prediction

[0118] Although 67 modes are defined in the VVC, the exact prediction direction for a given intra prediction mode index is further dependent on the block shape. Conventional angular intra prediction directions are defined from 45 degrees to -135 degrees in clockwise direction. In VVC, several conventional angular intra prediction modes are adaptively replaced with wide-angle intra prediction modes for non-square blocks. The replaced modes are signalled using the original mode indexes, which are remapped to the indexes of wide angular modes after parsing. The total number of intra prediction modes is unchanged, i.e., 67, and the intra mode coding method is unchanged.

[0119] To support these prediction directions, the top reference with length $2W+1$, and the left reference with length $2H+1$, are defined as shown in Fig. 6A and Fig. 6B, which illustrate reference samples for wide-angular intra prediction .

[0120] The number of replaced modes in wide-angular direction mode depends on the aspect ratio of a block. The replaced intra prediction modes are illustrated in Table 1.

Table 1 – Intra prediction modes replaced by wide-angular modes

Aspect ratio	Replaced intra prediction modes
$W / H == 16$	Modes 2,3,4,5,6,7,8,9,10,11,12,13,14,15
$W / H == 8$	Modes 2,3,4,5,6,7,8,9,10,11,12, 13
$W / H == 4$	Modes 2,3,4,5,6,7,8,9,10,11
$W / H == 2$	Modes 2,3,4,5,6,7,8,9
$W / H == 1$	None
$W / H == 1/2$	Modes 59,60,61,62,63,64,65,66
$W / H == 1/4$	Mode 57,58,59,60,61,62,63,64,65,66
$W / H == 1/8$	Modes 55, 56,57,58,59,60,61,62,63,64,65,66
$W / H == 1/16$	Modes 53, 54, 55, 56,57,58,59,60,61,62,63,64,65,66

[0121] Fig. 7 illustrates problem of discontinuity in case of directions beyond 45° . As shown in Fig. 7, two vertically adjacent predicted samples may use two non-adjacent reference samples in the case of wide-angle intra prediction. Hence, low-pass reference samples filter and side smoothing are applied to the wide-angle prediction to reduce the negative effect of the increased gap Δp_a . If a wide-angle mode represents a non-fractional offset. There are 8 modes in the wide-angle modes satisfy this condition, which are $[-14, -12, -10, -6, 72, 76, 78, 80]$. When a block is predicted by these modes, the samples in the reference buffer are directly copied without applying any interpolation. With this modification, the number of samples needed to be smoothing is reduced. Besides, it aligns the design of non-fractional modes in the conventional prediction modes and wide-angle modes.

[0122] In VVC, 4:2:2 and 4:4:4 chroma formats are supported as well as 4:2:0. Chroma

derived mode (DM) derivation table for 4:2:2 chroma format was initially ported from HEVC extending the number of entries from 35 to 67 to align with the extension of intra prediction modes. Since HEVC specification does not support prediction angle below -135 degree and above 45 degree, luma intra prediction modes ranging from 2 to 5 are mapped to 2. Therefore, chroma DM derivation table for 4:2:2 chroma format is updated by replacing some values of the entries of the mapping table to convert prediction angle more precisely for chroma blocks.

2.3. Inter prediction

[0123] For each inter-predicted CU, motion parameters consisting of motion vectors, reference picture indices and reference picture list usage index, and additional information needed for the new coding feature of VVC to be used for inter-predicted sample generation. The motion parameter can be signalled in an explicit or implicit manner. When a CU is coded with skip mode, the CU is associated with one PU and has no significant residual coefficients, no coded motion vector delta or reference picture index. A merge mode is specified whereby the motion parameters for the current CU are obtained from neighbouring CUs, including spatial and temporal candidates, and additional schedules introduced in VVC. The merge mode can be applied to any inter-predicted CU, not only for skip mode. The alternative to merge mode is the explicit transmission of motion parameters, where motion vector, corresponding reference picture index for each reference picture list and reference picture list usage flag and other needed information are signalled explicitly per each CU.

2.4. Intra block copy (IBC)

[0124] Intra block copy (IBC) is a tool adopted in HEVC extensions on SCC. It is well known that it significantly improves the coding efficiency of screen content materials. Since IBC mode is implemented as a block level coding mode, block matching (BM) is performed at the encoder to find the optimal block vector (or motion vector) for each CU. Here, a block vector is used to indicate the displacement from the current block to a

reference block, which is already reconstructed inside the current picture. The luma block vector of an IBC-coded CU is in integer precision. The chroma block vector rounds to integer precision as well. When combined with AMVR, the IBC mode can switch between 1-pel and 4-pel motion vector precisions. An IBC-coded CU is treated as the third prediction mode other than intra or inter prediction modes. The IBC mode is applicable to the CUs with both width and height smaller than or equal to 64 luma samples.

[0125] At the encoder side, hash-based motion estimation is performed for IBC. The encoder performs RD check for blocks with either width or height no larger than 16 luma samples. For non-merge mode, the block vector search is performed using hash-based search first. If hash search does not return valid candidate, block matching based local search will be performed.

[0126] In the hash-based search, hash key matching (32-bit CRC) between the current block and a reference block is extended to all allowed block sizes. The hash key calculation for every position in the current picture is based on 4×4 sub-blocks. For the current block of a larger size, a hash key is determined to match that of the reference block when all the hash keys of all 4×4 sub-blocks match the hash keys in the corresponding reference locations. If hash keys of multiple reference blocks are found to match that of the current block, the block vector costs of each matched reference are calculated and the one with the minimum cost is selected.

[0127] In block matching search, the search range is set to cover both the previous and current CTUs.

[0128] At CU level, IBC mode is signalled with a flag and it can be signalled as IBC AMVP mode or IBC skip/merge mode as follows:

- IBC skip/merge mode: a merge candidate index is used to indicate which of the block vectors in the list from neighbouring candidate IBC coded blocks is used to predict the current block. The merge list consists of spatial, HMVP, and pairwise candidates.
- IBC AMVP mode: block vector difference is coded in the same way as a motion vector difference. The block vector prediction method uses two candidates as predictors, one from left

neighbour and one from above neighbour (if IBC coded). When either neighbour is not available, a default block vector will be used as a predictor. A flag is signalled to indicate the block vector predictor index.

2.5. Multi-model LM (MMLM)

[0129] CCLM included in VVC is extended by adding three Multi-model LM (MMLM) modes. In each MMLM mode, the reconstructed neighboring samples are classified into two classes using a threshold which is the average of the luma reconstructed neighboring samples. The linear model of each class is derived using the Least-Mean-Square (LMS) method. For the CCLM mode, the LMS method is also used to derive the linear model. A slope adjustment to is applied to cross-component linear model (CCLM) and to Multi-model LM prediction. The adjustment is tilting the linear function which maps luma values to chroma values with respect to a center point determined by the average luma value of the reference samples.

2.6. Merge mode with MVD (MMVD)

[0130] In addition to merge mode, where the implicitly derived motion information is directly used for prediction samples generation of the current CU, the merge mode with motion vector differences (MMVD) is introduced in VVC. A MMVD flag is signalled right after sending a regular merge flag to specify whether MMVD mode is used for a CU.

[0131] In MMVD, after a merge candidate is selected, it is further refined by the signalled MVDs information. The further information includes a merge candidate flag, an index to specify motion magnitude, and an index for indication of motion direction. In MMVD mode, one for the first two candidates in the merge list is selected to be used as MV basis. The MMVD candidate flag is signalled to specify which one is used between the first and second merge candidates.

[0132] Distance index specifies motion magnitude information and indicate the pre-defined offset from the starting point. Fig. 8A and Fig. 8B illustrate MMVD search point. As shown in Fig. 8A and Fig. 8B, an offset is added to either horizontal component or

vertical component of starting MV. The relation of distance index and pre-defined offset is specified in Table 2.

Table 2 – The relation of distance index and pre-defined offset

Distance IDX	0	1	2	3	4	5	6	7
Offset (in unit of luma sample)	1/4	1/2	1	2	4	8	16	32

[0133] Direction index represents the direction of the MVD relative to the starting point. The direction index can represent of the four directions as shown in Table 3. It's noted that the meaning of MVD sign could be variant according to the information of starting MVs. When the starting MVs is an un-prediction MV or bi-prediction MVs with both lists point to the same side of the current picture (i.e. POCs of two references are both larger than the POC of the current picture, or are both smaller than the POC of the current picture), the sign in Table 3 specifies the sign of MV offset added to the starting MV. When the starting MVs is bi-prediction MVs with the two MVs point to the different sides of the current picture (i.e. the POC of one reference is larger than the POC of the current picture, and the POC of the other reference is smaller than the POC of the current picture), and the difference of POC in list 0 is greater than the one in list 1, the sign in Table 3 specifies the sign of MV offset added to the list0 MV component of starting MV and the sign for the list1 MV has opposite value. Otherwise, if the difference of POC in list 1 is greater than list 0, the sign in Table 3 specifies the sign of MV offset added to the list1 MV component of starting MV and the sign for the list0 MV has opposite value.

[0134] The MVD is scaled according to the difference of POCs in each direction. If the differences of POCs in both lists are the same, no scaling is needed. Otherwise, if the difference of POC in list 0 is larger than the one of list 1, the MVD for list 1 is scaled, by defining the POC difference of L0 as td and POC difference of L1 as tb , described in Fig. 26. If the POC difference of L1 is greater than L0, the MVD for list 0 is scaled in the same way. If the starting MV is uni-predicted, the MVD is added to the available MV.

Table 3 – Sign of MV offset specified by direction index

Direction IDX	00	01	10	11
x-axis	+	–	N/A	N/A
y-axis	N/A	N/A	+	–

2.7. Symmetric MVD coding

[0135] In VVC, besides the normal unidirectional prediction and bi-directional prediction mode MVD signalling, symmetric MVD mode for bi-predictional MVD signalling is applied. In the symmetric MVD mode, motion information including reference picture indices of both list-0 and list-1 and MVD of list-1 are not signaled but derived.

[0136] The decoding process of the symmetric MVD mode is as follows:

- At slice level, variables BiDirPredFlag, RefIdxSymL0 and RefIdxSymL1 are derived as follows:
 - If mvd_l1_zero_flag is 1, BiDirPredFlag is set equal to 0.
 - Otherwise, if the nearest reference picture in list-0 and the nearest reference picture in list-1 form a forward and backward pair of reference pictures or a backward and forward pair of reference pictures, BiDirPredFlag is set to 1, and both list-0 and list-1 reference pictures are short-term reference pictures. Otherwise BiDirPredFlag is set to 0.
- At CU level, a symmetrical mode flag indicating whether symmetrical mode is used or not is explicitly signaled if the CU is bi-prediction coded and BiDirPredFlag is equal to 1.

[0137] When the symmetrical mode flag is true, only mvp_l0_flag, mvp_l1_flag and MVD0 are explicitly signaled. The reference indices for list-0 and list-1 are set equal to the pair of reference pictures, respectively. MVD1 is set equal to (– MVD0). The final motion vectors are shown in below formula.

$$\begin{cases} (mvx_0, mvy_0) = (mvp_x_0 + mvd_x_0, mvp_y_0 + mvd_y_0) \\ (mvx_1, mvy_1) = (mvp_x_1 - mvd_x_0, mvp_y_1 - mvd_y_0) \end{cases} \quad (2-1)$$

[0138] Fig. 9 illustrates Illustration for symmetrical MVD mode.

[0139] In the encoder, symmetric MVD motion estimation starts with initial MV

evaluation. A set of initial MV candidates comprising of the MV obtained from uni-prediction search, the MV obtained from bi-prediction search and the MVs from the AMVP list. The one with the lowest rate-distortion cost is chosen to be the initial MV for the symmetric MVD motion search.

2.8. Bi-directional optical flow (BDOF)

[0140] The bi-directional optical flow (BDOF) tool is included in VVC. BDOF, previously referred to as BIO, was included in the JEM. Compared to the JEM version, the BDOF in VVC is a simpler version that requires much less computation, especially in terms of number of multiplications and the size of the multiplier.

[0141] BDOF is used to refine the bi-prediction signal of a CU at the 4×4 subblock level. BDOF is applied to a CU if it satisfies all the following conditions:

- The CU is coded using “true” bi-prediction mode, i.e., one of the two reference pictures is prior to the current picture in display order and the other is after the current picture in display order.
- The distances (i.e. POC difference) from two reference pictures to the current picture are same.
- Both reference pictures are short-term reference pictures.
- The CU is not coded using affine mode or the SbTMVP merge mode.
- CU has more than 64 luma samples.
- Both CU height and CU width are larger than or equal to 8 luma samples.
- BCW weight index indicates equal weight.
- WP is not enabled for the current CU.
- CIIP mode is not used for the current CU.

[0142] BDOF is only applied to the luma component. As its name indicates, the BDOF mode is based on the optical flow concept, which assumes that the motion of an object is smooth. For each 4×4 subblock, a motion refinement (v_x, v_y) is calculated by minimizing the difference between the L0 and L1 prediction samples. The motion refinement is then used to adjust the bi-predicted sample values in the 4×4 subblock. The following steps are applied in the BDOF process.

[0143] First, the horizontal and vertical gradients, $\frac{\partial I^{(k)}}{\partial x}(i, j)$ and $\frac{\partial I^{(k)}}{\partial y}(i, j)$, $k = 0, 1$, of the two prediction signals are computed by directly calculating the difference between two neighboring samples, i.e.,

$$\begin{aligned}\frac{\partial I^{(k)}}{\partial x}(i, j) &= ((I^{(k)}(i+1, j) \gg \text{shift1}) - (I^{(k)}(i-1, j) \gg \text{shift1})) \\ \frac{\partial I^{(k)}}{\partial y}(i, j) &= ((I^{(k)}(i, j+1) \gg \text{shift1}) - (I^{(k)}(i, j-1) \gg \text{shift1}))\end{aligned}\quad (2-2)$$

where $I^{(k)}(i, j)$ are the sample value at coordinate (i, j) of the prediction signal in list k , $k = 0, 1$, and shift1 is calculated based on the luma bit depth, bitDepth, as $\text{shift1} = \max(6, \text{bitDepth}-6)$.

[0144] Then, the auto- and cross-correlation of the gradients, S_1 , S_2 , S_3 , S_5 and S_6 , are calculated as

$$\begin{aligned}S_1 &= \sum_{(i,j) \in \Omega} \text{Abs}(\psi_x(i, j)), \quad S_3 = \sum_{(i,j) \in \Omega} \theta(i, j) \cdot \text{Sign}(\psi_x(i, j)) \\ S_2 &= \sum_{(i,j) \in \Omega} \psi_x(i, j) \cdot \text{Sign}(\psi_y(i, j))\end{aligned}\quad (2-3)$$

$$S_5 = \sum_{(i,j) \in \Omega} \text{Abs}(\psi_y(i, j)), \quad S_6 = \sum_{(i,j) \in \Omega} \theta(i, j) \cdot \text{Sign}(\psi_y(i, j))$$

where

$$\begin{aligned}\psi_x(i, j) &= \left(\frac{\partial I^{(1)}}{\partial x}(i, j) + \frac{\partial I^{(0)}}{\partial x}(i, j) \right) \gg n_a \\ \psi_y(i, j) &= \left(\frac{\partial I^{(1)}}{\partial y}(i, j) + \frac{\partial I^{(0)}}{\partial y}(i, j) \right) \gg n_a\end{aligned}\quad (2-4)$$

$$\theta(i, j) = (I^{(1)}(i, j) \gg n_b) - (I^{(0)}(i, j) \gg n_b)$$

where Ω is a 6×6 window around the 4×4 subblock, and the values of n_a and n_b are set equal to $\min(1, \text{bitDepth} - 11)$ and $\min(4, \text{bitDepth} - 8)$, respectively.

[0145] The motion refinement (v_x, v_y) is then derived using the cross- and auto-

correlation terms using the following:

$$\begin{aligned}
 v_x &= S_1 > 0? \text{clip3} \left(-th'_{BIO}, th'_{BIO}, -((S_3 \cdot 2^{n_b - n_a}) \gg \lfloor \log_2 S_1 \rfloor) \right) : 0 \\
 v_y &= S_5 > 0? \text{clip3} \left(-th'_{BIO}, th'_{BIO}, - \left(\left(S_6 \cdot 2^{n_b - n_a} \right. \right. \right. \\
 &\quad \left. \left. \left. - \left((v_x S_{2,m}) \ll n_{S_2} + v_x S_{2,s} \right) / 2 \right) \gg \lfloor \log_2 S_5 \rfloor \right) \right) : 0
 \end{aligned} \tag{2-5}$$

where $S_{2,m} = S_2 \gg n_{S_2}$, $S_{2,s} = S_2 \& (2^{n_{S_2}} - 1)$, $th'_{BIO} = 2^{\max(5, BD-7)}$. $\lfloor \cdot \rfloor$ is the floor function, and $n_{S_2} = 12$.

[0146] Based on the motion refinement and the gradients, the following adjustment is calculated for each sample in the 4×4 subblock:

$$b(x, y) = \text{rnd} \left(\left(v_x \left(\frac{\partial I^{(1)}(x, y)}{\partial x} - \frac{\partial I^{(0)}(x, y)}{\partial x} \right) + v_y \left(\frac{\partial I^{(1)}(x, y)}{\partial y} - \frac{\partial I^{(0)}(x, y)}{\partial y} \right) + 1 \right) / 2 \right) \tag{2-6}$$

[0147] Finally, the BDOF samples of the CU are calculated by adjusting the bi-prediction samples as follows:

$$\text{pred}_{BDOF}(x, y) = (I^{(0)}(x, y) + I^{(1)}(x, y) + b(x, y) + o_{offset}) \gg \text{shift} \tag{2-7}$$

[0148] These values are selected such that the multipliers in the BDOF process do not exceed 15-bit, and the maximum bit-width of the intermediate parameters in the BDOF process is kept within 32-bit.

[0149] In order to derive the gradient values, some prediction samples $I^{(k)}(i, j)$ in list k ($k = 0, 1$) outside of the current CU boundaries need to be generated. Fig. 10 illustrates extended CU region used in BDOF. As depicted in Fig. 10, the BDOF in VVC uses one extended row/column around the CU's boundaries. In order to control the computational complexity of generating the out-of-boundary prediction samples, prediction samples in the extended area (white positions) are generated by taking the reference samples at the nearby integer positions (using floor() operation on the coordinates) directly without

interpolation, and the normal 8-tap motion compensation interpolation filter is used to generate prediction samples within the CU (gray positions). These extended sample values are used in gradient calculation only. For the remaining steps in the BDOF process, if any sample and gradient values outside of the CU boundaries are needed, they are padded (i.e. repeated) from their nearest neighbors.

[0150] When the width and/or height of a CU are larger than 16 luma samples, it will be split into subblocks with width and/or height equal to 16 luma samples, and the subblock boundaries are treated as the CU boundaries in the BDOF process. The maximum unit size for BDOF process is limited to 16x16. For each subblock, the BDOF process could be skipped. When the SAD of between the initial L0 and L1 prediction samples is smaller than a threshold, the BDOF process is not applied to the subblock. The threshold is set equal to $(8 * W * (H >> 1))$, where W indicates the subblock width, and H indicates subblock height. To avoid the additional complexity of SAD calculation, the SAD between the initial L0 and L1 prediction samples calculated in DVMR process is re-used here.

[0151] If BCW is enabled for the current block, i.e., the BCW weight index indicates unequal weight, then bi-directional optical flow is disabled. Similarly, if WP is enabled for the current block, i.e., the `luma_weight_lx_flag` is 1 for either of the two reference pictures, then BDOF is also disabled. When a CU is coded with symmetric MVD mode or CIIP mode, BDOF is also disabled.

2.9. Combined inter and intra prediction (CIIP)

[0152] In VVC, when a CU is coded in merge mode, if the CU contains at least 64 luma samples (that is, CU width times CU height is equal to or larger than 64), and if both CU width and CU height are less than 128 luma samples, an additional flag is signalled to indicate if the combined inter/intra prediction (CIIP) mode is applied to the current CU. As its name indicates, the CIIP prediction combines an inter prediction signal with an intra prediction signal. The inter prediction signal in the CIIP mode P_{inter} is derived using the same inter prediction process applied to regular merge mode; and the intra

prediction signal P_{intra} is derived following the regular intra prediction process with the planar mode. Then, the intra and inter prediction signals are combined using weighted averaging, where the weight value is calculated depending on the coding modes of the top and left neighbouring blocks (depicted in Fig. 11 which illustrates top and left neighboring blocks used in CIIP weight derivation) as follows:

- If the top neighbor is available and intra coded, then set $isIntraTop$ to 1, otherwise set $isIntraTop$ to 0;
- If the left neighbor is available and intra coded, then set $isIntraLeft$ to 1, otherwise set $isIntraLeft$ to 0;
- If $(isIntraLeft + isIntraTop)$ is equal to 2, then wt is set to 3;
- Otherwise, if $(isIntraLeft + isIntraTop)$ is equal to 1, then wt is set to 2;
- Otherwise, set wt to 1.

[0153] The CIIP prediction is formed as follows:

$$P_{CIIP} = ((4 - wt) * P_{inter} + wt * P_{intra} + 2) \gg 2 \quad (2-8)$$

2.10. Affine motion compensated prediction

[0154] In HEVC, only translation motion model is applied for motion compensation prediction (MCP). While in the real world, there are many kinds of motion, e.g., zoom in/out, rotation, perspective motions and the other irregular motions. In VVC, a block-based affine transform motion compensation prediction is applied. Fig. 12 illustrates control point based affine motion model, for example (a) for 4-parameter affine model, and (b) for 6-parameter affine model. As shown in Fig. 12, the affine motion field of the block is described by motion information of two control point (4-parameter) or three control point motion vectors (6-parameter).

[0155] For 4-parameter affine motion model, motion vector at sample location (x, y) in a block is derived as:

$$\begin{cases} mv_x = \frac{mv_{1x} - mv_{0x}}{W}x + \frac{mv_{1y} - mv_{0y}}{W}y + mv_{0x} \\ mv_y = \frac{mv_{1y} - mv_{0y}}{W}x + \frac{mv_{1x} - mv_{0x}}{W}y + mv_{0y} \end{cases} \quad (2-9)$$

[0156] For 6-parameter affine motion model, motion vector at sample location (x, y) in a

block is derived as:

$$\begin{cases} mv_x = \frac{mv_{1x}-mv_{0x}}{W}x + \frac{mv_{2x}-mv_{0x}}{H}y + mv_{0x} \\ mv_y = \frac{mv_{1y}-mv_{0y}}{W}x + \frac{mv_{2y}-mv_{0y}}{H}y + mv_{0y} \end{cases} \quad (2-10)$$

Where (mv_{0x}, mv_{0y}) is motion vector of the top-left corner control point, (mv_{1x}, mv_{1y}) is motion vector of the top-right corner control point, and (mv_{2x}, mv_{2y}) is motion vector of the bottom-left corner control point.

[0157] In order to simplify the motion compensation prediction, block based affine transform prediction is applied. To derive motion vector of each 4×4 luma subblock, the motion vector of the center sample of each subblock, as shown in Fig. 13 which illustrates Affine MVF per subblock, is calculated according to above equations, and rounded to 1/16 fraction accuracy. Then the motion compensation interpolation filters are applied to generate the prediction of each subblock with derived motion vector. The subblock size of chroma-components is also set to be 4×4 . The MV of a 4×4 chroma subblock is calculated as the average of the MVs of the four corresponding 4×4 luma subblocks.

[0158] As done for translational motion inter prediction, there are also two affine motion inter prediction modes: affine merge mode and affine AMVP mode.

2.10.1. Affine merge prediction

[0159] AF_MERGE mode can be applied for CUs with both width and height larger than or equal to 8. In this mode the CPMVs of the current CU is generated based on the motion information of the spatial neighbouring CUs. There can be up to five CPMVP candidates and an index is signalled to indicate the one to be used for the current CU. The following three types of CPVM candidate are used to form the affine merge candidate list:

- Inherited affine merge candidates that extrapolated from the CPMVs of the neighbour CUs.
- Constructed affine merge candidates CPMVPs that are derived using the translational MVs of the neighbour CUs.
- Zero MVs.

[0160] In VVC, there are maximum two inherited affine candidates, which are derived

from affine motion model of the neighbouring blocks, one from left neighbouring CUs and one from above neighbouring CUs. Fig. 14 illustrates locations of inherited affine motion predictors. The candidate blocks are shown in Fig. 14. For the left predictor, the scan order is A0->A1, and for the above predictor, the scan order is B0->B1->B2. Only the first inherited candidate from each side is selected. No pruning check is performed between two inherited candidates. When a neighbouring affine CU is identified, its control point motion vectors are used to derive the CPMVP candidate in the affine merge list of the current CU. As shown in , if the neighbour left bottom block A is coded in affine mode, the motion vectors v_2 , v_3 and v_4 of the top left corner, above right corner and left bottom corner of the CU which contains the block A are attained. When block A is coded with 4-parameter affine model, the two CPMVs of the current CU are calculated according to v_2 , and v_3 . In case that block A is coded with 6-parameter affine model, the three CPMVs of the current CU are calculated according to v_2 , v_3 and v_4 .

[0161] Fig. 15 illustrates control point motion vector inheritance.

[0162] Constructed affine candidate means the candidate is constructed by combining the neighbour translational motion information of each control point. Fig. 16 illustrates locations of candidates position for constructed affine merge mode. The motion information for the control points is derived from the specified spatial neighbours and temporal neighbour shown in Fig. 16. CPMV_k (k=1, 2, 3, 4) represents the k-th control point. For CPMV₁, the B2->B3->A2 blocks are checked and the MV of the first available block is used. For CPMV₂, the B1->B0 blocks are checked and for CPMV₃, the A1->A0 blocks are checked. For TMVP is used as CPMV₄ if it's available.

[0163] After MVs of four control points are attained, affine merge candidates are constructed based on that motion information. The following combinations of control point MVs are used to construct in order:

{CPMV₁, CPMV₂, CPMV₃}, {CPMV₁, CPMV₂, CPMV₄}, {CPMV₁, CPMV₃, CPMV₄},
{CPMV₂, CPMV₃, CPMV₄}, { CPMV₁, CPMV₂}, { CPMV₁, CPMV₃}.

[0164] The combination of 3 CPMVs constructs a 6-parameter affine merge candidate and the combination of 2 CPMVs constructs a 4-parameter affine merge candidate. To avoid motion scaling process, if the reference indices of control points are different, the related combination of control point MVs is discarded.

[0165] After inherited affine merge candidates and constructed affine merge candidate are checked, if the list is still not full, zero MVs are inserted to the end of the list.

2.10.2. Affine AMVP prediction

[0166] Affine AMVP mode can be applied for CUs with both width and height larger than or equal to 16. An affine flag in CU level is signalled in the bitstream to indicate whether affine AMVP mode is used and then another flag is signalled to indicate whether 4-parameter affine or 6-parameter affine. In this mode, the difference of the CPMVs of current CU and their predictors CPMVPs is signalled in the bitstream. The affine AMVP candidate list size is 2 and it is generated by using the following four types of CPVM candidate in order:

- Inherited affine AMVP candidates that extrapolated from the CPMVs of the neighbour CUs.
- Constructed affine AMVP candidates CPMVPs that are derived using the translational MVs of the neighbour CUs.
- Translational MVs from neighbouring CUs.
- Zero MVs.

[0167] The checking order of inherited affine AMVP candidates is same to the checking order of inherited affine merge candidates. The only difference is that, for AMVP candidate, only the affine CU that has the same reference picture as in current block is considered. No pruning process is applied when inserting an inherited affine motion predictor into the candidate list.

[0168] Constructed AMVP candidate is derived from the specified spatial neighbours shown in Fig. 16. The same checking order is used as done in affine merge candidate construction. In addition, reference picture index of the neighbouring block is also checked. The first block in the checking order that is inter coded and has the same

reference picture as in current CUs is used. There is only one When the current CU is coded with 4-parameter affine mode, and mv_0 and mv_1 are both available, they are added as one candidate in the affine AMVP list. When the current CU is coded with 6-parameter affine mode, and all three CPMVs are available, they are added as one candidate in the affine AMVP list. Otherwise, constructed AMVP candidate is set as unavailable.

[0169] If affine AMVP list candidates is still less than 2 after inherited affine AMVP candidates and Constructed AMVP candidate are checked, mv_0 , mv_1 , and mv_2 will be added, in order, as the translational MVs to predict all control point MVs of the current CU, when available. Finally, zero MVs are used to fill the affine AMVP list if it is still not full.

2.10.3. Affine motion information storage

[0170] In VVC, the CPMVs of affine CUs are stored in a separate buffer. The stored CPMVs are only used to generate the inherited CPMVPs in affine merge mode and affine AMVP mode for the lately coded CUs. The subblock MVs derived from CPMVs are used for motion compensation, MV derivation of merge/AMVP list of translational MVs and de-blocking.

[0171] To avoid the picture line buffer for the additional CPMVs, affine motion data inheritance from the CUs from above CTU is treated differently to the inheritance from the normal neighbouring CUs. If the candidate CU for affine motion data inheritance is in the above CTU line, the bottom-left and bottom-right subblock MVs in the line buffer instead of the CPMVs are used for the affine MVP derivation. In this way, the CPMVs are only stored in local buffer. If the candidate CU is 6-parameter affine coded, the affine model is degraded to 4-parameter model. Fig. 17 illustrates illustration of motion vector usage for proposed combined method. As shown in Fig. 17, along the top CTU boundary, the bottom-left and bottom right subblock motion vectors of a CU are used for affine inheritance of the CUs in bottom CTUs.

2.10.4. Prediction refinement with optical flow for affine mode

[0172] Subblock based affine motion compensation can save memory access bandwidth and reduce computation complexity compared to pixel-based motion compensation, at the cost of prediction accuracy penalty. To achieve a finer granularity of motion compensation, prediction refinement with optical flow (PROF) is used to refine the subblock based affine motion compensated prediction without increasing the memory access bandwidth for motion compensation. In VVC, after the subblock based affine motion compensation is performed, luma prediction sample is refined by adding a difference derived by the optical flow equation. The PROF is described as following four steps:

[0173] Step 1) The subblock-based affine motion compensation is performed to generate subblock prediction $I(i, j)$.

[0174] Step2) The spatial gradients $g_x(i, j)$ and $g_y(i, j)$ of the subblock prediction are calculated at each sample location using a 3-tap filter $[-1, 0, 1]$. The gradient calculation is exactly the same as gradient calculation in BDOF.

$$g_x(i, j) = (I(i + 1, j) \gg \text{shift1}) - (I(i - 1, j) \gg \text{shift1}) \quad (2-11)$$

$$g_y(i, j) = (I(i, j + 1) \gg \text{shift1}) - (I(i, j - 1) \gg \text{shift1}) \quad (2-12)$$

shift1 is used to control the gradient's precision. The subblock (i.e. 4x4) prediction is extended by one sample on each side for the gradient calculation. To avoid additional memory bandwidth and additional interpolation computation, those extended samples on the extended borders are copied from the nearest integer pixel position in the reference picture.

[0175] Step 3) The luma prediction refinement is calculated by the following optical flow equation.

$$\Delta I(i, j) = g_x(i, j) * \Delta v_x(i, j) + g_y(i, j) * \Delta v_y(i, j) \quad (2-13)$$

where the $\Delta v(i, j)$ is the difference between sample MV computed for sample location (i, j) , denoted by $v(i, j)$, and the subblock MV of the subblock to which sample (i, j) belongs, as shown in Fig. 18 which illustrates subblock MV VSB and pixel $\Delta v(i, j)$ (small arrow). The $\Delta v(i, j)$ is quantized in the unit of 1/32 luma sample precision.

[0176] Since the affine model parameters and the sample location relative to the subblock

center are not changed from subblock to subblock, $\Delta v(i, j)$ can be calculated for the first subblock, and reused for other subblocks in the same CU. Let $dx(i, j)$ and $dy(i, j)$ be the horizontal and vertical offset from the sample location (i, j) to the center of the subblock (x_{SB}, y_{SB}) , $\Delta v(x, y)$ can be derived by the following equation,

$$\begin{cases} dx(i, j) = i - x_{SB} \\ dy(i, j) = j - y_{SB} \end{cases} \quad (2-14)$$

$$\begin{cases} \Delta v_x(i, j) = C * dx(i, j) + D * dy(i, j) \\ \Delta v_y(i, j) = E * dx(i, j) + F * dy(i, j) \end{cases} \quad (2-15)$$

[0177] In order to keep accuracy, the enter of the subblock (x_{SB}, y_{SB}) is calculated as $((W_{SB} - 1)/2, (H_{SB} - 1)/2)$, where W_{SB} and H_{SB} are the subblock width and height, respectively.

[0178] For 4-parameter affine model,

$$\begin{cases} C = F = \frac{v_{1x} - v_{0x}}{w} \\ E = -D = \frac{v_{1y} - v_{0y}}{w} \end{cases} \quad (2-16)$$

[0179] For 6-parameter affine model,

$$\begin{cases} C = \frac{v_{1x} - v_{0x}}{w} \\ D = \frac{v_{2x} - v_{0x}}{h} \\ E = \frac{v_{1y} - v_{0y}}{w} \\ F = \frac{v_{2y} - v_{0y}}{h} \end{cases} \quad (2-17)$$

where (v_{0x}, v_{0y}) , (v_{1x}, v_{1y}) , (v_{2x}, v_{2y}) are the top-left, top-right and bottom-left control point motion vectors, w and h are the width and height of the CU.

[0180] Step 4) Finally, the luma prediction refinement $\Delta I(i, j)$ is added to the subblock prediction $I(i, j)$. The final prediction I' is generated as the following equation.

$$I'(i, j) = I(i, j) + \Delta I(i, j) \quad (2-18)$$

[0181] PROF is not be applied in two cases for an affine coded CU: 1) all control point MVs are the same, which indicates the CU only has translational motion; 2) the affine

motion parameters are greater than a specified limit because the subblock based affine MC is degraded to CU based MC to avoid large memory access bandwidth requirement.

[0182] A fast encoding method is applied to reduce the encoding complexity of affine motion estimation with PROF. PROF is not applied at affine motion estimation stage in following two situations: a) if this CU is not the root block and its parent block does not select the affine mode as its best mode, PROF is not applied since the possibility for current CU to select the affine mode as best mode is low; b) if the magnitude of four affine parameters (C, D, E, F) are all smaller than a predefined threshold and the current picture is not a low delay picture, PROF is not applied because the improvement introduced by PROF is small for this case. In this way, the affine motion estimation with PROF can be accelerated.

2.11. Subblock-based temporal motion vector prediction (SbTMVP)

[0183] VVC supports the subblock-based temporal motion vector prediction (SbTMVP) method. Similar to the temporal motion vector prediction (TMVP) in HEVC, SbTMVP uses the motion field in the collocated picture to improve motion vector prediction and merge mode for CUs in the current picture. The same collocated picture used by TMVP is used for SbTMVP. SbTMVP differs from TMVP in the following two main aspects:

- TMVP predicts motion at CU level, but SbTMVP predicts motion at sub-CU level;
- Whereas TMVP fetches the temporal motion vectors from the collocated block in the collocated picture (the collocated block is the bottom-right or center block relative to the current CU), SbTMVP applies a motion shift before fetching the temporal motion information from the collocated picture, where the motion shift is obtained from the motion vector from one of the spatial neighboring blocks of the current CU.

[0184] Fig. 19A and Fig. 19B illustrate the SbTMVP process in VVC. Fig. 19A illustrates spatial neighboring blocks used by ATVMP. Fig. 19B illustrates deriving sub-CU motion field by applying a motion shift from spatial neighbor and scaling the motion information from the corresponding collocated sub-CUs. The SbTMVP process is illustrated in Fig. 19A and Fig. 19B. SbTMVP predicts the motion vectors of the sub-CUs within the current CU in two steps. In the first step, the spatial neighbor A1 in Fig. 19A

is examined. If A1 has a motion vector that uses the collocated picture as its reference picture, this motion vector is selected to be the motion shift to be applied. If no such motion is identified, then the motion shift is set to (0, 0).

[0185] In the second step, the motion shift identified in Step 1 is applied (i.e., added to the current block's coordinates) to obtain sub-CU-level motion information (motion vectors and reference indices) from the collocated picture as shown in Fig. 19B. The example in Fig. 19B assumes the motion shift is set to block A1's motion. Then, for each sub-CU, the motion information of its corresponding block (the smallest motion grid that covers the center sample) in the collocated picture is used to derive the motion information for the sub-CU. After the motion information of the collocated sub-CU is identified, it is converted to the motion vectors and reference indices of the current sub-CU in a similar way as the TMVP process of HEVC, where temporal motion scaling is applied to align the reference pictures of the temporal motion vectors to those of the current CU.

[0186] In VVC, a combined subblock based merge list which contains both SbTMVP candidate and affine merge candidates is used for the signalling of subblock based merge mode. The SbTMVP mode is enabled/disabled by a sequence parameter set (SPS) flag. If the SbTMVP mode is enabled, the SbTMVP predictor is added as the first entry of the list of subblock based merge candidates, and followed by the affine merge candidates. The size of subblock based merge list is signalled in SPS and the maximum allowed size of the subblock based merge list is 5 in VVC.

[0187] The sub-CU size used in SbTMVP is fixed to be 8x8, and as done for affine merge mode, SbTMVP mode is only applicable to the CU with both width and height are larger than or equal to 8.

[0188] The encoding logic of the additional SbTMVP merge candidate is the same as for the other merge candidates, that is, for each CU in P or B slice, an additional RD check is performed to decide whether to use the SbTMVP candidate.

2.12. Adaptive motion vector resolution (AMVR)

[0189] In HEVC, motion vector differences (MVDs) (between the motion vector and predicted motion vector of a CU) are signalled in units of quarter-luma-sample when `use_integer_mv_flag` is equal to 0 in the slice header. In VVC, a CU-level adaptive motion vector resolution (AMVR) scheme is introduced. AMVR allows MVD of the CU to be coded in different precision. Dependent on the mode (normal AMVP mode or affine AMVP mode) for the current CU, the MVDs of the current CU can be adaptively selected as follows:

- Normal AMVP mode: quarter-luma-sample, half-luma-sample, integer-luma-sample or four-luma-sample.
- Affine AMVP mode: quarter-luma-sample, integer-luma-sample or 1/16 luma-sample.

[0190] The CU-level MVD resolution indication is conditionally signalled if the current CU has at least one non-zero MVD component. If all MVD components (that is, both horizontal and vertical MVDs for reference list L0 and reference list L1) are zero, quarter-luma-sample MVD resolution is inferred.

[0191] For a CU that has at least one non-zero MVD component, a first flag is signalled to indicate whether quarter-luma-sample MVD precision is used for the CU. If the first flag is 0, no further signaling is needed and quarter-luma-sample MVD precision is used for the current CU. Otherwise, a second flag is signalled to indicate half-luma-sample or other MVD precisions (integer or four-luma sample) is used for normal AMVP CU. In the case of half-luma-sample, a 6-tap interpolation filter instead of the default 8-tap interpolation filter is used for the half-luma sample position. Otherwise, a third flag is signalled to indicate whether integer-luma-sample or four-luma-sample MVD precision is used for normal AMVP CU. In the case of affine AMVP CU, the second flag is used to indicate whether integer-luma-sample or 1/16 luma-sample MVD precision is used. In order to ensure the reconstructed MV has the intended precision (quarter-luma-sample, half-luma-sample, integer-luma-sample or four-luma-sample), the motion vector predictors for the CU will be rounded to the same precision as that of the MVD before

being added together with the MVD. The motion vector predictors are rounded toward zero (that is, a negative motion vector predictor is rounded toward positive infinity and a positive motion vector predictor is rounded toward negative infinity).

[0192] The encoder determines the motion vector resolution for the current CU using RD check. To avoid always performing CU-level RD check four times for each MVD resolution, in VTM11, the RD check of MVD precisions other than quarter-luma-sample is only invoked conditionally. For normal AMVP mode, the RD cost of quarter-luma-sample MVD precision and integer-luma sample MV precision is computed first. Then, the RD cost of integer-luma-sample MVD precision is compared to that of quarter-luma-sample MVD precision to decide whether it is necessary to further check the RD cost of four-luma-sample MVD precision. When the RD cost for quarter-luma-sample MVD precision is much smaller than that of the integer-luma-sample MVD precision, the RD check of four-luma-sample MVD precision is skipped. Then, the check of half-luma-sample MVD precision is skipped if the RD cost of integer-luma-sample MVD precision is significantly larger than the best RD cost of previously tested MVD precisions. For affine AMVP mode, if affine inter mode is not selected after checking rate-distortion costs of affine merge/skip mode, merge/skip mode, quarter-luma-sample MVD precision normal AMVP mode and quarter-luma-sample MVD precision affine AMVP mode, then 1/16 luma-sample MV precision and 1-pel MV precision affine inter modes are not checked. Furthermore, affine parameters obtained in quarter-luma-sample MV precision affine inter mode is used as starting search point in 1/16 luma-sample and quarter-luma-sample MV precision affine inter modes.

2.13. Bi-prediction with CU-level weight (BCW)

[0193] In HEVC, the bi-prediction signal is generated by averaging two prediction signals obtained from two different reference pictures and/or using two different motion vectors. In VVC, the bi-prediction mode is extended beyond simple averaging to allow weighted averaging of the two prediction signals.

$$P_{\text{bi-pred}} = ((8 - w) * P_0 + w * P_1 + 4) \gg 3 \quad (2-19)$$

[0194] Five weights are allowed in the weighted averaging bi-prediction, $w \in \{-2, 3, 4, 5, 10\}$. For each bi-predicted CU, the weight w is determined in one of two ways:

1) for a non-merge CU, the weight index is signalled after the motion vector difference;
 2) for a merge CU, the weight index is inferred from neighbouring blocks based on the merge candidate index. BCW is only applied to CUs with 256 or more luma samples (i.e., CU width times CU height is greater than or equal to 256). For low-delay pictures, all 5 weights are used. For non-low-delay pictures, only 3 weights ($w \in \{3, 4, 5\}$) are used.

- At the encoder, fast search algorithms are applied to find the weight index without significantly increasing the encoder complexity. These algorithms are summarized as follows. For further details readers are referred to the VTM software and document JVET-L0646. When combined with AMVR, unequal weights are only conditionally checked for 1-pel and 4-pel motion vector precisions if the current picture is a low-delay picture.
- When combined with affine, affine ME will be performed for unequal weights if and only if the affine mode is selected as the current best mode.
- When the two reference pictures in bi-prediction are the same, unequal weights are only conditionally checked.
- Unequal weights are not searched when certain conditions are met, depending on the POC distance between current picture and its reference pictures, the coding QP, and the temporal level.

[0195] The BCW weight index is coded using one context coded bin followed by bypass coded bins. The first context coded bin indicates if equal weight is used; and if unequal weight is used, additional bins are signalled using bypass coding to indicate which unequal weight is used.

[0196] Weighted prediction (WP) is a coding tool supported by the H.264/AVC and HEVC standards to efficiently code video content with fading. Support for WP was also added into the VVC standard. WP allows weighting parameters (weight and offset) to be signalled for each reference picture in each of the reference picture lists L0 and L1. Then, during motion compensation, the weight(s) and offset(s) of the corresponding reference picture(s) are applied. WP and BCW are designed for different types of video content. In

order to avoid interactions between WP and BCW, which will complicate VVC decoder design, if a CU uses WP, then the BCW weight index is not signalled, and w is inferred to be 4 (i.e. equal weight is applied). For a merge CU, the weight index is inferred from neighbouring blocks based on the merge candidate index. This can be applied to both normal merge mode and inherited affine merge mode. For constructed affine merge mode, the affine motion information is constructed based on the motion information of up to 3 blocks. The BCW index for a CU using the constructed affine merge mode is simply set equal to the BCW index of the first control point MV.

[0197] In VVC, CIIP and BCW cannot be jointly applied for a CU. When a CU is coded with CIIP mode, the BCW index of the current CU is set to 2, e.g., equal weight.

2.14. Local illumination compensation (LIC)

[0198] Local illumination compensation (LIC) is a coding tool to address the issue of local illumination changes between current picture and its temporal reference pictures. The LIC is based on a linear model where a scaling factor and an offset are applied to the reference samples to obtain the prediction samples of a current block. Specifically, the LIC can be mathematically modeled by the following equation:

$$P(x, y) = \alpha \cdot P_r(x + v_x, y + v_y) + \beta$$

where $P(x, y)$ is the prediction signal of the current block at the coordinate (x, y) ; $P_r(x + v_x, y + v_y)$ is the reference block pointed by the motion vector (v_x, v_y) ; α and β are the corresponding scaling factor and offset that are applied to the reference block. Fig. 20 illustrates local illumination compensation. Fig. 20 illustrates the LIC process. In Fig. 20, when the LIC is applied for a block, a least mean square error (LMSE) method is employed to derive the values of the LIC parameters (i.e., α and β) by minimizing the difference between the neighboring samples of the current block (i.e., the template T in Fig. 20) and their corresponding reference samples in the temporal reference pictures (i.e., either $T0$ or $T1$ in Fig. 20). Additionally, to reduce the computational complexity, both

the template samples and the reference template samples are subsampled (adaptive subsampling) to derive the LIC parameters, i.e., only the shaded samples in Fig. 20 are used to derive α and β .

[0199] Fig. 21 illustrates no subsampling for the short side. To improve the coding performance, no subsampling for the short side is performed as shown in Fig. 21.

2.15. Decoder side motion vector refinement (DMVR)

[0200] In order to increase the accuracy of the MVs of the merge mode, a bilateral-matching (BM) based decoder side motion vector refinement is applied in VVC. In bi-prediction operation, a refined MV is searched around the initial MVs in the reference picture list L0 and reference picture list L1. The BM method calculates the distortion between the two candidate blocks in the reference picture list L0 and list L1. Fig. 22 illustrates decoding side motion vector refinement. As illustrated in Fig. 22, the SAD between the two blocks based on each MV candidate (e.g., MV0' and MV1') around the initial MV is calculated. The MV candidate with the lowest SAD becomes the refined MV and used to generate the bi-predicted signal.

[0201] In VVC, the application of DMVR is restricted and is only applied for the CUs which are coded with following modes and features:

- CU level merge mode with bi-prediction MV.
- One reference picture is in the past and another reference picture is in the future with respect to the current picture.
- The distances (i.e. POC difference) from two reference pictures to the current picture are same.
- Both reference pictures are short-term reference pictures.
- CU has more than 64 luma samples.
- Both CU height and CU width are larger than or equal to 8 luma samples.
- BCW weight index indicates equal weight.
- WP is not enabled for the current block.
- CIIP mode is not used for the current block.

[0202] The refined MV derived by DMVR process is used to generate the inter prediction samples and also used in temporal motion vector prediction for future pictures coding. While the original MV is used in deblocking process and also used in spatial motion vector prediction for future CU coding.

[0203] The additional features of DMVR are mentioned in the following sub-clauses.

2.15.1. Searching scheme

[0204] In DVMR, the search points are surrounding the initial MV and the MV offset obey the MV difference mirroring rule. In other words, any points that are checked by DMVR, denoted by candidate MV pair (MV0, MV1) obey the following two equations:

$$MV0' = MV0 + MV_offset \quad (2-20)$$

$$MV1' = MV1 - MV_offset \quad (2-21)$$

[0205] Where MV_offset represents the refinement offset between the initial MV and the refined MV in one of the reference pictures. The refinement search range is two integer luma samples from the initial MV. The searching includes the integer sample offset search stage and fractional sample refinement stage.

[0206] 25 points full search is applied for integer sample offset searching. The SAD of the initial MV pair is first calculated. If the SAD of the initial MV pair is smaller than a threshold, the integer sample stage of DMVR is terminated. Otherwise SADs of the remaining 24 points are calculated and checked in raster scanning order. The point with the smallest SAD is selected as the output of integer sample offset searching stage. To reduce the penalty of the uncertainty of DMVR refinement, it is proposed to favor the original MV during the DMVR process. The SAD between the reference blocks referred by the initial MV candidates is decreased by 1/4 of the SAD value.

[0207] The integer sample search is followed by fractional sample refinement. To save the calculational complexity, the fractional sample refinement is derived by using parametric error surface equation, instead of additional search with SAD comparison. The

fractional sample refinement is conditionally invoked based on the output of the integer sample search stage. When the integer sample search stage is terminated with center having the smallest SAD in either the first iteration or the second iteration search, the fractional sample refinement is further applied.

[0208] In parametric error surface based sub-pixel offsets estimation, the center position cost and the costs at four neighboring positions from the center are used to fit a 2-D parabolic error surface equation of the following form

$$E(x, y) = A(x - x_{min})^2 + B(y - y_{min})^2 + C \quad (2-22)$$

where (x_{min}, y_{min}) corresponds to the fractional position with the least cost and C corresponds to the minimum cost value. By solving the above equations by using the cost value of the five search points, the (x_{min}, y_{min}) is computed as:

$$x_{min} = (E(-1,0) - E(1,0)) / (2(E(-1,0) + E(1,0) - 2E(0,0))) \quad (2-23)$$

$$y_{min} = (E(0,-1) - E(0,1)) / (2(E(0,-1) + E(0,1) - 2E(0,0))) \quad (2-24)$$

[0209] The value of x_{min} and y_{min} are automatically constrained to be between -8 and 8 since all cost values are positive and the smallest value is $E(0,0)$. This corresponds to half pel offset with 1/16th-pel MV accuracy in VVC. The computed fractional (x_{min}, y_{min}) are added to the integer distance refinement MV to get the sub-pixel accurate refinement delta MV.

2.15.2. Bilinear-interpolation and sample padding

[0210] In VVC, the resolution of the MVs is 1/16 luma samples. The samples at the fractional position are interpolated using an 8-tap interpolation filter. In DMVR, the search points are surrounding the initial fractional-pel MV with integer sample offset, therefore the samples of those fractional position need to be interpolated for DMVR search process. To reduce the calculation complexity, the bi-linear interpolation filter is used to generate the fractional samples for the searching process in DMVR. Another important effect is that by using bi-linear filter is that with 2-sample search range, the DVMR does

not access more reference samples compared to the normal motion compensation process. After the refined MV is attained with DMVR search process, the normal 8-tap interpolation filter is applied to generate the final prediction. In order to not access more reference samples to normal MC process, the samples, which is not needed for the interpolation process based on the original MV but is needed for the interpolation process based on the refined MV, will be padded from those available samples.

2.15.3. Maximum DMVR processing unit

[0211] When the width and/or height of a CU are larger than 16 luma samples, it will be further split into subblocks with width and/or height equal to 16 luma samples. The maximum unit size for DMVR searching process is limit to 16x16.

2.16. Multi-pass decoder-side motion vector refinement

[0212] In this contribution, a multi-pass decoder-side motion vector refinement is applied instead of DMVR. In the first pass, bilateral matching (BM) is applied to a coding block. In the second pass, BM is applied to each 16x16 subblock within the coding block. In the third pass, MV in each 8x8 subblock is refined by applying bi-directional optical flow (BDOF). The refined MVs are stored for both spatial and temporal motion vector prediction.

2.16.1. First pass – Block based bilateral matching MV refinement

[0213] In the first pass, a refined MV is derived by applying BM to a coding block. Similar to decoder-side motion vector refinement (DMVR), the refined MV is searched around the two initial MVs (MV0 and MV1) in the reference picture lists L0 and L1. The refined MVs (MV0_pass1 and MV1_pass1) are derived around the initiate MVs based on the minimum bilateral matching cost between the two reference blocks in L0 and L1.

[0214] BM performs local search to derive integer sample precision intDeltaMV and half-pel sample precision halfDeltaMv. The local search applies a 3×3 square search pattern to loop through the search range [–sHor, sHor] in a horizontal direction and [–sVer, sVer] in a vertical direction, wherein, the values of sHor and sVer are determined

by the block dimension, and the maximum value of sHor and sVer is 8.

[0215] The bilateral matching cost is calculated as: $\text{bilCost} = \text{mvDistanceCost} + \text{sadCost}$. When the block size $\text{cbW} * \text{cbH}$ is greater than 64, MRSAD cost function is applied to remove the DC effect of the distortion between the reference blocks. When the bilCost at the center point of the 3×3 search pattern has the minimum cost, the intDeltaMV or halfDeltaMV local search is terminated. Otherwise, the current minimum cost search point becomes the new center point of the 3×3 search pattern and the search for the minimum cost continues, until it reaches the end of the search range.

[0216] The existing fractional sample refinement is further applied to derive the final deltaMV. The refined MVs after the first pass are then derived as:

- $\text{MV0_pass1} = \text{MV0} + \text{deltaMV}$,
- $\text{MV1_pass1} = \text{MV1} - \text{deltaMV}$.

2.16.2. Second pass – Subblock based bilateral matching MV refinement

[0217] In the second pass, a refined MV is derived by applying BM to a 16×16 grid subblock. For each subblock, the refined MV is searched around the two MVs (MV0_pass1 and MV1_pass1), obtained on the first pass for the reference picture list L0 and L1. The refined MVs ($\text{MV0_pass2}(\text{sbIdx2})$ and $\text{MV1_pass2}(\text{sbIdx2})$) are derived based on the minimum bilateral matching cost between the two reference subblocks in L0 and L1.

[0218] For each subblock, BM performs full search to derive integer sample precision intDeltaMV. The full search has a search range $[-\text{sHor}, \text{sHor}]$ in a horizontal direction and $[-\text{sVer}, \text{sVer}]$ in a vertical direction, wherein, the values of sHor and sVer are determined by the block dimension, and the maximum value of sHor and sVer is 8.

[0219] The bilateral matching cost is calculated by applying a cost factor to the SATD cost between the two reference subblocks, as: $\text{bilCost} = \text{satdCost} * \text{costFactor}$. The search area $(2 * \text{sHor} + 1) * (2 * \text{sVer} + 1)$ is divided up to 5 diamond shape search regions shown on Fig. 23. Each search region is assigned a costFactor, which is determined by the

distance (intDeltaMV) between each search point and the starting MV, and each diamond region is processed in the order starting from the center of the search area. In each region, the search points are processed in the raster scan order starting from the top left going to the bottom right corner of the region. When the minimum bilCost within the current search region is less than a threshold equal to $sbW * sbH$, the int-pel full search is terminated, otherwise, the int-pel full search continues to the next search region until all search points are examined.

[0220] BM performs local search to derive half sample precision halfDeltaMv. The search pattern and cost function are the same as defined in 2.9.1.

[0221] The existing VVC DMVR fractional sample refinement is further applied to derive the final deltaMV(sbIdx2). The refined MVs at second pass is then derived as:

- $MV0_pass2(sbIdx2) = MV0_pass1 + \text{deltaMV}(sbIdx2)$,
- $MV1_pass2(sbIdx2) = MV1_pass1 - \text{deltaMV}(sbIdx2)$.

2.16.3. Third pass – Subblock based bi-directional optical flow MV refinement

[0222] In the third pass, a refined MV is derived by applying BDOF to an 8×8 grid subblock. For each 8×8 subblock, BDOF refinement is applied to derive scaled Vx and Vy without clipping starting from the refined MV of the parent subblock of the second pass. The derived bioMv(Vx, Vy) is rounded to 1/16 sample precision and clipped between -32 and 32.

[0223] The refined MVs ($MV0_pass3(sbIdx3)$ and $MV1_pass3(sbIdx3)$) at third pass are derived as:

- $MV0_pass3(sbIdx3) = MV0_pass2(sbIdx2) + \text{bioMv}$,
- $MV1_pass3(sbIdx3) = MV0_pass2(sbIdx2) - \text{bioMv}$.

2.17. Sample-based BDOF

[0224] In the sample-based BDOF, instead of deriving motion refinement (Vx, Vy) on a block basis, it is performed per sample.

[0225] The coding block is divided into 8×8 subblocks. For each subblock, whether to

apply BDOF or not is determined by checking the SAD between the two reference subblocks against a threshold. If decided to apply BDOF to a subblock, for every sample in the subblock, a sliding 5×5 window is used and the existing BDOF process is applied for every sliding window to derive V_x and V_y . The derived motion refinement (V_x , V_y) is applied to adjust the bi-predicted sample value for the center sample of the window.

2.18. Extended merge prediction

[0226] In VVC, the merge candidate list is constructed by including the following five types of candidates in order:

- (1) Spatial MVP from spatial neighbour CUs.
- (2) Temporal MVP from collocated CUs.
- (3) History-based MVP from a FIFO table.
- (4) Pairwise average MVP.
- (5) Zero MVs.

[0227] The size of merge list is signalled in sequence parameter set header and the maximum allowed size of merge list is 6. For each CU code in merge mode, an index of best merge candidate is encoded using truncated unary binarization (TU). The first bin of the merge index is coded with context and bypass coding is used for other bins.

[0228] The derivation process of each category of merge candidates is provided in this session. As done in HEVC, VVC also supports parallel derivation of the merging candidate lists for all CUs within a certain size of area.

2.18.1. Spatial candidates derivation

[0229] The derivation of spatial merge candidates in VVC is same to that in HEVC except the positions of first two merge candidates are swapped. Fig. 24 illustrates positions of spatial merge candidate. A maximum of four merge candidates are selected among candidates located in the positions depicted in Fig. 24. The order of derivation is B0, A0, B1, A1 and B2. Position B2 is considered only when one or more than one CUs of position B0, A0, B1, A1 are not available (e.g. because it belongs to another slice or tile) or is intra coded. After candidate at position A1 is added, the addition of the

remaining candidates is subject to a redundancy check which ensures that candidates with same motion information are excluded from the list so that coding efficiency is improved. To reduce computational complexity, not all possible candidate pairs are considered in the mentioned redundancy check. Fig. 25 illustrates candidate pairs considered for redundancy check of spatial merge candidates. Instead only the pairs linked with an arrow in Fig. 25 are considered and a candidate is only added to the list if the corresponding candidate used for redundancy check has not the same motion information.

2.18.2. Temporal candidates derivation

[0230] In this step, only one candidate is added to the list. Particularly, in the derivation of this temporal merge candidate, a scaled motion vector is derived based on co-located CU belonging to the collocated reference picture. The reference picture list to be used for derivation of the co-located CU is explicitly signalled in the slice header. Fig. 26 illustrates illustration of motion vector scaling for temporal merge candidate. The scaled motion vector for temporal merge candidate is obtained as illustrated by the dotted line in Fig. 26, which is scaled from the motion vector of the co-located CU using the POC distances, t_b and t_d , where t_b is defined to be the POC difference between the reference picture of the current picture and the current picture and t_d is defined to be the POC difference between the reference picture of the co-located picture and the co-located picture. The reference picture index of temporal merge candidate is set equal to zero.

[0231] The position for the temporal candidate is selected between candidates C_0 and C_1 , as depicted in Fig. 27. If CU at position C_0 is not available, is intra coded, or is outside of the current row of CTUs, position C_1 is used. Otherwise, position C_0 is used in the derivation of the temporal merge candidate.

2.18.3. History-based merge candidates derivation

[0232] The history-based MVP (HMVP) merge candidates are added to merge list after the spatial MVP and TMVP. In this method, the motion information of a previously coded block is stored in a table and used as MVP for the current CU. The table with multiple

HMVP candidates is maintained during the encoding/decoding process. The table is reset (emptied) when a new CTU row is encountered. Whenever there is a non-subblock inter-coded CU, the associated motion information is added to the last entry of the table as a new HMVP candidate.

[0233] The HMVP table size S is set to be 6, which indicates up to 6 History-based MVP (HMVP) candidates may be added to the table. When inserting a new motion candidate to the table, a constrained first-in-first-out (FIFO) rule is utilized wherein redundancy check is firstly applied to find whether there is an identical HMVP in the table. If found, the identical HMVP is removed from the table and all the HMVP candidates afterwards are moved forward.

[0234] HMVP candidates could be used in the merge candidate list construction process. The latest several HMVP candidates in the table are checked in order and inserted to the candidate list after the TMVP candidate. Redundancy check is applied on the HMVP candidates to the spatial or temporal merge candidate.

[0235] To reduce the number of redundancy check operations, the following simplifications are introduced:

[0236] Number of HMPV candidates is used for merge list generation is set as $(N \leq 4) ? M : (8 - N)$, wherein N indicates number of existing candidates in the merge list and M indicates number of available HMVP candidates in the table.

[0237] Once the total number of available merge candidates reaches the maximally allowed merge candidates minus 1, the merge candidate list construction process from HMVP is terminated.

2.18.4. Pair-wise average merge candidates derivation

[0238] Pairwise average candidates are generated by averaging predefined pairs of candidates in the existing merge candidate list, and the predefined pairs are defined as $\{(0, 1), (0, 2), (1, 2), (0, 3), (1, 3), (2, 3)\}$, where the numbers denote the merge indices to the

merge candidate list. The averaged motion vectors are calculated separately for each reference list. If both motion vectors are available in one list, these two motion vectors are averaged even when they point to different reference pictures; if only one motion vector is available, use the one directly; if no motion vector is available, keep this list invalid.

[0239] When the merge list is not full after pair-wise average merge candidates are added, the zero MVPs are inserted in the end until the maximum merge candidate number is encountered.

2.18.5. Merge estimation region

[0240] Merge estimation region (MER) allows independent derivation of merge candidate list for the CUs in the same merge estimation region (MER). A candidate block that is within the same MER to the current CU is not included for the generation of the merge candidate list of the current CU. In addition, the updating process for the history-based motion vector predictor candidate list is updated only if $(x_{Cb} + cbWidth) \gg \text{Log2ParMrgLevel}$ is greater than $x_{Cb} \gg \text{Log2ParMrgLevel}$ and $(y_{Cb} + cbHeight) \gg \text{Log2ParMrgLevel}$ is great than $(y_{Cb} \gg \text{Log2ParMrgLevel})$ and where (x_{Cb}, y_{Cb}) is the top-left luma sample position of the current CU in the picture and $(cbWidth, cbHeight)$ is the CU size. The MER size is selected at encoder side and signalled as `log2_parallel_merge_level_minus2` in the sequence parameter set.

2.19. New merge candidates

2.19.1. Non-adjacent merge candidates derivation

[0241] Fig. 28 illustrates VVC spatial neighboring blocks of the current block. In VVC, five spatially neighboring blocks shown in Fig. 28 as well as one temporal neighbor are used to derive merge candidates.

[0242] It is proposed to derive the additional merge candidates from the positions non-adjacent to the current block using the same pattern as that in VVC. To achieve this, for each search round i , a virtual block is generated based on the current block as follows:

[0243] First, the relative position of the virtual block to the current block is calculated by:

$$\text{Offsetx} = -i \times \text{gridX}, \text{Offsety} = -i \times \text{gridY}$$

where the Offsetx and Offsety denote the offset of the top-left corner of the virtual block relative to the top-left corner of the current block, gridX and gridY are the width and height of the search grid.

[0244] Second, the width and height of the virtual block are calculated by:

$$\text{newWidth} = i \times 2 \times \text{gridX} + \text{currWidth} \quad \text{newHeight} = i \times 2 \times \text{gridY} + \text{currHeight}.$$

where the currWidth and currHeight are the width and height of current block. The newWidth and newHeight are the width and height of new virtual block.

gridX and gridY are currently set to currWidth and currHeight, respectively.

[0245] Fig. 29 illustrates the relationship between the virtual block and the current block.

[0246] After generating the virtual block, the blocks A_i , B_i , C_i , D_i and E_i can be regarded as the VVC spatial neighboring blocks of the virtual block and their positions are obtained with the same pattern as that in VVC. Obviously, the virtual block is the current block if the search round i is 0. In this case, the blocks A_i , B_i , C_i , D_i and E_i are the spatially neighboring blocks that are used in VVC merge mode.

[0247] When constructing the merge candidate list, the pruning is performed to guarantee each element in merge candidate list to be unique. The maximum search round is set to 1, which means that five non-adjacent spatial neighbor blocks are utilized.

[0248] Non-adjacent spatial merge candidates are inserted into the merge list after the temporal merge candidate in the order of $B_1 \rightarrow A_1 \rightarrow C_1 \rightarrow D_1 \rightarrow E_1$.

2.19.2. STMVP

[0249] It is proposed to derive an averaging candidate as STMVP candidate using three spatial merge candidates and one temporal merge candidate.

[0250] STMVP is inserted before the above-left spatial merge candidate.

[0251] The STMVP candidate is pruned with all the previous merge candidates in the merge list.

[0252] For the spatial candidates, the first three candidates in the current merge candidate list are used.

[0253] For the temporal candidate, the same position as VTM / HEVC collocated position is used.

[0254] For the spatial candidates, the first, second, and third candidates inserted in the current merge candidate list before STMVP are denoted as F, S, and T.

[0255] The temporal candidate with the same position as VTM / HEVC collocated position used in TMVP is denoted as Col.

[0256] The motion vector of the STMVP candidate in prediction direction X (denoted as mvLX) is derived as follows:

1) If the reference indices of the four merge candidates are all valid and are all equal to zero in prediction direction X (X = 0 or 1),

$$mvLX = (mvLX_F + mvLX_S + mvLX_T + mvLX_Col) >> 2$$

2) If reference indices of three of the four merge candidates are valid and are equal to zero in prediction direction X (X = 0 or 1),

$$mvLX = (mvLX_F \times 3 + mvLX_S \times 3 + mvLX_Col \times 2) >> 3 \quad \text{or}$$

$$mvLX = (mvLX_F \times 3 + mvLX_T \times 3 + mvLX_Col \times 2) >> 3 \quad \text{or}$$

$$mvLX = (mvLX_S \times 3 + mvLX_T \times 3 + mvLX_Col \times 2) >> 3$$

3) If reference indices of two of the four merge candidates are valid and are equal to zero in prediction direction X (X = 0 or 1),

$$mvLX = (mvLX_F + mvLX_Col) >> 1 \quad \text{or}$$

$$mvLX = (mvLX_S + mvLX_Col) >> 1 \quad \text{or}$$

$$mvLX = (mvLX_T + mvLX_Col) >> 1$$

Note: If the temporal candidate is unavailable, the STMVP mode is off.

2.19.3. Merge list size

[0257] If considering both non-adjacent and STMVP merge candidates, the size of merge list is signalled in sequence parameter set header and the maximum allowed size of merge

list is 8.

2.20. Geometric partitioning mode (GPM)

[0258] In VVC, a geometric partitioning mode is supported for inter prediction. The geometric partitioning mode is signalled using a CU-level flag as one kind of merge mode, with other merge modes including the regular merge mode, the MMVD mode, the CIIP mode and the subblock merge mode. In total 64 partitions are supported by geometric partitioning mode for each possible CU size $w \times h = 2^m \times 2^n$ with $m, n \in \{3 \cdots 6\}$ excluding 8x64 and 64x8.

[0259] Fig. 30 illustrates examples of the GPM splits grouped by identical angles. When this mode is used, a CU is split into two parts by a geometrically located straight line (Fig. 30). The location of the splitting line is mathematically derived from the angle and offset parameters of a specific partition. Each part of a geometric partition in the CU is inter-predicted using its own motion; only uni-prediction is allowed for each partition, that is, each part has one motion vector and one reference index. The uni-prediction motion constraint is applied to ensure that same as the conventional bi-prediction, only two motion compensated prediction are needed for each CU. The uni-prediction motion for each partition is derived using the process described in 2.20.1.

[0260] If geometric partitioning mode is used for the current CU, then a geometric partition index indicating the partition mode of the geometric partition (angle and offset), and two merge indices (one for each partition) are further signalled. The number of maximum GPM candidate size is signalled explicitly in SPS and specifies syntax binarization for GPM merge indices. After predicting each of part of the geometric partition, the sample values along the geometric partition edge are adjusted using a blending processing with adaptive weights as in 2.20.2. This is the prediction signal for the whole CU, and transform and quantization process will be applied to the whole CU as in other prediction modes. Finally, the motion field of a CU predicted using the geometric partition modes is stored as in 2.20.3.

2.20.1. Uni-prediction candidate list construction

[0261] The uni-prediction candidate list is derived directly from the merge candidate list constructed according to the extended merge prediction process in 2.18. Denote n as the index of the uni-prediction motion in the geometric uni-prediction candidate list. The LX motion vector of the n -th extended merge candidate, with X equal to the parity of n , is used as the n -th uni-prediction motion vector for geometric partitioning mode. Fig. 31 illustrates uni-prediction MV selection for geometric partitioning mode. These motion vectors are marked with “x” in Fig. 31. In case a corresponding LX motion vector of the n -th extended merge candidate does not exist, the $L(1 - X)$ motion vector of the same candidate is used instead as the uni-prediction motion vector for geometric partitioning mode.

2.20.2. Blending along the geometric partitioning edge

[0262] After predicting each part of a geometric partition using its own motion, blending is applied to the two prediction signals to derive samples around geometric partition edge. The blending weight for each position of the CU are derived based on the distance between individual position and the partition edge.

[0263] The distance for a position (x, y) to the partition edge are derived as:

$$d(x, y) = (2x + 1 - w) \cos(\varphi_i) + (2y + 1 - h) \sin(\varphi_i) - \rho_j \quad (2-25)$$

$$\rho_j = \rho_{x,j} \cos(\varphi_i) + \rho_{y,j} \sin(\varphi_i) \quad (2-26)$$

$$\rho_{x,j} = \begin{cases} 0 & i \% 16 = 8 \text{ or } (i \% 16 \neq 0 \text{ and } h \geq w) \\ \pm(j \times w) \gg 2 & \text{otherwise} \end{cases} \quad (2-27)$$

$$\rho_{y,j} = \begin{cases} \pm(j \times h) \gg 2 & i \% 16 = 8 \text{ or } (i \% 16 \neq 0 \text{ and } h \geq w) \\ 0 & \text{otherwise} \end{cases} \quad (2-28)$$

where i, j are the indices for angle and offset of a geometric partition, which depend on the signaled geometric partition index. The sign of $\rho_{x,j}$ and $\rho_{y,j}$ depend on angle index i .

[0264] The weights for each part of a geometric partition are derived as following:

$$wldxL(x, y) = partldx ? 32 + d(x, y) : 32 - d(x, y) \quad (2-29)$$

$$w_0(x, y) = \frac{\text{Clip3}(0, 8, (\text{wIdxL}(x, y) + 4) \gg 3)}{8} \quad (2-30)$$

$$w_1(x, y) = 1 - w_0(x, y) \quad (2-31)$$

The partIdx depends on the angle index i . One example of weigh w_0 is illustrated in Fig. 32. Fig. 32 illustrates exemplified generation of a bending weight w_0 using geometric partitioning mode.

2.20.3. Motion field storage for geometric partitioning mode

[0265] Mv1 from the first part of the geometric partition, Mv2 from the second part of the geometric partition and a combined Mv of Mv1 and Mv2 are stored in the motion filed of a geometric partitioning mode coded CU.

[0266] The stored motion vector type for each individual position in the motion filed are determined as:

$$sType = \text{abs}(\text{motionIdx}) < 32 ? 2 : (\text{motionIdx} \leq 0 ? (1 - \text{partIdx}) : \text{partIdx}) \quad (2-32)$$

where motionIdx is equal to $d(4x + 2, 4y + 2)$, which is recalculated from equation (2-18). The partIdx depends on the angle index i .

[0267] If sType is equal to 0 or 1, Mv0 or Mv1 are stored in the corresponding motion field, otherwise if sType is equal to 2, a combined Mv from Mv0 and Mv2 are stored. The combined Mv are generated using the following process:

- 1) If Mv1 and Mv2 are from different reference picture lists (one from L0 and the other from L1), then Mv1 and Mv2 are simply combined to form the bi-prediction motion vectors.

Otherwise, if Mv1 and Mv2 are from the same list, only uni-prediction motion Mv2 is stored.

2.21. Multi-hypothesis prediction

[0268] In multi-hypothesis prediction (MHP), up to two additional predictors are signalled on top of inter AMVP mode, regular merge mode, affine merge and MMVD mode. The resulting overall prediction signal is accumulated iteratively with each additional prediction signal.

$$p_{n+1} = (1 - \alpha_{n+1})p_n + \alpha_{n+1}h_{n+1}$$

[0269] The weighting factor α is specified according to the following Table 4:

Table 4 – weighting factor for MHP

add_hyp_weight_idx	α
0	1/4
1	-1/8

[0270] For inter AMVP mode, MHP is only applied if non-equal weight in BCW is selected in bi-prediction mode.

[0271] The additional hypothesis can be either merge or AMVP mode. In the case of merge mode, the motion information is indicated by a merge index, and the merge candidate list is the same as in the Geometric Partition Mode. In the case of AMVP mode, the reference index, MVP index, and MVD are signaled.

2.22. Non-adjacent spatial candidate

[0272] The non-adjacent spatial merge candidates are inserted after the TMVP in the regular merge candidate list. Fig. 33 illustrates spatial neighboring blocks used to derive the spatial merge candidates. The pattern of the spatial merge candidates is shown on Fig. 33. The distances between the non-adjacent spatial candidates and the current coding block are based on the width and height of the current coding block.

2.23. Template matching (TM)

[0273] Template matching (TM) is a decoder-side MV derivation method to refine the motion information of the current CU by finding the closest match between a template (i.e., top and/or left neighbouring blocks of the current CU) in the current picture and a block (i.e., same size to the template) in a reference picture. Fig. 34 illustrates template matching performs on a search area around initial MV. As illustrated in Fig. 34, a better MV is to be searched around the initial motion of the current CU within a $[-8, +8]$ -pel search range. The template matching that was previously proposed in JVET-J0021 is adopted in this contribution with two modifications: search step size is determined based

on AMVR mode and TM can be cascaded with bilateral matching process in merge modes.

[0274] In AMVP mode, an MVP candidate is determined based on template matching error to pick up the one which reaches the minimum difference between current block template and reference block template, and then TM performs only for this particular MVP candidate for MV refinement. TM refines this MVP candidate, starting from full-pel MVD precision (or 4-pel for 4-pel AMVR mode) within a $[-8, +8]$ -pel search range by using iterative diamond search. The AMVP candidate may be further refined by using cross search with full-pel MVD precision (or 4-pel for 4-pel AMVR mode), followed sequentially by half-pel and quarter-pel ones depending on AMVR mode as specified in Table 5. This search process ensures that the MVP candidate still keeps the same MV precision as indicated by AMVR mode after TM process.

Table 5 – Search patterns of AMVR and merge mode with AMVR.

Search pattern	AMVR mode				Merge mode	
	4-pel	Full-pel	Half-pel	Quarter-pel	AltIF=0	AltIF=1
4-pel diamond	v					
4-pel cross	v					
Full-pel diamond		v	v	v	v	v
Full-pel cross		v	v	v	v	v
Half-pel cross			v	v	v	v
Quarter-pel cross				v	v	
1/8-pel cross					v	

[0275] In merge mode, similar search method is applied to the merge candidate indicated by the merge index. As Table 5 shows, TM may perform all the way down to 1/8-pel MVD precision or skipping those beyond half-pel MVD precision, depending on whether the alternative interpolation filter (that is used when AMVR is of half-pel mode) is used according to merged motion information. Besides, when TM mode is enabled, template

matching may work as an independent process or an extra MV refinement process between block-based and subblock-based bilateral matching (BM) methods, depending on whether BM can be enabled or not according to its enabling condition check.

2.24. Overlapped block motion compensation (OBMC)

[0276] Overlapped Block Motion Compensation (OBMC) has previously been used in H.263. In the JEM, unlike in H.263, OBMC can be switched on and off using syntax at the CU level. When OBMC is used in the JEM, the OBMC is performed for all motion compensation (MC) block boundaries except the right and bottom boundaries of a CU. Moreover, it is applied for both the luma and chroma components. In the JEM, a MC block is corresponding to a coding block. When a CU is coded with sub-CU mode (includes sub-CU merge, affine and FRUC mode), each sub-block of the CU is a MC block. To process CU boundaries in a uniform fashion, OBMC is performed at sub-block level for all MC block boundaries, where sub-block size is set equal to 4×4 , as illustrated in Fig. 35. Fig. 35 illustrates illustration of sub-blocks where OBMC applies.

[0277] When OBMC applies to the current sub-block, besides current motion vectors, motion vectors of four connected neighbouring sub-blocks, if available and are not identical to the current motion vector, are also used to derive prediction block for the current sub-block. These multiple prediction blocks based on multiple motion vectors are combined to generate the final prediction signal of the current sub-block.

[0278] Prediction block based on motion vectors of a neighbouring sub-block is denoted as P_N , with N indicating an index for the neighbouring *above*, *below*, *left* and *right* sub-blocks and prediction block based on motion vectors of the current sub-block is denoted as P_C . When P_N is based on the motion information of a neighbouring sub-block that contains the same motion information to the current sub-block, the OBMC is not performed from P_N . Otherwise, every sample of P_N is added to the same sample in P_C , i.e., four rows/columns of P_N are added to P_C . The weighting factors $\{1/4, 1/8, 1/16, 1/32\}$ are used for P_N and the weighting factors $\{3/4, 7/8, 15/16, 31/32\}$ are used for P_C . The

exception are small MC blocks, (i.e., when height or width of the coding block is equal to 4 or a CU is coded with sub-CU mode), for which only two rows/columns of P_N are added to P_C . In this case weighting factors $\{1/4, 1/8\}$ are used for P_N and weighting factors $\{3/4, 7/8\}$ are used for P_C . For P_N generated based on motion vectors of vertically (horizontally) neighbouring sub-block, samples in the same row (column) of P_N are added to P_C with a same weighting factor.

[0279] In the JEM, for a CU with size less than or equal to 256 luma samples, a CU level flag is signalled to indicate whether OBMC is applied or not for the current CU. For the CUs with size larger than 256 luma samples or not coded with AMVP mode, OBMC is applied by default. At the encoder, when OBMC is applied for a CU, its impact is taken into account during the motion estimation stage. The prediction signal formed by OBMC using motion information of the top neighbouring block and the left neighbouring block is used to compensate the top and left boundaries of the original signal of the current CU, and then the normal motion estimation process is applied.

2.25. Multiple transform selection (MTS) for core transform

[0280] In addition to DCT-II which has been employed in HEVC, a Multiple Transform Selection (MTS) scheme is used for residual coding both inter and intra coded blocks. It uses multiple selected transforms from the DCT8/DST7. The newly introduced transform matrices are DST-VII and DCT-VIII. Table 6 shows the basis functions of the selected DST/DCT.

Table 6 – Transform basis functions of DCT-II/ VIII and DSTVII for N-point input

Transform Type	Basis function $T_i(j)$, $i, j = 0, 1, \dots, N-1$
DCT-II	$T_i(j) = \omega_0 \cdot \sqrt{\frac{2}{N}} \cdot \cos\left(\frac{\pi \cdot i \cdot (2j + 1)}{2N}\right)$ where, $\omega_0 = \begin{cases} \sqrt{\frac{2}{N}} & i = 0 \\ 1 & i \neq 0 \end{cases}$
DCT-VIII	$T_i(j) = \sqrt{\frac{4}{2N + 1}} \cdot \cos\left(\frac{\pi \cdot (2i + 1) \cdot (2j + 1)}{4N + 2}\right)$
DST-VII	$T_i(j) = \sqrt{\frac{4}{2N + 1}} \cdot \sin\left(\frac{\pi \cdot (2i + 1) \cdot (j + 1)}{2N + 1}\right)$

[0281] In order to keep the orthogonality of the transform matrix, the transform matrices are quantized more accurately than the transform matrices in HEVC. To keep the intermediate values of the transformed coefficients within the 16-bit range, after horizontal and after vertical transform, all the coefficients are to have 10-bit.

[0282] In order to control MTS scheme, separate enabling flags are specified at SPS level for intra and inter, respectively. When MTS is enabled at SPS, a CU level flag is signalled to indicate whether MTS is applied or not. Here, MTS is applied only for luma. The MTS signaling is skipped when one of the below conditions is applied.

- The position of the last significant coefficient for the luma TB is less than 1 (i.e., DC only).
- The last significant coefficient of the luma TB is located inside the MTS zero-out region.

[0283] If MTS CU flag is equal to zero, then DCT2 is applied in both directions. However, if MTS CU flag is equal to one, then two other flags are additionally signalled to indicate the transform type for the horizontal and vertical directions, respectively. Transform and signalling mapping table as shown in Table 7. Unified the transform selection for ISP and implicit MTS is used by removing the intra-mode and block-shape dependencies. If current block is ISP mode or if the current block is intra block and both intra and inter explicit MTS is on, then only DST7 is used for both horizontal and vertical transform

cores. When it comes to transform matrix precision, 8-bit primary transform cores are used. Therefore, all the transform cores used in HEVC are kept as the same, including 4-point DCT-2 and DST-7, 8-point, 16-point and 32-point DCT-2. Also, other transform cores including 64-point DCT-2, 4-point DCT-8, 8-point, 16-point, 32-point DST-7 and DCT-8, use 8-bit primary transform cores.

Table 7 – Transform and signalling mapping table

MTS_CU_flag	MTS_Hor_flag	MTS_Ver_flag	Intra/inter	
			Horizontal	Vertical
0			DCT2	
1	0	0	DST7	DST7
	0	1	DCT8	DST7
	1	0	DST7	DCT8
	1	1	DCT8	DCT8

[0284] To reduce the complexity of large size DST-7 and DCT-8, High frequency transform coefficients are zeroed out for the DST-7 and DCT-8 blocks with size (width or height, or both width and height) equal to 32. Only the coefficients within the 16x16 lower-frequency region are retained.

[0285] As in HEVC, the residual of a block can be coded with transform skip mode. To avoid the redundancy of syntax coding, the transform skip flag is not signalled when the CU level MTS_CU_flag is not equal to zero. Note that implicit MTS transform is set to DCT2 when LFNST or MIP is activated for the current CU. Also the implicit MTS can be still enabled when MTS is enabled for inter coded blocks.

2.26. Subblock transform (SBT)

[0286] In VTM, subblock transform is introduced for an inter-predicted CU. In this transform mode, only a sub-part of the residual block is coded for the CU. When inter-predicted CU with `cu_cbf` equal to 1, `cu_sbt_flag` may be signaled to indicate whether the whole residual block or a sub-part of the residual block is coded. In the former case, inter MTS information is further parsed to determine the transform type of the CU. In the latter case, a part of the residual block is coded with inferred adaptive transform and the other part of the residual block is zeroed out.

[0287] When SBT is used for an inter-coded CU, SBT type and SBT position information are signaled in the bitstream. There are two SBT types and two SBT positions, as indicated in Fig. 36. For SBT-V (or SBT-H), the TU width (or height) may equal to half of the CU width (or height) or 1/4 of the CU width (or height), resulting in 2:2 split or 1:3/3:1 split. The 2:2 split is like a binary tree (BT) split while the 1:3/3:1 split is like an asymmetric binary tree (ABT) split. In ABT splitting, only the small region contains the non-zero residual. If one dimension of a CU is 8 in luma samples, the 1:3/3:1 split along that dimension is disallowed. There are at most 8 SBT modes for a CU.

[0288] Position-dependent transform core selection is applied on luma transform blocks in SBT-V and SBT-H (chroma TB always using DCT-2). The two positions of SBT-H and SBT-V are associated with different core transforms. More specifically, the horizontal and vertical transforms for each SBT position is specified in Fig. 36. For example, the horizontal and vertical transforms for SBT-V position 0 is DCT-8 and DST-7, respectively. When one side of the residual TU is greater than 32, the transform for both dimensions is set as DCT-2. Therefore, the subblock transform jointly specifies the TU tiling, cbf, and horizontal and vertical core transform type of a residual block.

[0289] The SBT is not applied to the CU coded with combined inter-intra mode.

2.27. Template matching based adaptive merge candidate reorder

[0290] To improve the coding efficiency, after the merge candidate list is constructed,

the order of each merge candidate is adjusted according to the template matching cost. The merge candidates are arranged in the list in accordance with the template matching cost of ascending order. It is operated in the form of sub-group.

[0291] The template matching cost is measured by the SAD (Sum of absolute differences) between the neighbouring samples of the current CU and their corresponding reference samples. If a merge candidate includes bi-predictive motion information, the corresponding reference samples are the average of the corresponding reference samples in reference list0 and the corresponding reference samples in reference list1, as illustrated in Fig. 37. Fig. 37 illustrates neighbouring samples used for calculating SAD. If a merge candidate includes sub-CU level motion information, the corresponding reference samples consist of the neighbouring samples of the corresponding reference sub-blocks, as illustrated in Fig. 38. Fig. 38 illustrates neighbouring samples used for calculating SAD for sub-CU level motion information.

[0292] The sorting process is operated in the form of sub-group, as illustrated in Fig. 39. The first three merge candidates are sorted together. The following three merge candidates are sorted together.

[0293] The template size (width of the left template or height of the above template) is 1. The sub-group size is 3.

2.28. Adaptive Merge Candidate List

[0294] We can assume the number of the merge candidates is 8. We take the first 5 merge candidates as a first subgroup and take the following 3 merge candidates as a second subgroup (i.e., the last subgroup).

[0295] For the encoder, after the merge candidate list is constructed, some merge candidates are adaptively reordered in an ascending order of costs of merge candidates as shown in Fig. 40. Fig. 40 illustrates reorder process in encoder.

[0296] More specifically, the template matching costs for the merge candidates in all

subgroups except the last subgroup are computed; then reorder the merge candidates in their own subgroups except the last subgroup; finally, the final merge candidate list will be got.

[0297] For the decoder, after the merge candidate list is constructed, some/no merge candidates are adaptively reordered in ascending order of costs of merge candidates as shown in Fig. 41. Fig. 38 illustrates reorder process in decoder. In Fig. 41, the subgroup the selected (signaled) merge candidate located in is called the selected subgroup.

[0298] More specifically, if the selected merge candidate is located in the last subgroup, the merge candidate list construction process is terminated after the selected merge candidate is derived, no reorder is performed and the merge candidate list is not changed; otherwise, the execution process is as follows:

[0299] The merge candidate list construction process is terminated after all the merge candidates in the selected subgroup are derived; compute the template matching costs for the merge candidates in the selected subgroup; reorder the merge candidates in the selected subgroup; finally, a new merge candidate list will be got.

[0300] For both encoder and decoder:

[0301] A template matching cost is derived as a function of T and RT, wherein T is a set of samples in the template and RT is a set of reference samples for the template.

[0302] When deriving the reference samples of the template for a merge candidate, the motion vectors of the merge candidate are rounded to the integer pixel accuracy.

[0303] The reference samples of the template (RT) for bi-directional prediction are derived by weighted averaging of the reference samples of the template in reference list0 (RT_0) and the reference samples of the template in reference list1 (RT_1) as follows.

$$RT = ((8 - w) * RT_0 + w * RT_1 + 4) \gg 3 \quad (2-33)$$

where the weight of the reference template in reference list0 (8-w) and the weight of the reference template in reference list1 (w) are decided by the BCW index of the merge candidate. BCW index equal to {0,1,2,3,4} corresponds to w equal to {-2,3,4,5,10}, respectively.

[0304] If the Local Illumination Compensation (LIC) flag of the merge candidate is true, the reference samples of the template are derived with LIC method.

[0305] The template matching cost is calculated based on the sum of absolute differences (SAD) of T and RT.

[0306] The template size is 1. That means the width of the left template and/or the height of the above template is 1.

[0307] If the coding mode is MMVD, the merge candidates to derive the base merge candidates are not reordered.

[0308] If the coding mode is GPM, the merge candidates to derive the uni-prediction candidate list are not reordered.

2.29. Geometric prediction mode with Motion Vector Difference

[0309] In Geometric prediction mode with Motion Vector Difference (GMVD), each geometric partition in GPM can decide to use GMVD or not. If GMVD is chosen for a geometric region, the MV of the region is calculated as a sum of the MV of a merge candidate and an MVD. All other processing is kept the same as in GPM.

[0310] With GMVD, an MVD is signaled as a pair of direction and distance. There are nine candidate distances ($\frac{1}{4}$ -pel, $\frac{1}{2}$ -pel, 1-pel, 2-pel, 3-pel, 4-pel, 6-pel, 8-pel, 16-pel), and eight candidate directions (four horizontal/vertical directions and four diagonal directions) involved. In addition, when `pic_fpel_mmvd_enabled_flag` is equal to 1, the MVD in GMVD is also left shifted by 2 as in MMVD.

2.30. Affine MMVD

[0311] In affine MMVD, an affine merge candidate (which is called, base affine merge candidate) is selected, the MVs of the control points are further refined by the signalled

MVD information.

[0312] The MVD information for the MVs of all the control points are the same in one prediction direction.

[0313] When the starting MVs is bi-prediction MVs with the two MVs point to the different sides of the current picture (i.e. the POC of one reference is larger than the POC of the current picture, and the POC of the other reference is smaller than the POC of the current picture), the MV offset added to the list0 MV component of starting MV and the MV offset for the list1 MV has opposite value; otherwise, when the starting MVs is bi-prediction MVs with both lists point to the same side of the current picture (i.e. POCs of two references are both larger than the POC of the current picture, or are both smaller than the POC of the current picture), the MV offset added to the list0 MV component of starting MV and the MV offset for the list1 MV are the same.

2.31. Adaptive decoder side motion vector refinement (ADMVR)

[0314] In ECM-2.0, a multi-pass decoder-side motion vector refinement (DMVR) method is applied in regular merge mode if the selected merge candidate meets the DMVR conditions. In the first pass, bilateral matching (BM) is applied to the coding block. In the second pass, BM is applied to each 16x16 subblock within the coding block. In the third pass, MV in each 8x8 subblock is refined by applying bi-directional optical flow (BDOF).

[0315] Adaptive decoder side motion vector refinement method consists of the two new merge modes introduced to refine MV only in one direction, either L0 or L1, of the bi prediction for the merge candidates that meet the DMVR conditions. The multi-pass DMVR process is applied for the selected merge candidate to refine the motion vectors, however either MVD0 or MVD1 is set to zero in the 1st pass (i.e., PU level) DMVR.

[0316] Like the regular merge mode, merge candidates for the proposed merge modes are derived from the spatial neighboring coded blocks, TMVPs, non-adjacent blocks, HMVPs, and pair-wise candidate. The difference is that only those meet DMVR conditions are added into the candidate list. The same merge candidate list (i.e., ADMVR

merge list) is used by the two proposed merge modes and merge index is coded as in regular merge mode.

2.32. IBC with Template Matching

[0317] It is proposed to also use Template Matching with IBC for both IBC merge mode and IBC AMVP mode.

[0318] The IBC-TM merge list has been modified compared to the one used by regular IBC merge mode such that the candidates are selected according to a pruning method with a motion distance between the candidates as in the regular TM merge mode. The ending zero motion fulfillment (which is a nonsense regarding Intra coding) has been replaced by motion vectors to the left $(-W, 0)$, top $(0, -H)$ and top-left $(-W, -H)$ CUs, then, if necessary, the list is fulfilled with the left one without pruning.

[0319] In the IBC-TM merge mode, the selected candidates are refined with the Template Matching method prior to the RDO or decoding process. The IBC-TM merge mode has been put in competition with the regular IBC merge mode and a TM-merge flag is signaled.

[0320] In the IBC-TM AMVP mode, up to 3 candidates are selected from the IBC merge list. Each of those 3 selected candidates are refined using the Template Matching method and sorted according to their resulting Template Matching cost. Only the 2 first ones are then considered in the motion estimation process as usual.

[0321] The Template Matching refinement for both IBC-TM merge and AMVP modes is quite simple since IBC motion vectors are constrained to be integer and within a reference region as shown in Fig. 42. Fig. 42 illustrates IBC reference region depending on current CU position. So, in IBC-TM merge mode, all refinements are performed at integer precision, and in IBC-TM AMVP mode, they are performed either at integer or 4-pel precision. In both cases, the refined motion vectors in each refinement step must respect the constraint of the reference region.

2.33. IBC Merge Mode with Block Vector Differences

[0322] IBC merge mode with block vector differences is shown as follows.

[0323] The distance set is {1-pel, 2-pel, 4-pel, 8-pel, 12-pel, 16-pel, 24-pel, 32-pel, 40-pel, 48-pel, 56-pel, 64-pel, 72-pel, 80-pel, 88-pel, 96-pel, 104-pel, 112-pel, 120-pel, 128-pel}, and the BVD directions are two horizontal and two vertical directions.

[0324] The base candidates are selected from the first five candidates in the reordered IBC merge list. And based on the SAD cost between the template (one row above and one column left to the current block) and its reference for each refinement position, all the possible MBVD refinement positions (20×4) for each base candidate are reordered. Finally, the top 8 refinement positions with the lowest template SAD costs are kept as available positions, consequently for MBVD index coding.

2.34. Reconstruction-Reordered IBC (RR-IBC)

[0325] Fig. 43 illustrates examples of symmetry in screen content pictures.

[0326] Screen content coding tools like Intra Block Copy (IBC) generate a prediction block by directly copying a prior coded reference region in the same picture. Symmetry is often observed in video content, especially in text character regions and computer-generated graphics in screen content sequences, as shown in Fig. 43. Therefore, a specific screen content coding tool considering the symmetry would be efficient to compress such kinds of video contents.

[0327] A Reconstruction-Reordered IBC (RR-IBC) mode is proposed for screen content video coding. When it is applied, the samples in a reconstruction block are flipped according to a flip type of the current block. At the encoder side, the original block is flipped before motion search and residual calculation, while the prediction block is derived without flipping. At the decoder side, the reconstruction block is flipped back to restore the original block.

[0328] Two flip methods, horizontal flip and vertical flip, are supported for RR-IBC

coded blocks. A syntax flag is firstly signalled for an IBC AMVP coded block, indicating whether the reconstruction is flipped, and if it is flipped, another flag is further signaled specifying the flip type. For IBC merge, the flip type is inherited from neighbouring blocks, without syntax signalling. Considering the horizontal or vertical symmetry, the current block and the reference block are normally aligned horizontally or vertically. Therefore, when a horizontal flip is applied, the vertical component of the BV is not signaled and inferred to be equal to 0. Similarly, the horizontal component of the BV is not signaled and inferred to be equal to 0 when a vertical flip is applied.

[0329] To better utilize the symmetry property, a flip-aware BV adjustment approach is applied to refine the block vector candidate. Fig. 44A illustrates an illustration of BV adjustment for horizontal flip. Fig. 44B illustrates an illustration of BV adjustment for vertical flip. For example, as shown in Fig. 44A and Fig. 44B, (x_{nbr}, y_{nbr}) and (x_{cur}, y_{cur}) represent the coordinates of the center sample of the neighboring block and the current block, respectively, BV^{nbr} and BV^{cur} denotes the BV of the neighboring block and the current block, respectively. Instead of directly inheriting the BV from a neighbouring block, the horizontal component of BV^{cur} is calculated by adding a motion shift to the horizontal component of BV^{nbr} (denoted as BV^{nbr}_h) in case that the neighbouring block is coded with a horizontal flip, i.e., $BV^{cur}_h = 2(x_{nbr} - x_{cur}) + BV^{nbr}_h$. Similarly, the vertical component of BV^{cur} is calculated by adding a motion shift to the vertical component of BV^{nbr} (denoted as BV^{nbr}_v) in case that the neighbouring block is coded with a vertical flip, i.e., $BV^{cur}_v = 2(y_{nbr} - y_{cur}) + BV^{nbr}_v$.

2.35. Intra template matching

[0330] Intra template matching prediction (Intra TMP) is a special intra prediction mode that copies the best prediction block from the reconstructed part of the current frame, whose L-shaped template matches the current template. For a predefined search range, the encoder searches for the most similar template to the current template in a reconstructed part of the current frame and uses the corresponding block as a prediction block. The encoder then signals the usage of this mode, and the same prediction operation is

performed at the decoder side.

[0331] Fig. 45 illustrates an intra template matching search area used. The prediction signal is generated by matching the L-shaped causal neighbor of the current block with another block in a predefined search area in Fig. 45 consisting of:

R1: current CTU,

R2: top-left CTU,

R3: above CTU,

R4: left CTU.

[0332] SAD is used as a cost function.

[0333] Within each region, the decoder searches for the template that has least SAD with respect to the current one and uses its corresponding block as a prediction block.

[0334] The dimensions of all regions (SearchRange_w, SearchRange_h) are set proportional to the block dimension (BlkW, BlkH) to have a fixed number of SAD comparisons per pixel. That is:

$$\text{SearchRange_w} = a * \text{BlkW}$$

$$\text{SearchRange_h} = a * \text{BlkH}$$

Where ‘a’ is a constant that controls the gain/complexity trade-off. In practice, ‘a’ is equal to 5.

[0335] The Intra template matching tool is enabled for CUs with size less than or equal to 64 in width and height. This maximum CU size for Intra template matching is configurable.

[0336] The Intra template matching prediction mode is signaled at CU level through a dedicated flag when DIMD is not used for current CU.

3. Problems

[0337] In current design of LIC, when both above and left neighbouring reconstructed samples are available, the samples of both above and left are used to derive the LIC

parameters. The coding performance of LIC can be improved by increasing the diversity of derived parameters, the LIC parameters can be derived using multiple templates, such as above, or left, or both above and left neighbouring reconstructed samples.

4. Detailed solutions

[0338] The detailed embodiments below should be considered as examples to explain general concepts. These embodiments should not be interpreted in a narrow way. Furthermore, these embodiments can be combined in any manner.

[0339] In this disclosure, Local illumination compensation (LIC) may not be limited to the current LIC technology. LIC may refer to an inter prediction technique to model local illumination variation between current block and its prediction block as a function of that between current block template and reference block template. The parameters of the function may be denoted by a linear equation (e.g., $\alpha * p[x] + \beta$) or a non-linear equation.

[0340] Fig. 46A-Fig. 46E illustrate template used to derive the parameters of the filter, respectively.

[0341] Fig. 47A-Fig. 47F illustrate combined template used to derive the parameters of the filter, respectively.

[0342] Fig. 48 illustrates a L-shape template used to derive the parameters of the filter.

[0343] Fig. 49 illustrates a non-adjacent template used to derive the parameters of the filter.

[0344] Fig. 50 illustrates samples of a specific line used to derive LIC parameters.

[0345] Fig. 51 illustrates samples of a specific line used to derive LIC parameters including left-above samples.

LIC with multiple templates

1. Instead of using the reconstructed samples of one neighbouring row and/or column adjacent to the current block to derive LIC parameters, it is proposed to use at least one reconstructed sample out of the one neighbouring row and/or column reconstructed

sample adjacent to the current block to derive LIC parameters. It is also proposed to exclude at least one reconstructed sample of the one neighbouring row and/or column adjacent to the current block to derive LIC parameters. Denote the proposed coding mode as LIC_MTPL.

- a. In one example, the template may consist of neighbouring reconstructed samples. Denote the template width and template height as W^T and H^T .
 - i. In one example, only samples of the above template may be used. An example is shown in Fig. 46A .
 - ii. In one example, only samples of the left template may be used. An example is shown in Fig. 46B.
 - iii. In one example, only samples of the left-bottom template may be used. An example is shown in Fig. 46C.
 - iv. In one example, only samples of the right-above template may be used. An example is shown in Fig. 46D.
 - v. In one example, only samples of the left-above template may be used. An example is shown in Fig. 46E.
 - vi. In one example, more than one of above/left/right-above/left-bottom/left-above template may be combined into one template and used. An example is shown in Fig. 47A- Fig. 47F .
 - vii. In one example, L-shape template may be used. An example is shown in Fig. 48.
 - viii. In one example, a non-adjacent template may be used. An example is shown in Fig. 49.
 - ix. In one example, $W^T = 1$ or $H^T = 1$.
 - x. In one example, the template may consist of more than one rows and/or columns.
 - xi. In one example, samples of the template may be in the same CTU/CTU row/tile/slice as the current video unit.
 - 1) In one example, samples of the template in a different CTU/CTU row/tile/slice from the current video unit may be excluded when deriving LIC parameters.
- b. In one example, the template may consist of the neighbouring reconstructed samples of a specific row and/or column except for the nearest row and/or column (line 0). Denote the specific row and/or column as line X.
 - i. In one example, X is an integer larger than 0. An example is shown in Fig. 50.
 - ii. In one example, left-above samples of the specific line may be used. An example is shown in Fig. 51.
 - iii. In one example, samples of more than one lines may be used.

- iv. In one example, the specific row and/or column may be in the same CTU/CTU row/tile/slice as the current video unit.
- c. In one example, when above (left) samples are not available, the left (above) samples may be used to derive LIC parameters.
 - i. In one example, a sample is “unavailable” if it is located in a different video processing unit (e.g., out of the current picture, or current sub-picture, or current tile, or current slice, or current brick, or current CTU, or current processing unit, or any other current video unit) or not reconstructed or cross-filtering video processing unit is disallowed.
 - ii. Alternatively, LIC_MTPL may be not used.
- d. In one example, the derivation of LIC parameters used in LIC_MTPL may be same as LIC.
 - i. Alternatively, the derivation of LIC parameters used in LIC_MTPL may be different from LIC.
- e. In one example, the determination of the template that is used to derive LIC parameters may be signalled.
 - i. In one example, one or more syntax elements may be used to indicate which template is used to derive LIC parameters.
 - 1) In one example, the index of the template may be signalled.
 - a) In one example, the index of the template may refer to the index of the line number.
 - b) In one example, the index of the template may refer to a specific pre-defined template, such as left template, or above template, or left-above template.
 - 2) In one example, the syntax elements may be signalled conditionally, where the condition may include:
 - a) Whether LIC is allowed to be used.
 - b) Whether LIC is used.
 - c) Whether one or more template samples are available.
 - ii. In one example, reordering may be used in the determination of the template.
 - 1) In one example, a set of templates is pre-defined/signalled/derived and reordering is performed for the set of templates, and the indication of the first M templates after reordering may be signalled.
 - 2) In one example, template matching cost and/or bilateral matching cost may be used in the reordering.
 - a) In one example, different LIC parameters are derived using different templates and the prediction of the current template is

refined using the LIC parameters and used to calculate the TM costs.

- i. In one example, the current template may refer to the neighbouring reconstructed samples.
 - ii. In one example, the current template may be different from the template used to derive LIC parameters. For example, it can be non-adjacent neighboring samples.
 - f. In one example, the determination of template that is used to derive the LIC parameters may be derived.
 - i. In one example, TM cost and/or BM cost may be used to determine the template.
 - ii. In one example, the above reordering method may be used, and the best template is determined as the template with the best cost (e.g., the lowest TM cost).
2. In one example, LIC_MTPL may be applied to AMVP mode.
 - a. The AMVP mode may be regular AMVP mode, affine AMVP mode, TM AMVP mode, or IBC AMVP mode.
 - b. In one example, LIC_MTPL may be not used for TM AMVP mode.
 - i. In one example, LIC_MTPL may be not used to construct the TM based AMVP candidate list construction.
 - c. In one example, LIC_MTPL may be used for TM AMVP mode but not used to construct the TM based AMVP candidate list.
3. In one example, LIC_MTPL may be applied to merge mode.
 - a. The merge mode may be regular merge mode, sub-block-based merge mode, TM merge mode, affine merge mode, MMVD, or IBC merge mode.
 - b. In one example, the template or the indication of the template may be inherited for merge mode, which is used to derive the LIC parameters.
 - c. Alternatively, the template or the indication of the template may be signalled for merge mode.
 - d. Alternatively, the template or the indication of the template may be derived for merge mode.
 - e. In one example, LIC_MTPL may be applied to ARMC process.
 - i. In one example, LIC_MTPL may be applied to ARMC process of a specific coding tool.
 - ii. Alternatively, LIC_MTPL may be not applied to ARMC process.
4. In one example, the template and/or the index of the template used in the current video unit may be used by the following video units.

- a. In one example, the following video unit may refer to luma video unit and/or chroma video unit.
- 5. In one example, LIC_MTPL may be applied to a specific coding tool.
 - a. In one example, the coding tool may refer to inter coding tool.
 - i. In one example, inter coding tool may refer to a specific coding tool such as CIIP (e.g., CIIP-Planar, CIIP-TIMD, CIIP-TM), BCW (e.g., BCW index derived by TM), MMVD (e.g., MMVD or TM based reordering for MMVD), template matching (TM), affine (e.g., affine-MMVD, TM based reordering for affine MMVD), DMVR/multi-pass DMVR, PROF, BDOF/sample based BDOF, adaptive decoder-side motion vector refinement (ADMVR), OBMC or TM based OBMC, MHP, GPM (e.g., GPM, GPM-TM, GPM-MMVD, GPM-intra), bilateral/template matching AMVP-merge mode, or their variants.
 - ii. Alternatively, LIC_MTPL may be not used for one or more above inter coding tools.
 - 1) In one example, LIC_MTPL may be not used for Affine mode.
 - 2) In one example, LIC_MTPL may be not used for AMVP-merge mode.
 - 3) In one example, LIC_MTPL may be not used for BCW.
 - 4) In one example, LIC_MTPL may be not used for AMVR.
 - a) Alternatively, LIC_MTPL may be not used for a specific motion vector resolution.
 - 5) In one example, LIC_MTPL may be not used for SMVD.
 - b. In one example, LIC_MTPL may be applied to uni-prediction and bi-prediction.
 - i. Alternatively, LIC_MTPL may be applied to uni-prediction or bi-prediction.
- 6. In one example, LIC may be not applied to a specific coding tool.
 - a. In one example, the coding tool may refer to SMVD.
- 7. Whether to and/or how to apply LIC_MTPL may depend on coding information, the coding information may refer to:
 - a. whether a specific coding method is allowed, such as LIC.
 - b. block dimensions and/or block size.
 - i. In one example, the block is allowed to be coded with LIC_MTPL when the block size ($W \times H$) is less than or equal to a threshold ($T1$), wherein W and H denotes block width and block height, respectively.
 - 1) In one example, $T1 = 64$, or 128 , or 256 , or 512 , or 1024 , or 2048 , or 4096 .

- ii. In one example, the block is allowed to be coded with LIC_MTPL when the block size ($W \times H$) is larger than or equal to a threshold (T_2), wherein W and H denotes block width and block height, respectively.
 - 1) In one example, $T_2 = 8$, or 16, or 32, or 64, or 128, or 256, or 512.
- iii. In one example, the block is allowed to be coded with LIC_MTPL when W is larger than or equal to a threshold (T_3) and/or H is larger than or equal to a threshold (T_4).
 - 1) In one example, $T_3 = 4/8/16/32/64$.
 - 2) In one example, $T_4 = 4/8/16/32/64$.
- iv. In one example, the block is allowed to be coded with LIC_MTPL when W is less than or equal to a threshold (T_5) and/or H is less than or equal to a threshold (T_6).
 - 1) In one example, $T_5 = 8/16/32/64/128$.
 - 2) In one example, $T_6 = 8/16/32/64/128$.
- v. In one example, the left template may be not allowed to be used when W is larger than or equal to $S \times H$, and/or the above template may be not allowed to be used when H is larger than or equal to $S \times W$.
 - 1) In one example, the above template may be not allowed to be used when W is larger than or equal to $S \times H$, and/or the left template may be not allowed to be used when H is larger than or equal to $S \times W$.
- vi. In one example, the block size may refer to luma block size.
- vii. In one example, the block size may refer to chroma block size.
- c. block depth.
- d. slice/picture type and/or partition tree type (single, or dual tree, or local dual tree).
- e. temporal layer identification.
- f. block location.
 - i. In one example, when the current video unit locates at the left boundary of current picture, the left template is not used.
 - ii. In one example, when the current video unit locates at the above boundary of current picture, and/or the above boundary of current CTU, the above template is not used.
- g. colour format.
- h. colour component.
 - i. In one example, LIC_MTPL may apply to all colour components.
 - ii. In one example, when LIC_MTPL is applied to chroma components, it may be different from that for luma component.
 - iii. In one example, whether to and/or how to apply LIC_MTPL to a first component may depend on whether to and/or how to apply LIC_MTPL to a second component.

- 1) In one example, the first component may refer to chroma component (e.g., Cb and/or Cr), and the second component may refer to luma component (e.g., Y).
- 2) In one example, the way to apply LIC_MTPL to the first component may be same as the second component.
 - a) Alternatively, the way to apply LIC_MTPL to the first component may be different from the second component.
- 3) In one example, the template and/or the indication of the template of the current chroma block may be inherited from luma block.
- iv. In one example, LIC_MTPL may be applied to luma component, but not to chroma components.
 - 1) In one example, luma component may refer to Y in YCbCr colour space or G in RGB colour space.
 - 2) In one example, chroma components may refer to Cb and/or Cr in YCbCr colour space or R and/or B in RGB colour space.
8. Indication of LIC_MTPL may be conditionally signalled wherein the condition may include:
 - a. block dimensions and/or block size.
 - i. In one example, the indication of the LIC_MTPL may be not signalled when the block size ($W \times H$) is less than or equal to a threshold ($T7$), wherein W and H denotes block width and block height, respectively.
 - 1) In one example, $T7 = 64$, or 128 , or 256 , or 512 , or 1024 , or 2048 , or 4096 .
 - ii. In one example, the indication of the LIC_MTPL may be not signalled when the block size ($W \times H$) is less than or equal to a threshold ($T8$), wherein W and H denotes block width and block height, respectively.
 - 1) In one example, $T8 = 16$, or 32 , or 64 , or 128 , or 256 , or 512 .
 - iii. In one example, the indication of LIC_MTPL may be signalled when W is larger than or equal to a threshold ($T9$) and/or H is larger than or equal to a threshold ($T10$).
 - 1) In one example, $T9 = 4/8/16/32/64$.
 - 2) In one example, $T10 = 4/8/16/32/64$.
 - iv. In one example, the indication of LIC_MTPL may be signalled when W is less than or equal to a threshold ($T11$) and/or H is less than or equal to a threshold ($T12$).
 - 1) In one example, $T11 = 8/16/32/64/128$.
 - 2) In one example, $T12 = 8/16/32/64/128$.
 - v. In one example, the above thresholds (e.g., $T7/T8/T9/T10/T11/T12$) may depend on slice/picture type.

- vi. In one example, the block size may refer to luma block size.
- vii. In one example, the block size may refer to chroma block size.
- b. block depth.
- c. slice/picture type and/or partition tree type (single, or dual tree, or local dual tree).
- d. temporal layer identification.
- e. block location.
 - i. In one example, when the current video unit locates at the left boundary of current picture, the indication of LIC_MTPL may be not signalled.
 - ii. In one example, when the current video unit locates at the above boundary of current picture, and/or the above boundary of current CTU, the indication of LIC_MTPL may be not signalled.
- f. colour format.
- g. colour component.
- 9. Whether current block is coded with LIC_MTPL mode may be signalled using one or more syntax elements (SE).
 - a. In one example, the syntax element may be binarized with fixed length coding, or truncated unary coding, or unary coding, or EG coding, or coded a flag.
 - b. In one example, the syntax element may be bypass coded or context coded.
 - i. The context may depend on coded information, such as block dimensions, and/or block size, and/or slice/picture types, and/or the information of neighbouring blocks (adjacent or non-adjacent), and/or the information of other coding tools used for current block, and/or the information of temporal layer.
 - c. In one example, the one or more syntax elements may be signalled at sequence header/picture header/SPS/VPS/DPS/DCI/PPS/APS/slice header/tile group header.
 - d. In one example, the syntax element may be coded in a predictive way.
 - i. For example, the syntax element of the current block may be predicted by that of a neighboring block.
 - e. In one example, the syntax element may be coded conditionally.
 - i. For example, only if a first SE indicates that LIC_MTPL is applicable, a second SE may be signaled to indicate whether LIC_MTPL is used.
 - 1) The first SE may be at sequence header/picture header/SPS/VPS/DPS/DCI/PPS/APS/slice header/tile group header.
 - 2) The second SE may be for a block.
 - ii. For example, only if the second SE indicates LIC_MTPL is used, a third SE may be signaled to indicate how to perform LIC_MTPL.

10. The disclosed methods above may apply to other coding tools, such as variants of LIC (e.g., LIC with filtering which uses more than 2 parameters), or other coding tools which using the neighbouring samples to derive one or more parameters.

LIC with multiple models

11. It is proposed that more than one LIC models may be used for the current video unit. Denote the coding mode as LIC_MM.

- a. In one example, the neighbouring reconstructed samples are divided into N sets, and the parameters of each LIC model may be derived from each set, wherein N is an integer larger than 1.
 - i. In one example, how to divide the sets may depend on coding information.
 - 1) In one example, the sample value may be used to divide the sets.
 - a) In one example, one or more thresholds depending on sample value may be used.
 - i. In one example, when N is equal to 2, a threshold (T1) is used to divide the samples into two sets.
 1. In one example, T1 may be equal to the mean of all samples.
 - ii. In one example, when N is equal to 3, two thresholds (T1 and T2) are used to divide the samples into three sets.
 - iii. In one example, the thresholds may depend on colour components and/or colour format.
 - b) In one example, how to divide the sets may depend on block size/dimensions.
 - ii. In one example, how to divide the sets may be signalled.
 - b. In one example, LIC_MM may be applied to a specific coding tool.
 - i. In one example, the coding tool may refer to inter coding tool.
 - 1) In one example, inter coding tool may refer to a specific coding tool such as CIIP (e.g., CIIP-Planar, CIIP-TIMD, CIIP-TM), BCW (e.g., BCW index derived by TM), MMVD (e.g., MMVD or TM based reordering for MMVD), template matching (TM), affine (e.g., affine-MMVD, TM based reordering for affine MMVD), DMVR/multi-pass DMVR, PROF, BDOF/sample based BDOF, adaptive decoder-side motion vector refinement (ADMVR), OBMC or TM based OBMC, MHP, GPM (e.g., GPM, GPM-TM, GPM-MMVD, GPM-intra), bilateral/template matching AMVP-merge mode, or their variants.

- 2) Alternatively, LIC_MM may be not used for one or more above inter coding tools.
 - a) In one example, LIC_MM may be not used for Affine mode.
 - b) In one example, LIC_MM may be not used for AMVP-merge mode.
 - c) In one example, LIC_MM may be not used for BCW.
 - d) In one example, LIC_MM may be not used for AMVR.
 - i. Alternatively, LIC_MM may be not used for a specific motion vector resolution.
 - e) In one example, LIC_MM may be not used for SMVD.
 - ii. In one example, LIC_MM may be applied to uni-prediction and bi-prediction.
 - 1) Alternatively, LIC_MM may be applied to uni-prediction or bi-prediction.
12. Whether to and/or how to apply LIC_MM may depend on coding information, the coding information may refer to:
- a. whether a specific coding method is allowed, such as LIC.
 - b. block dimensions and/or block size.
 - c. block depth.
 - d. slice/picture type and/or partition tree type (single, or dual tree, or local dual tree).
 - e. temporal layer identification.
 - f. block location.
 - g. colour format.
 - h. colour component.
13. Indication of LIC_MM may be conditionally signalled wherein the condition may include:
- a. block dimensions and/or block size.
 - b. block depth.
 - c. slice/picture type and/or partition tree type (single, or dual tree, or local dual tree).
 - d. temporal layer identification.
 - e. block location.
 - f. colour format.
 - g. colour component.
14. Whether current block is coded with LIC_MM mode may be signalled using one or more syntax elements.
- a. In one example, the syntax element may be binarized with fixed length coding, or truncated unary coding, or unary coding, or EG coding, or coded a flag.
 - b. In one example, the syntax element may be bypass coded or context coded.

- i. The context may depend on coded information, such as block dimensions, and/or block size, and/or slice/picture types, and/or the information of neighbouring blocks (adjacent or non-adjacent), and/or the information of other coding tools used for current block, and/or the information of temporal layer.
- c. In one example, the one or more syntax elements may be signalled at sequence header/picture header/SPS/VPS/DPS/DCI/PPS/APS/slice header/tile group header.
- d. In one example, the syntax element may be coded in a predictive way.
 - i. For example, the syntax element of the current block may be predicted by that of a neighboring block.

General aspects

15. In above examples, the video unit may refer to the video unit may refer to colour component/sub-picture/slice/tile/coding tree unit (CTU)/CTU row/groups of CTU/coding unit (CU)/prediction unit (PU)/transform unit (TU)/ coding tree block (CTB)/ coding block (CB)/prediction block(PB)/transform block (TB)/a block/sub-block of a block/sub-region within a block/ any other region that contains more than one sample or pixel.
16. Whether to and/or how to apply the disclosed methods above may be signalled at sequence level/group of pictures level/picture level/slice level/tile group level, such as in sequence header/picture header/SPS/VPS/DPS/DCI/PPS/APS/slice header/tile group header.
17. Whether and/or how to apply the above methods may depend on the following information:
 - a. A message signalled in the DPS/SPS/VPS/PPS/APS/picture header/slice header/tile group header/coding tree unit (CTU)/Coding unit (CU)/CTU row/group of CTUs/TU/PU block/Video coding unit.
 - b. Position of CU/PU/TU/block/Video coding unit.
 - c. Block dimension of current block and/or its neighbouring blocks.
 - d. Block shape of current block and/or its neighbouring blocks.
 - e. coded mode of a block, e.g., IBC or non-IBC inter mode or non-IBC subblock mode.
 - f. Indication of the colour format (such as 4:2:0, 4:4:4).
 - g. Coding tree structure.
 - h. Slice/tile group type and/or picture type.
 - i. Colour component (e.g., may be only applied on chroma components or luma component).
 - j. Temporal layer ID.

- k. Profiles/Levels/Tiers of a standard.
- 18. A syntax element disclosed above may be binarized as a flag, a fixed length code, an EG(x) code, a unary code, a truncated unary code, a truncated binary code, etc. It can be signed or unsigned.
- 19. A syntax element disclosed above may be coded with at least one context model. Or it may be bypass coded.
- 20. A syntax element disclosed above may be signaled in a conditional way.
 - a. The SE is signaled only if the corresponding function is applicable.
 - b. The SE is signaled only if the dimensions (width and/or height) of the block satisfy a condition.
- 21. A syntax element disclosed above may be signaled at block level/ sequence level/group of pictures level/picture level/slice level/tile group level, such as in coding structures of CTU/CU/TU/PU/CTB/CB/TB/PB, or sequence header/picture header/SPS/VPS/DPS/DCI/PPS/APS/slice header/tile group header.
- 22. The proposed method(s) may be combined with another coding tool such as affine/MTS/LFNST/MMVD/MIP/ISP/CCLM/CCCM/SMVD/BDOF/DMVR/HMVP/T emplate Matching/IBC/Palette/etc.
- 23. The proposed method(s) may be excluded with another coding tool such as affine/MTS/LFNST/MMVD/MIP/ISP/CCLM/CCCM/SMVD/BDOF/DMVR/HMVP/T emplate Matching/IBC/Palette/etc.
 - a. In one example, if the proposed method(s) is used, the excluded coding tool is disabled implicitly without signaling.
- 24. In one example, if the excluded coding tool is used, the proposed method(s) is disabled implicitly without signaling.

[0346] Fig. 52 illustrates a flowchart of a method 5200 for video processing in accordance with embodiments of the present disclosure. The method 5200 is implemented during a conversion between a video unit of a video and a bitstream of the video.

[0347] At block 5210, for a conversion between a video unit of a video and a bitstream of the video, a local illumination compensation (LIC) parameter is derived using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, when the video unit is coded with a first target coding mode.

[0348] At block 5220, the conversion is performed based on the LIC parameter. In some

embodiments, the conversion may comprise encoding the video unit into the bitstream. Alternatively, the conversion may comprise decoding the video unit from the bitstream.

[0349] The method 5200 enables the diversity of derived parameters to be increased to improve the coding performance of LIC.

[0350] In some embodiments, at least one of: a reconstructed sample of the neighbouring row or a reconstructed sample of a column adjacent to the current block may be excluded to derive the LIC parameter. In some embodiments, a template may comprise one or more neighbouring reconstructed samples. In this case, W^T represents a width of the template and H^T represents a height of the template. In some embodiments, a sample of an above template may be used to derive the LIC parameters. In some other embodiments, a sample of a left template may be used to derive the LIC parameters. Alternatively, a sample of a left-bottom template may be used to derive the LIC parameters. In some embodiments, a sample of a right-above template may be used to derive the LIC parameters. In some other embodiments, a sample of a left-above template may be used to derive the LIC parameters.

[0351] In some embodiments, a plurality of the following templates: above template, left template, right-above template, left-bottom template, and left-above template are combined into one template that may be used to derive the LIC parameter. In some embodiments, a L-shape template may be used. In some other embodiments, a non-adjacent template may be used. In some embodiments, at least one of: W^T or H^T may equal to 1. In some embodiments, the template may comprise a plurality of rows and/or a plurality of columns.

[0352] In some embodiments, samples of the template may be in the same coding tree unit (CTU), the same CTU row, the same CTU tile, or the same CTU slice of the video unit. In some other embodiments, samples of the template in a different CTU, a different CTU row, a different CTU tile, or a different CTU slice of the video unit may be excluded for deriving LIC parameters.

[0353] In some embodiments, the template may comprise at least one of: a neighbouring

reconstructed sample of a row except for a nearest row, or a neighbouring reconstructed sample of a column except for a nearest column. In this case, line X represents at least one of: the row, or the column. In some embodiments, X may be an integer larger than 0. In some embodiments, a left-above sample of the line X may be used.

[0354] In some embodiments, samples of a plurality of rows or columns may be used. In some embodiments, at least one of: the row or the column may be in the same CTU, the same CTU row, the same CTU tile, or the same CTU slice of the video unit.

[0355] In some embodiments, if above samples are not available, a left sample may be used to derive the LIC parameter. Alternatively, if left samples are not available, an above sample may be used to derive the LIC parameter. In some embodiments, if a sample is located in a different video unit or the sample is not reconstructed or a cross-filtering video unit is disallowed, the sample may be not available. For example, if the sample is out of at least one of: a current picture, a current sub-picture, a current tile, a current slice, a current brick, a current CTU, a current processing unit, or any other current video unit, the sample may be located in the different video unit. In some other embodiments, the first target coding mode may be not used.

[0356] In some embodiments, a determination of a template may be signalled. In this case, the template is used to derive the LIC parameter. For example, one or more syntax elements may be used to indicate which template is used to derive the LIC parameter. In some embodiments, an index of the template may be signalled. In some embodiments, the index of the template may comprise an index of a line number. Alternatively, the index of the template may comprise a predefined template. For example, the predefined template may comprise at least one of: a left template, an above template, or a left-above template.

[0357] In some embodiments, the one or more syntax elements may be signalled if a condition is satisfied. For example, the condition may comprise at least one of: whether LIC is allowed to be used, whether LIC is used, or whether one or more template samples are available.

[0358] In some embodiments, reordering may be used in the determination of the template. In some embodiments, if a set of templates are at least one of: predefined, signalled, or derived, and the reordering is performed for the set of templates, an indication of first M templates after reordering may be signalled. In this case, M is an integer number. In some other embodiments, at least one of: template matching cost, or bilateral matching cost may be used in the reordering. For example, different LIC parameters may be derived using different templates. In this case, a prediction of a current template is refined using the LIC parameter and used to calculate template matching (TM) cost. In some embodiments, a current template may comprise a neighbouring reconstructed sample. In some other embodiments, a current template may be different from a template that is used to derive the LIC parameter. Alternatively, the template used to derive LIC parameter is a non-adjacent neighboring sample.

[0359] In some embodiments, a determination of a template may be derived. In this case, the template is used to derive the LIC parameter. For example, at least one of: TM cost, or bilateral matching (BM) cost may be used to determine the template. In some embodiments, a reordering approach may be used, and a best template may be determined as a template with best cost. For example, the best cost may comprise a lowest TM cost.

[0360] In some embodiments, the first target coding mode may be applied to advanced motion vector prediction (AMVP) mode. In some examples, the AMVP mode may comprise at least one of: regular AMVP mode, affine AMVP mode, TM AMVP mode, or intra block copy (IBC) AMVP mode. In some embodiments, the first target coding mode may be not used for TM AMVP mode. For example, the first target coding mode may be not used to construct a TM based AMVP candidate list construction. In some other embodiments, the first target coding mode may be used for TM AMVP mode but the first target coding mode may be not used to construct a TM based AMVP candidate list.

[0361] In some embodiments, the first target coding mode may be applied to merge mode. For example, the merge mode may comprise at least one of: regular merge mode, sub-

block-based merge mode, TM merge mode, affine merge mode, merge mode with motion vector differences (MMVD), or IBC merge mode. In some examples, at least one of: a template, or an indication of the template may be inherited for the merge mode. In this case, at least one of: the template, or the indication of the template may be used to derive the LIC parameter. In some other embodiments, at least one of: a template, or an indication of the template may be signalled for the merge mode. Alternatively, at least one of: a template, or an indication of the template may be derived for the merge mode.

[0362] In some embodiments, the first target coding mode may be applied to an adaptive reordering of merge candidates (ARMC) process. For example, the first target coding mode may be applied to an ARMC process of a coding tool. In some other embodiments, the first target coding mode may be not applied to an ARMC process.

[0363] In some embodiments, a derivation of the LIC parameter used in the first target coding mode may be same as a derivation of the LIC parameter used in LIC. In some embodiments, a derivation of the LIC parameter used in the first target coding mode may be different from a derivation of the LIC parameter used in LIC.

[0364] In some embodiments, at least one of: a template used in a current video unit, or an index of the template used in a current video unit may be used by a following video unit. For example, the following video unit may comprise at least one of: a luma video unit, or a chroma video unit.

[0365] In some embodiments, the first target coding mode may be applied to a coding tool. For example, the coding tool may comprise an inter coding tool. In some examples, the inter coding tool may comprise a specific coding tool. In this case, the specific coding tool may be at least one of: combined inter and intra prediction (CIIP), bi-prediction with CU-level weight (BCW), merge mode with motion vector difference (MMVD), template matching (TM), affine, decoder side motion vector refinement (DMVR), multi-pass DMVR, prediction refinement with optical flow (PROF), bi-directional optical flow (BDOF), sample based BDOF, adaptive decoder-side motion vector refinement

(ADMVR), overlapped block motion compensation (OBMC), TM based OBMC, multi-hypothesis prediction (MHP), geometric partitioning mode (GPM), bilateral matching advanced motion vector prediction (AMVP)-merge mode, template matching AMVP-merge mode, a variant of CIIP, a variant of BCW, a variant of MMVD, a variant of TM, a variant of affine, a variant of DMVR, a variant of multi-pass DMVR, a variant of PROF, a variant of BDOF, a variant of sample based BDOF, a variant of ADMVR, a variant of OBMC, a variant of TM based OBMC, a variant of MHP, a variant of GPM, a variant of bilateral matching AMVP-merge mode, or a variant of template matching AMVP-merge mode. In some embodiments, the CIIP may be at least one of: CIIP-Planar, CIIP-TIMD, CIIP-TM. In some embodiments, the BCW may be BCW index derived by TM. In other some embodiments, the MMVD may be at least one of: MMVD or TM based reordering for MMVD. Alternatively, the affine may be at least one of: affine-MMVD, TM based reordering for affine MMVD. In some other embodiments, the GPM may be at least one of: GPM, GPM-TM, GPM-MMVD, or GPM-intra.

[0366] In some embodiments, the first target coding mode may be not used for one or more inter coding tools. In some embodiments, the first target coding mode may be not used for an affine mode. In some other embodiments, the first target coding mode may be not used for an AMVP-merge mode. Alternatively, the first target coding mode may be not used for a BCW. In some embodiments, the first target coding mode may be not used for an AMVR. In some other embodiments, the first target coding mode may be not used for a motion vector resolution. Alternatively, the first target coding mode may be not used for symmetric motion vector differences (SMVD).

[0367] In some embodiments, the first target coding mode may be applied to at least one of: uni-prediction, or bi-prediction. In some embodiments, LIC may be not applied to a coding tool. In some embodiments, the coding tool may comprise SMVD.

[0368] In some embodiments, an indication of whether to and/or how to apply the first target coding mode may depend on coding information. For example, the coding

information may comprise at least one of the followings: an indication of whether a coding approach is allowed, a block dimension, a block size, a block depth, a slice type, a picture type, a partition tree type, a temporal layer identification, a block location, colour format, or a colour component.

[0369] In some embodiments, the coding approach may comprise LIC. In some embodiments, if a block size of a block is less than or equals to a first threshold, the block may be allowed to be coded with the first target coding mode. In some examples, the first threshold may equal to one of: 64, 128, 256, 512, 1024, 2048, or 4096. In some other embodiments, if a block size of a block is larger than or equals to a second threshold, the block may be allowed to be coded with the first target coding mode. For example, the second threshold may equal to one of: 8, 16, 32, 64, 128, 256, or 512.

[0370] In some embodiments, if at least one of: a block width of a block is larger than or equals to a third threshold, or a block height of the block is larger than or equals to a fourth threshold, the block may be allowed to be coded with the first target coding mode. In some examples, the third threshold may equal to one of: 4, 8, 16, 32, or 64. In some other examples, the fourth threshold may equal to one of: 4, 8, 16, 32, or 64.

[0371] In some embodiments, if at least one of: a block width of a block is less than or equals to a fifth threshold, or a block height of the block is less than or equals to a sixth threshold, the block may be allowed to be coded with the first target coding mode. In some examples, the fifth threshold may equal to one of: 8, 16, 32, 64, or 128. In some other examples, the sixth threshold may equal to one of: 8, 16, 32, 64, or 128.

[0372] In some embodiments, at least one of: if W is larger than or equals to $S \cdot H$, a left template may be not allowed to be used, or if H is larger than or equals to $S \cdot W$, an above template may be not allowed to be used. In this case, W represents a block width of a block, H represents a block height of a block, S represents a scale ratio. For example, the scale ratio may be one of: 2, 4, or 8.

[0373] In some other embodiments, at least one of: if W is larger than or equals to $S \cdot H$,

an above template may be not allowed to be used, or if H is larger than or equals to $S \cdot W$, a left template may be not allowed to be used. In this case, W represents a block width of a block, H represents a block height of a block, S a scale ratio. For example, the scale ratio may be one of: 2, 4, or 8.

[0374] In some embodiments, the block size may comprise a luma block size. In some other embodiments, the block size may comprise a chroma block size. In some embodiments, the partition tree type may comprises at least one of: single tree, dual tree, or local dual tree. In some other embodiments, if a current video unit is at a left boundary of a current picture, a left template may be not used. Alternatively, if at least one of: a current video unit is at an above boundary of current picture, or the current video is at an above boundary of a current CTU, an above template may be not used.

[0375] In some embodiments, the first target coding mode may be applied to all colour components. In some embodiments, if the first target coding mode is applied to a chroma component, the first target coding mode may be different from a first target coding mode applied to a luma component.

[0376] In some embodiments, an indication of whether to and/or how to apply the first target coding mode to a first component may depend on an indication of whether to and/or how to apply the first target coding mode to a second component. In some examples, the first component may comprise a chroma component, and the second component may comprise a luma component. In some other examples, the chroma component may comprise at least one of: Cb, or Cr. Alternatively, the luma component may comprises Y.

[0377] In some embodiments, a way to apply the first target coding mode to the first component may be same as a way to apply the first target coding mode to the second component. In some other embodiments, a way to apply the first target coding mode to the first component may be different from a way to apply the first target coding mode to the second component. Alternatively, at least one of: a template of a current chroma block, or an indication of the template of the current chroma block may be inherited from a luma

block.

[0378] In some embodiments, the first target coding mode may be applied to a luma component, but the first target coding mode may not be applied to a chroma component. For example, the luma component may comprise at least one of: Y in YCbCr colour space, or G in RGB colour space. In some other embodiments, the chroma component may comprise at least one of: Cb in YCbCr colour space, Cr in YCbCr colour space, R in RGB colour space, or B in RGB colour space.

[0379] In some embodiments, if a condition is satisfied, an indication of the first target coding mode may be signalled. In some embodiments, the condition may comprise at least one of the followings: a block dimension, a block size, a block depth, a slice type, a picture type, a partition tree type, a temporal layer identification, a block location, colour format, or a colour component.

[0380] In some embodiments, if a block size of a block is less than or equals to a seventh threshold, the indication of the first target coding mode may be not signalled. In some examples, the seventh threshold may equal to one of: 64, 128, 256, 512, 1024, 2048, or 4096. In some other embodiments, if a block size of a block is less than or equals to an eighth threshold, the indication of the first target coding mode may be not signalled. In some examples, the eighth threshold may equal to one of: 16, 32, 64, 128, 256, or 512.

[0381] In some embodiments, if at least one of: a block width of a block is larger than or equals to a ninth threshold, or a block height of the block is larger than or equals to a tenth threshold, the indication of the first target coding mode may be signalled. In some examples, the ninth threshold may equal to one of: 4, 8, 16, 32, or 64. In some other examples, the tenth threshold may equal to one of: 4, 8, 16, 32, or 64.

[0382] In some embodiments, if at least one of: a block width of a block is less than or equals to an eleventh threshold, or a block height of the block is less than or equals to a twelfth threshold, the indication of the first target coding mode may be signalled. For example, the eleventh threshold may equal to one of: 8, 16, 32, 64, or 128. In some other

examples, the twelfth threshold may equal to one of: 8, 16, 32, 64, or 128.

[0383] In some embodiments, at least one of thresholds: the seventh threshold, the eighth threshold, the ninth threshold, the tenth threshold, the eleventh threshold, or the twelfth threshold may depend on at least one of: slice type, or picture type. For example, the block size may comprise a luma block size. In some examples, the block size may comprise a chroma block size.

[0384] In some embodiments, the partition tree type may comprise at least one of: single tree, dual tree, or local dual tree. In some embodiments, if a current video unit is at the left boundary of a current picture, the indication of the first target coding mode may be not signalled. In some other embodiments, if at least one of: a current video unit is at the above boundary of current picture, or the current video is at an above boundary of a current CTU, the indication of the first target coding mode may be not signalled.

[0385] In some embodiments, an indication of whether a current block is coded with the first target coding mode may be signalled using one or more syntax elements. In some embodiments, the one or more syntax elements may be binarized with at least one of: fixed length coding, truncated unary coding, unary coding, EG coding, or coded a flag.

[0386] In some embodiments, the one or more syntax elements may be bypass coded or context coded. In some examples, context of the one or more syntax elements may depend on coded information. For example, the coded information may comprise at least one of the followings: a block dimension, a block size, a slice type, a picture type, information of a neighbouring block, information of another coding tool used for the current block, or information of a temporal layer. In some examples, the neighbouring block may be adjacent or non-adjacent.

[0387] In some embodiments, the one or more syntax elements may be signalled at one of the followings: a sequence header, a picture header, a sequence parameter set (SPS), a video parameter set (VPS), a dependency parameter set (DPS), a decoding capability information (DCI), a picture parameter set (PPS), an adaptation parameter sets (APS), a

slice header, or a tile group header. In some embodiments, the one or more syntax elements may be coded in a predictive way. For example, one or more syntax elements of the current block may be predicted by one or more syntax elements of a neighboring block.

[0388] In some embodiments, the one or more syntax elements may be coded if a condition is satisfied. In some examples, if a first syntax element indicates that the first target coding mode is applicable, a second syntax element may be signalled to indicate whether the first target coding mode is used. For example, the first syntax element may be signalled at one of the followings: a sequence header, a picture header, a sequence parameter set (SPS), a video parameter set (VPS), a dependency parameter set (DPS), a decoding capability information (DCI), a picture parameter set (PPS), an adaptation parameter sets (APS), a slice header, or a tile group header. In some embodiments, the second syntax element may be for a block. Alternatively, if a second syntax element indicates that the first target coding mode is used, a third syntax element may be signalled to indicate a way of performing the first target coding mode.

[0389] In some embodiments, the first target coding mode may be applied to another coding tool. For example, the other coding tool may comprise at least one of: a variant of LIC, or another coding tool using a neighbouring sample to derive one or more parameters. In some examples, the variant of LIC may comprise LIC with filtering which uses a plurality of parameters.

[0390] According to further embodiments of the present disclosure, a non-transitory computer-readable recording medium is provided. The non-transitory computer-readable recording medium stores a bitstream of a video which is generated by a method performed by an apparatus for video processing. The method comprises: deriving a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to a video unit of the video being coded with a first target coding mode; and generating the bitstream based on the LIC parameter.

[0391] According to still further embodiments of the present disclosure, a method for storing bitstream of a video is provided. The method comprises: deriving a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to a video unit of the video being coded with a first target coding mode; generating the bitstream based on the LIC parameter; and storing the bitstream in a non-transitory computer-readable recording medium.

[0392] Fig. 53 illustrates a flowchart of a method 5300 for video processing in accordance with embodiments of the present disclosure. The method 5300 is implemented during a conversion between a video unit of a video and a bitstream of the video.

[0393] At block 5310, for a conversion between a video unit of a video and a bitstream of the video, a plurality of local illumination compensation (LIC) models is applied to the video unit, when the video unit is coded with a second target coding mode.

[0394] At block 5320, the conversion is performed based on the plurality of LIC models. In some embodiments, the conversion may comprise encoding the video unit into the bitstream. Alternatively, the conversion may comprise decoding the video unit from the bitstream.

[0395] The method 5300 enables a plurality of LIC models to be applied to improve the coding performance of LIC.

[0396] In some embodiments, neighbouring reconstructed samples may be divided into N sets, and parameters of each LIC model are derived from each set. In this case, N is an integer larger than 1. In some embodiments, a way to divide the neighbouring reconstructed samples into the N sets may be based on coding information. In some examples, a sample value is used to divide the neighbouring reconstructed samples into the N sets. In some other embodiments, one or more thresholds based on the sample value may be used.

[0397] In some embodiments, if N equals to 2, a first threshold may be used to divide the neighbouring reconstructed samples into two sets. For example, the first threshold may equal to a mean of all the neighbouring reconstructed samples. In some other embodiments, if N equals to 3, a first threshold and a second threshold may be used to divide the neighbouring reconstructed samples into three sets. Alternatively, the one or more thresholds may be based on at least one of: a colour component, or colour format. In some embodiments, the way to divide the neighbouring reconstructed samples into the N sets may be based on at least one of: a block size, or a block dimension. Alternatively, a way to divide the neighbouring reconstructed samples into the N sets may be signalled.

[0398] In some embodiments, the second target coding mode may be applied to a coding tool. For example, the coding tool may comprise an inter coding tool. In some examples, the inter coding tool may comprise a specific coding tool. In this case, the specific coding tool is at least one of: combined inter and intra prediction (CIIP), bi-prediction with CU-level weight (BCW), merge mode with motion vector difference (MMVD), template matching (TM), affine, decoder side motion vector refinement (DMVR), multi-pass DMVR, prediction refinement with optical flow (PROF), bi-directional optical flow (BDOF), sample based BDOF, adaptive decoder-side motion vector refinement (ADMVR), overlapped block motion compensation (OBMC), TM based OBMC, multi-hypothesis prediction (MHP), geometric partitioning mode (GPM), bilateral matching advanced motion vector prediction (AMVP)-merge mode, template matching AMVP-merge mode, a variant of CIIP, a variant of BCW, a variant of MMVD, a variant of TM, a variant of affine, a variant of DMVR, a variant of multi-pass DMVR, a variant of PROF, a variant of BDOF, a variant of sample based BDOF, a variant of ADMVR, a variant of OBMC, a variant of TM based OBMC, a variant of MHP, a variant of GPM, a variant of bilateral matching AMVP-merge mode, or a variant of template matching AMVP-merge mode.

[0399] In some embodiments, the CIIP may be at least one of: CIIP-Planar, CIIP-TIMD, CIIP-TM. In some other embodiments, the BCW may be BCW index derived by TM. In

some embodiments, the MMVD may be at least one of: MMVD or TM based reordering for MMVD. Alternatively, the affine may be at least one of: affine-MMVD, TM based reordering for affine MMVD. In some other embodiments, the GPM may be at least one of: GPM, GPM-TM, GPM-MMVD, or GPM-intra.

[0400] In some embodiments, the second target coding mode may be not used for one or more inter coding tools. In some other embodiments, the second target coding mode may be not used for an affine mode. In some embodiments, the second target coding mode may be not used for an AMVP-merge mode. Alternatively, the second target coding mode may be not used for a BCW.

[0401] In some embodiments, the second target coding mode may be not used for an AMVR. In some embodiments, the second target coding mode may not be used for a motion vector resolution. In some other embodiments, the second target coding mode may not be used for symmetric motion vector differences (SMVD). Alternatively, the second target coding mode may be applied to at least one of: uni-prediction, or bi-prediction.

[0402] In some embodiments, an indication of whether to and/or how to apply the second target coding mode may depend on coding information. For example, the coding information may comprises at least one of the followings: an indication of whether a coding approach is allowed, a block dimension, a block size, a block depth, a slice type, a picture type, a partition tree type, a temporal layer identification, a block location, colour format, or a colour component. In some examples, the partition tree type may comprise at least one of: single tree, dual tree, or local dual tree.

[0403] In some embodiments, if a condition is satisfied, the second target coding mode may be signalled. For example, the condition may comprise at least one of the followings: a block dimension, a block size, a block depth, a slice type, a picture type, a partition tree type, a temporal layer identification, a block location, colour format, or a colour component. In some embodiments, the partition tree type may comprise at least one of: single tree, dual tree, or local dual tree.

[0404] In some embodiments, an indication of whether a current block may be coded with the second target coding mode is signalled using one or more syntax elements. In some examples, the one or more syntax elements may be binarized with at least one of: fixed length coding, truncated unary coding, unary coding, EG coding, or coded a flag. In some other examples, the one or more syntax elements may be bypass coded or context coded. In some embodiments, context of the one or more syntax elements may depend on coded information. For example, the coded information may comprise at least one of the followings: a block dimension, a block size, a slice type, a picture type, information of a neighbouring block, information of another coding tool used for the current block, or information of a temporal layer. In some examples, the neighbouring block may be adjacent or non-adjacent.

[0405] In some embodiments, the one or more syntax elements may be signalled at one of the followings: a sequence header, a picture header, a sequence parameter set (SPS), a video parameter set (VPS), a dependency parameter set (DPS), a decoding capability information (DCI), a picture parameter set (PPS), an adaptation parameter sets (APS), a slice header, or a tile group header. In some embodiments, the one or more syntax elements may be coded in a predictive way. For example, one or more syntax elements of the current block may be predicted by one or more syntax elements of a neighboring block.

[0406] In some embodiments, the video unit may comprise at least one of the followings: a colour component, a sub-picture, a slice, a tile, a coding tree unit (CTU), a CTU row, a group of CTU, a coding unit (CU), a prediction unit (PU), a transform unit (TU), a coding tree block (CTB), a coding block (CB), a prediction block (PB), a transform block (TB), a block, a sub-block of a block, a sub-region within a block, a region containing more than one sample or pixel.

[0407] In some embodiments, an indication of whether to and/or how to apply at least one of: the first target coding mode, or the second target coding mode may be indicated at one of the followings: sequence level, group of pictures level, picture level, slice level,

or tile group level. In some examples, an indication of whether to and/or how to apply at least one of: the first target coding mode, or the second target coding mode may be indicated in one of the followings: a sequence header, a picture header, a sequence parameter set (SPS), a video parameter set (VPS), a dependency parameter set (DPS), a decoding capability information (DCI), a picture parameter set (PPS), an adaptation parameter sets (APS), a slice header, or a tile group header.

[0408] In some embodiments, whether to and/or how to apply at least one of: the first target coding mode, or the second target coding mode depends on at least one of the followings: a message indicated in one of: DPS, SPS, VPS, PPS, APS, picture header, slice header, tile group header, coding tree unit (CTU), coding unit (CU), CTU row, group of CTUs, TU, PU block, video coding unit, a position of a CU block, a position of a PU block, a position of a TU block, a position of a video coding unit, block dimension of a current block, block dimension of a neighbouring block of the current block, block shape of a current block, block shape of a neighbouring block of the current block, a coded mode of a block, an indication of a colour format, a coding tree structure, a slice group type, a tile group type, a slice picture type, a tile picture type, a colour component, an ID of a temporal layer, a profile of a standard, a level of a standard, or a tier of a standard. In some embodiments, the coded mode of the block may be at least one of: IBC inter mode, non-IBC inter mode, or non-IBC subblock mode. In some other embodiments, the indication of a colour format may be 4:2:0 or 4:4:4. Alternatively, the colour component may be applied on a chroma component or a luma component.

[0409] In some embodiments, the syntax element may be binarized as a flag, a fixed length code, an EG(x) code, a unary code, a truncated unary code, or a truncated binary code. For example, the syntax element may be signed or unsigned.

[0410] In some embodiments, the syntax element may be coded with at least one context model, or bypass coded. In some embodiments, the syntax element may be signaled in at least one of following conditions: if a corresponding function is applicable, or if

dimensions of the block satisfy a condition. For example, the dimensions may be width and/or height.

[0411] In some embodiments, the syntax element may be signaled at one of the followings: a block level, a sequence level, a group of pictures level, a picture level, a slice level, or a tile group level.

[0412] In some embodiments, the syntax element may be signaled in one of the followings: a coding structure of CTU, a coding structure of CU, a coding structure of TU, a coding structure of PU, a coding structure of CTB, a coding structure of CB, a coding structure of TB, a coding structure of PB, a sequence header, a picture header, a sequence parameter set (SPS), a video parameter set (VPS), a dependency parameter set (DPS), a decoding capability information (DCI), a picture parameter set (PPS), an adaptation parameter sets (APS), a slice header, or a tile group header.

[0413] In some embodiments, at least one of: the first target coding mode, or the second target coding mode may be combined with at least one of the following coding tools: affine, multiple transform selection (MTS), low-frequency non-separable transform (LFNST), merge mode with motion vector difference (MMVD), matrix based intra prediction (MIP), intra sub-partitions (ISP), cross-component linear model (CCLM), convolutional cross-component model (CCCM), symmetric motion vector differences (SMVD), bi-directional optical flow (BDOF), decoder side motion vector refinement (DMVR), history-based MVP (HMVP), template matching, intra block copy (IBC), or Palette.

[0414] In some embodiments, at least one of: the first target coding mode, or the second target coding mode may be excluded with at least one of the following coding tools: affine, multiple transform selection (MTS), LFNST, merge mode with motion vector difference (MMVD), MIP, ISP, cross-component linear model (CCLM), convolutional cross-component model (CCCM), SMVD, bi-directional optical flow (BDOF), decoder side motion vector refinement (DMVR), history-based MVP (HMVP), template matching,

intra block copy (IBC), or Palette. In some examples, if at least one of: the first target coding mode, or the second target coding mode is used, the excluded coding tool may be disabled without signaling. In some other examples, if the excluded coding tool is used, the at least one of: the first target coding mode, or the second target coding mode may be disabled without signaling.

[0415] According to further embodiments of the present disclosure, a non-transitory computer-readable recording medium is provided. The non-transitory computer-readable recording medium stores a bitstream of a video which is generated by a method performed by an apparatus for video processing. The method comprises: applying a plurality of local illumination compensation (LIC) models to a video unit of the video, in response to the video unit of the video being coded with a second target coding mode; and generating the bitstream based on the plurality of LIC models.

[0416] According to still further embodiments of the present disclosure, a method for storing bitstream of a video is provided. The method comprises: applying a plurality of local illumination compensation (LIC) models to a video unit of the video, in response to the video unit of the video being coded with a second target coding mode; generating the bitstream based on the plurality of LIC models; and storing the bitstream in a non-transitory computer-readable recording medium.

[0417] Implementations of the present disclosure can be described in view of the following clauses, the features of which can be combined in any reasonable manner.

[0418] Clause 1. A method for video processing, comprising: deriving, for a conversion between a video unit of a video and a bitstream of the video, a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to the video unit being coded with a first target coding mode; and performing the conversion based on the LIC parameter.

[0419] Clause 2. The method of clause 1, wherein at least one of: a reconstructed sample

of the neighbouring row or a reconstructed sample of a column adjacent to the current block is excluded to derive the LIC parameter.

[0420] Clause 3. The method of clause 1, wherein a template comprises one or more neighbouring reconstructed samples, wherein WT represents a width of the template and HT represents a height of the template.

[0421] Clause 4. The method of clause 3, wherein a sample of an above template is used to derive the LIC parameters.

[0422] Clause 5. The method of clause 3, wherein a sample of a left template is used to derive the LIC parameters.

[0423] Clause 6. The method of clause 3, wherein a sample of a left-bottom template is used to derive the LIC parameters.

[0424] Clause 7. The method of clause 3, wherein a sample of a right-above template is used to derive the LIC parameters.

[0425] Clause 8. The method of clause 3, wherein a sample of a left-above template is used to derive the LIC parameters.

[0426] Clause 9. The method of clause 3, wherein a plurality of the following templates: above template, left template, right-above template, left-bottom template, and left-above template are combined into one template that is used to derive the LIC parameter.

[0427] Clause 10. The method of clause 3, wherein a L-shape template is used.

[0428] Clause 11. The method of clause 3, wherein a non-adjacent template is used.

[0429] Clause 12. The method of clause 3, wherein at least one of: WT or HT equals to 1.

[0430] Clause 13. The method of clause 3, wherein the template comprises a plurality of rows and/or a plurality of columns.

[0431] Clause 14. The method of clause 3, wherein samples of the template are in the same coding tree unit (CTU), the same CTU row, the same CTU tile, or the same CTU slice of the video unit.

[0432] Clause 15. The method of clause 14, wherein samples of the template in a different CTU, a different CTU row, a different CTU tile, or a different CTU slice of the video unit are excluded for deriving LIC parameters.

[0433] Clause 16. The method of clause 1, wherein the template comprises at least one of: a neighbouring reconstructed sample of a row except for a nearest row, or a neighbouring reconstructed sample of a column except for a nearest column, wherein line X represents at least one of: the row, or the column.

[0434] Clause 17. The method of clause 16, wherein X is an integer larger than 0.

[0435] Clause 18. The method of clause 16, wherein a left-above sample of the line X is used.

[0436] Clause 19. The method of clause 16, wherein samples of a plurality of rows or columns are used.

[0437] Clause 20. The method of clause 16, wherein at least one of: the row or the column is in the same CTU, the same CTU row, the same CTU tile, or the same CTU slice of the video unit.

[0438] Clause 21. The method of clause 1, wherein if above samples are not available, a left sample is used to derive the LIC parameter.

[0439] Clause 22. The method of clause 1, wherein if left samples are not available, an above sample is used to derive the LIC parameter.

[0440] Clause 23. The method of clause 22, wherein if a sample is located in a different video unit or the sample is not reconstructed or a cross-filtering video unit is disallowed, the sample is not available.

[0441] Clause 24. The method of clause 23, wherein if the sample is out of at least one of: a current picture, a current sub-picture, a current tile, a current slice, a current brick, a current CTU, a current processing unit, or any other current video unit, the sample is located in the different video unit.

[0442] Clause 25. The method of clause 23, wherein the first target coding mode is not used.

[0443] Clause 26. The method of clause 1, wherein a determination of a template is signalled, wherein the template is used to derive the LIC parameter.

[0444] Clause 27. The method of clause 26, wherein one or more syntax elements are used to indicate which template is used to derive the LIC parameter.

[0445] Clause 28. The method of clause 27, wherein an index of the template is signalled.

[0446] Clause 29. The method of clause 28, wherein the index of the template comprises an index of a line number.

[0447] Clause 30. The method of clause 28, wherein the index of the template comprises a predefined template.

[0448] Clause 31. The method of 30, wherein the predefined template comprises at least one of: a left template, an above template, or a left-above template.

[0449] Clause 32. The method of clause 27, wherein the one or more syntax elements are signalled if a condition is satisfied.

[0450] Clause 33. The method of clause 32, wherein the condition comprises at least one of: whether LIC is allowed to be used, whether LIC is used, or whether one or more template samples are available.

[0451] Clause 34. The method of clause 26, wherein reordering is used in the determination of the template.

[0452] Clause 35. The method of clause 34, wherein if a set of templates are at least one

of: predefined, signalled, or derived, and the reordering is performed for the set of templates, an indication of first M templates after reordering is signalled, wherein M is an integer number.

[0453] Clause 36. The method of clause 34, wherein at least one of: template matching cost, or bilateral matching cost is used in the reordering.

[0454] Clause 37. The method of clause 36, wherein different LIC parameters are derived using different templates, and wherein a prediction of a current template is refined using the LIC parameter and used to calculate template matching (TM) cost.

[0455] Clause 38. The method of clause 37, wherein a current template comprises a neighbouring reconstructed sample.

[0456] Clause 39. The method of clause 37, wherein a current template is different from a template that is used to derive the LIC parameter.

[0457] Clause 40. The method of clause 39, wherein the template used to derive LIC parameter is a non-adjacent neighboring sample.

[0458] Clause 41. The method of clause 1, wherein a determination of a template is derived, wherein the template is used to derive the LIC parameter.

[0459] Clause 42. The method of clause 41, wherein at least one of: TM cost, or bilateral matching (BM) cost is used to determine the template.

[0460] Clause 43. The method of clause 41, wherein a reordering approach is used, and a best template is determined as a template with best cost.

[0461] Clause 44. The method of clause 43, wherein the best cost comprises a lowest TM cost.

[0462] Clause 45. The method of clause 1, wherein the first target coding mode is applied to advanced motion vector prediction (AMVP) mode.

[0463] Clause 46. The method of clause 45, wherein the AMVP mode comprises at least

one of: regular AMVP mode, affine AMVP mode, TM AMVP mode, or intra block copy (IBC) AMVP mode.

[0464] Clause 47. The method of clause 45, wherein the first target coding mode is not used for TM AMVP mode.

[0465] Clause 48. The method of clause 47, wherein the first target coding mode is not used to construct a TM based AMVP candidate list construction.

[0466] Clause 49. The method of clause 45, wherein the first target coding mode is used for TM AMVP mode but the first target coding mode is not used to construct a TM based AMVP candidate list.

[0467] Clause 50. The method of clause 1, wherein the first target coding mode is applied to merge mode.

[0468] Clause 51. The method of clause 50, wherein the merge mode comprises at least one of: regular merge mode, sub-block-based merge mode, TM merge mode, affine merge mode, merge mode with motion vector differences (MMVD), or IBC merge mode.

[0469] Clause 52. The method of clause 50, wherein at least one of: a template, or an indication of the template is inherited for the merge mode, wherein at least one of: the template, or the indication of the template is used to derive the LIC parameter.

[0470] Clause 53. The method of clause 50, wherein at least one of: a template, or an indication of the template is signalled for the merge mode.

[0471] Clause 54. The method of clause 50, wherein at least one of: a template, or an indication of the template is derived for the merge mode.

[0472] Clause 55. The method of clause 50, wherein the first target coding mode is applied to an adaptive reordering of merge candidates (ARMC) process.

[0473] Clause 56. The method of clause 55, wherein the first target coding mode is applied to an ARMC process of a coding tool.

[0474] Clause 57. The method of clause 50, wherein the first target coding mode is not applied to an ARMC process.

[0475] Clause 58. The method of clause 1, wherein a derivation of the LIC parameter used in the first target coding mode is same as a derivation of the LIC parameter used in LIC.

[0476] Clause 59. The method of clause 1, wherein a derivation of the LIC parameter used in the first target coding mode is different from a derivation of the LIC parameter used in LIC.

[0477] Clause 60. The method of clause 1, wherein at least one of: a template used in a current video unit, or an index of the template used in a current video unit is used by a following video unit.

[0478] Clause 61. The method of clause 60, wherein the following video unit comprises at least one of: a luma video unit, or a chroma video unit.

[0479] Clause 62. The method of clause 1, wherein the first target coding mode is applied to a coding tool.

[0480] Clause 63. The method of clause 62, wherein the coding tool comprises an inter coding tool.

[0481] Clause 64. The method of clause 63, wherein the inter coding tool comprises a specific coding tool, wherein the specific coding tool is at least one of: combined inter and intra prediction (CIIP), bi-prediction with CU-level weight (BCW), merge mode with motion vector difference (MMVD), template matching (TM), affine, decoder side motion vector refinement (DMVR), multi-pass DMVR, prediction refinement with optical flow (PROF), bi-directional optical flow (BDOF), sample based BDOF, adaptive decoder-side motion vector refinement (ADMVR), overlapped block motion compensation (OBMC), TM based OBMC, multi-hypothesis prediction (MHP), geometric partitioning mode (GPM), bilateral matching advanced motion vector prediction (AMVP)-merge mode,

template matching AMVP-merge mode, a variant of CIIP, a variant of BCW, a variant of MMVD, a variant of TM, a variant of affine, a variant of DMVR, a variant of multi-pass DMVR, a variant of PROF, a variant of BDOF, a variant of sample based BDOF, a variant of ADMVR, a variant of OBMC, a variant of TM based OBMC, a variant of MHP, a variant of GPM, a variant of bilateral matching AMVP-merge mode, or a variant of template matching AMVP-merge mode.

[0482] Clause 65. The method of clause 64, wherein the CIIP is at least one of: CIIP-Planar, CIIP-TIMD, CIIP-TM.

[0483] Clause 66. The method of clause 64, wherein the BCW is BCW index derived by TM.

[0484] Clause 67. The method of clause 64, wherein the MMVD is at least one of: MMVD or TM based reordering for MMVD.

[0485] Clause 68. The method of clause 64, wherein the affine is at least one of: affine-MMVD, TM based reordering for affine MMVD.

[0486] Clause 69. The method of clause 64, wherein the GPM is at least one of: GPM, GPM-TM, GPM-MMVD, or GPM-intra.

[0487] Clause 70. The method of clause 63, wherein the first target coding mode is not used for one or more inter coding tools.

[0488] Clause 71. The method of clause 70, wherein the first target coding mode is not used for an affine mode.

[0489] Clause 72. The method of clause 70, wherein the first target coding mode is not used for an AMVP-merge mode.

[0490] Clause 73. The method of clause 70, wherein the first target coding mode is not used for a BCW.

[0491] Clause 74. The method of clause 70, wherein the first target coding mode is not

used for an AMVR.

[0492] Clause 75. The method of clause 70, wherein the first target coding mode is not used for a motion vector resolution.

[0493] Clause 76. The method of clause 70, wherein the first target coding mode is not used for symmetric motion vector differences (SMVD).

[0494] Clause 77. The method of clause 62, wherein the first target coding mode is applied to at least one of: uni-prediction, or bi-prediction.

[0495] Clause 78. The method of clause 1, wherein LIC is not applied to a coding tool.

[0496] Clause 79. The method of clause 78, wherein the coding tool comprises SMVD.

[0497] Clause 80. The method of clause 1, wherein an indication of whether to and/or how to apply the first target coding mode depends on coding information.

[0498] Clause 81. The method of clause 80, wherein the coding information comprises at least one of the followings: an indication of whether a coding approach is allowed, a block dimension, a block size, a block depth, a slice type, a picture type, a partition tree type, a temporal layer identification, a block location, colour format, or a colour component.

[0499] Clause 82. The method of clause 81, wherein the coding approach comprises LIC.

[0500] Clause 83. The method of clause 81, wherein if a block size of a block is less than or equals to a first threshold, the block is allowed to be coded with the first target coding mode.

[0501] Clause 84. The method of clause 83, wherein the first threshold equals to one of: 64, 128, 256, 512, 1024, 2048, or 4096.

[0502] Clause 85. The method of clause 81, wherein if a block size of a block is larger than or equals to a second threshold, the block is allowed to be coded with the first target coding mode.

[0503] Clause 86. The method of clause 85, wherein the second threshold equals to one of: 8, 16, 32, 64, 128, 256, or 512.

[0504] Clause 87. The method of clause 81, wherein if at least one of: a block width of a block is larger than or equals to a third threshold, or a block height of the block is larger than or equals to a fourth threshold, the block is allowed to be coded with the first target coding mode.

[0505] Clause 88. The method of clause 87, wherein the third threshold equals to one of: 4, 8, 16, 32, or 64.

[0506] Clause 89. The method of clause 87, wherein the fourth threshold equals to one of: 4, 8, 16, 32, or 64.

[0507] Clause 90. The method of clause 81, wherein if at least one of: a block width of a block is less than or equals to a fifth threshold, or a block height of the block is less than or equals to a sixth threshold, the block is allowed to be coded with the first target coding mode.

[0508] Clause 91. The method of clause 90, wherein the fifth threshold equals to one of: 8, 16, 32, 64, or 128.

[0509] Clause 92. The method of clause 90, wherein the sixth threshold equals to one of: 8, 16, 32, 64, or 128.

[0510] Clause 93. The method of clause 81, wherein at least one of: if W is larger than or equals to $S \cdot H$, a left template is not allowed to be used, or if H is larger than or equals to $S \cdot W$, an above template is not allowed to be used, wherein W represents a block width of a block, H represents a block height of a block, S represents a scale ratio.

[0511] Clause 94. The method of clause 81, wherein at least one of: if W is larger than or equals to $S \cdot H$, an above template is not allowed to be used, or if H is larger than or equals to $S \cdot W$, a left template is not allowed to be used, wherein W represents a block width of a block, H represents a block height of a block, S represents a scale ratio.

[0512] Clause 95. The method of clause 81, wherein the block size comprises a luma block size.

[0513] Clause 96. The method of clause 81, wherein the block size comprises a chroma block size.

[0514] Clause 97. The method of clause 81, wherein the partition tree type comprises at least one of: single tree, dual tree, or local dual tree.

[0515] Clause 98. The method of clause 81, wherein if a current video unit is at a left boundary of a current picture, a left template is not used.

[0516] Clause 99. The method of clause 81, wherein if at least one of: a current video unit is at an above boundary of current picture, or the current video is at an above boundary of a current CTU, an above template is not used.

[0517] Clause 100. The method of clause 81, wherein the first target coding mode is applied to all colour components.

[0518] Clause 101. The method of clause 81, wherein if the first target coding mode is applied to a chroma component, the first target coding mode is different from a first target coding mode applied to a luma component.

[0519] Clause 102. The method of clause 81, wherein an indication of whether to and/or how to apply the first target coding mode to a first component depends on an indication of whether to and/or how to apply the first target coding mode to a second component.

[0520] Clause 103. The method of clause 102, wherein the first component comprises a chroma component, and the second component comprises a luma component.

[0521] Clause 104. The method of clause 103, wherein the chroma component comprises at least one of: Cb, or Cr.

[0522] Clause 105. The method of clause 103, wherein the luma component comprises Y.

[0523] Clause 106. The method of clause 102, wherein a way to apply the first target coding mode to the first component is same as a way to apply the first target coding mode to the second component.

[0524] Clause 107. The method of clause 102, wherein a way to apply the first target coding mode to the first component is different from a way to apply the first target coding mode to the second component.

[0525] Clause 108. The method of clause 102, wherein at least one of: a template of a current chroma block, or an indication of the template of the current chroma block is inherited from a luma block.

[0526] Clause 109. The method of clause 81, wherein the first target coding mode is applied to a luma component, but the first target coding mode is not applied to a chroma component.

[0527] Clause 110. The method of clause 109, wherein the luma component comprises at least one of: Y in YCbCr colour space, or G in RGB colour space.

[0528] Clause 111. The method of clause 109, wherein the chroma component comprises at least one of: Cb in YCbCr colour space, Cr in YCbCr colour space, R in RGB colour space, or B in RGB colour space.

[0529] Clause 112. The method of clause 1, wherein if a condition is satisfied, an indication of the first target coding mode is signalled.

[0530] Clause 113. The method of clause 112, wherein the condition comprises at least one of the followings: a block dimension, a block size, a block depth, a slice type, a picture type, a partition tree type, a temporal layer identification, a block location, colour format, or a colour component.

[0531] Clause 114. The method of clause 113, wherein if a block size of a block is less than or equals to a seventh threshold, the indication of the first target coding mode is not signalled.

[0532] Clause 115. The method of clause 114, wherein the seventh threshold equals to one of: 64, 128, 256, 512, 1024, 2048, or 4096.

[0533] Clause 116. The method of clause 113, wherein if a block size of a block is less than or equals to an eighth threshold, the indication of the first target coding mode is not signalled.

[0534] Clause 117. The method of clause 116, wherein the eighth threshold equals to one of: 16, 32, 64, 128, 256, or 512.

[0535] Clause 118. The method of clause 113, wherein if at least one of: a block width of a block is larger than or equals to a ninth threshold, or a block height of the block is larger than or equals to a tenth threshold, the indication of the first target coding mode is signalled.

[0536] Clause 119. The method of clause 118, wherein the ninth threshold equals to one of: 4, 8, 16, 32, or 64.

[0537] Clause 120. The method of clause 118, wherein the tenth threshold equals to one of: 4, 8, 16, 32, or 64.

[0538] Clause 121. The method of clause 113, wherein if at least one of: a block width of a block is less than or equals to an eleventh threshold, or a block height of the block is less than or equals to a twelfth threshold, the indication of the first target coding mode is signalled.

[0539] Clause 122. The method of clause 121, wherein the eleventh threshold equals to one of: 8, 16, 32, 64, or 128.

[0540] Clause 123. The method of clause 121, wherein the twelfth threshold equals to one of: 8, 16, 32, 64, or 128.

[0541] Clause 124. The method of any of clauses 114-123, wherein at least one of thresholds: the seventh threshold, the eighth threshold, the ninth threshold, the tenth

threshold, the eleventh threshold, or the twelfth threshold depends on at least one of: slice type, or picture type.

[0542] Clause 125. The method of clause 113, wherein the block size comprises a luma block size.

[0543] Clause 126. The method of clause 113, wherein the block size comprises a chroma block size.

[0544] Clause 127. The method of clause 113, wherein the partition tree type comprises at least one of: single tree, dual tree, or local dual tree.

[0545] Clause 128. The method of clause 113, wherein if a current video unit is at the left boundary of a current picture, the indication of the first target coding mode is not signalled.

[0546] Clause 129. The method of clause 113, wherein if at least one of: a current video unit is at the above boundary of current picture, or the current video is at an above boundary of a current CTU, the indication of the first target coding mode is not signalled.

[0547] Clause 130. The method of clause 1, wherein an indication of whether a current block is coded with the first target coding mode is signalled using one or more syntax elements.

[0548] Clause 131. The method of clause 130, wherein the one or more syntax elements are binarized with at least one of: fixed length coding, truncated unary coding, unary coding, EG coding, or coded a flag.

[0549] Clause 132. The method of clause 130, wherein the one or more syntax elements are bypass coded or context coded.

[0550] Clause 133. The method of clause 132, wherein context of the one or more syntax elements depends on coded information.

[0551] Clause 134. The method of clause 133, wherein the coded information comprises

at least one of the followings: a block dimension, a block size, a slice type, a picture type, information of a neighbouring block, information of another coding tool used for the current block, or information of a temporal layer.

[0552] Clause 135. The method of clause 134, wherein the neighbouring block is adjacent or non-adjacent.

[0553] Clause 136. The method of clause 130, wherein the one or more syntax elements are signalled at one of the followings: a sequence header, a picture header, a sequence parameter set (SPS), a video parameter set (VPS), a dependency parameter set (DPS), a decoding capability information (DCI), a picture parameter set (PPS), an adaptation parameter sets (APS), a slice header, or a tile group header.

[0554] Clause 137. The method of clause 130, wherein the one or more syntax elements are coded in a predictive way.

[0555] Clause 138. The method of clause 137, wherein one or more syntax elements of the current block are predicted by one or more syntax elements of a neighboring block.

[0556] Clause 139. The method of clause 130, wherein the one or more syntax elements are coded if a condition is satisfied.

[0557] Clause 140. The method of clause 139, wherein if a first syntax element indicates that the first target coding mode is applicable, a second syntax element is signalled to indicate whether the first target coding mode is used.

[0558] Clause 141. The method of clause 140, wherein the first syntax element is signalled at one of the followings: a sequence header, a picture header, a sequence parameter set (SPS), a video parameter set (VPS), a dependency parameter set (DPS), a decoding capability information (DCI), a picture parameter set (PPS), an adaptation parameter sets (APS), a slice header, or a tile group header.

[0559] Clause 142. The method of clause 140, wherein the second syntax element is for a block.

[0560] Clause 143. The method of clause 139, wherein if a second syntax element indicates that the first target coding mode is used, a third syntax element is signalled to indicate a way of performing the first target coding mode.

[0561] Clause 144. The method of clause 1, wherein the first target coding mode is applied to another coding tool.

[0562] Clause 145. The method of clause 144, wherein the other coding tool comprises at least one of: a variant of LIC, or another coding tool using a neighbouring sample to derive one or more parameters.

[0563] Clause 146. The method of clause 145, wherein the variant of LIC comprises LIC with filtering which uses a plurality of parameters.

[0564] Clause 147. A method for video processing, comprising: applying, for a conversion between a video unit of a video and a bitstream of the video, a plurality of local illumination compensation (LIC) models to the video unit, in response to the video unit being coded with a second target coding mode; and performing the conversion based on the plurality of LIC models.

[0565] Clause 148. The method of clause 147, wherein neighbouring reconstructed samples are divided into N sets, and parameters of each LIC model are derived from each set, wherein N is an integer larger than 1.

[0566] Clause 149. The method of clause 148, wherein a way to divide the neighbouring reconstructed samples into the N sets is based on coding information.

[0567] Clause 150. The method of clause 149, wherein a sample value is used to divide the neighbouring reconstructed samples into the N sets.

[0568] Clause 151. The method of clause 150, wherein one or more thresholds based on the sample value are used.

[0569] Clause 152. The method of clause 151, wherein if N equals to 2, a first threshold

is used to divide the neighbouring reconstructed samples into two sets.

[0570] Clause 153. The method of clause 152, wherein the first threshold equals to a mean of all the neighbouring reconstructed samples.

[0571] Clause 154. The method of clause 151, wherein if N equals to 3, a first threshold and a second threshold are used to divide the neighbouring reconstructed samples into three sets.

[0572] Clause 155. The method of clause 151, wherein the one or more thresholds are based on at least one of: a colour component, or colour format.

[0573] Clause 156. The method of clause 150, wherein the way to divide the neighbouring reconstructed samples into the N sets is based on at least one of: a block size, or a block dimension.

[0574] Clause 157. The method of clause 148, wherein a way to divide the neighbouring reconstructed samples into the N sets is signalled.

[0575] Clause 158. The method of clause 147, wherein the second target coding mode is applied to a coding tool.

[0576] Clause 159. The method of clause 158, wherein the coding tool comprises an inter coding tool.

[0577] Clause 160. The method of clause 159, wherein the inter coding tool comprises a specific coding tool, wherein the specific coding tool is at least one of: combined inter and intra prediction (CIIP), bi-prediction with CU-level weight (BCW), merge mode with motion vector difference (MMVD), template matching (TM), affine, decoder side motion vector refinement (DMVR), multi-pass DMVR, prediction refinement with optical flow (PROF), bi-directional optical flow (BDOF), sample based BDOF, adaptive decoder-side motion vector refinement (ADMVR), overlapped block motion compensation (OBMC), TM based OBMC, multi-hypothesis prediction (MHP), geometric partitioning mode (GPM), bilateral matching advanced motion vector prediction (AMVP)-merge mode,

template matching AMVP-merge mode, a variant of CIIP, a variant of BCW, a variant of MMVD, a variant of TM, a variant of affine, a variant of DMVR, a variant of multi-pass DMVR, a variant of PROF, a variant of BDOF, a variant of sample based BDOF, a variant of ADMVR, a variant of OBMC, a variant of TM based OBMC, a variant of MHP, a variant of GPM, a variant of bilateral matching AMVP-merge mode, or a variant of template matching AMVP-merge mode.

[0578] Clause 161. The method of clause 160, wherein the CIIP is at least one of: CIIP-Planar, CIIP-TIMD, CIIP-TM.

[0579] Clause 162. The method of clause 160, wherein the BCW is BCW index derived by TM.

[0580] Clause 163. The method of clause 160, wherein the MMVD is at least one of: MMVD or TM based reordering for MMVD.

[0581] Clause 164. The method of clause 160, wherein the affine is at least one of: affine-MMVD, TM based reordering for affine MMVD.

[0582] Clause 165. The method of clause 160, wherein the GPM is at least one of: GPM, GPM-TM, GPM-MMVD, or GPM-intra.

[0583] Clause 166. The method of clause 159, wherein the second target coding mode is not used for one or more inter coding tools.

[0584] Clause 167. The method of clause 166, wherein the second target coding mode is not used for an affine mode.

[0585] Clause 168. The method of clause 166, wherein the second target coding mode is not used for an AMVP-merge mode.

[0586] Clause 169. The method of clause 166, wherein the second target coding mode is not used for a BCW.

[0587] Clause 170. The method of clause 166, wherein the second target coding mode

is not used for an AMVR.

[0588] Clause 171. The method of clause 166, wherein the second target coding mode is not used for a motion vector resolution.

[0589] Clause 172. The method of clause 166, wherein the second target coding mode is not used for symmetric motion vector differences (SMVD).

[0590] Clause 173. The method of clause 158, wherein the second target coding mode is applied to at least one of: uni-prediction, or bi-prediction.

[0591] Clause 174. The method of clause 147, wherein an indication of whether to and/or how to apply the second target coding mode depends on coding information.

[0592] Clause 175. The method of clause 174, wherein the coding information comprises at least one of the followings: an indication of whether a coding approach is allowed, a block dimension, a block size, a block depth, a slice type, a picture type, a partition tree type, a temporal layer identification, a block location, colour format, or a colour component.

[0593] Clause 176. The method of clause 175, wherein the partition tree type comprises at least one of: single tree, dual tree, or local dual tree.

[0594] Clause 177. The method of clause 147, wherein if a condition is satisfied, the second target coding mode is signalled.

[0595] Clause 178. The method of clause 177, wherein the condition comprises at least one of the followings: a block dimension, a block size, a block depth, a slice type, a picture type, a partition tree type, a temporal layer identification, a block location, colour format, or a colour component.

[0596] Clause 179. The method of clause 178, wherein the partition tree type comprises at least one of: single tree, dual tree, or local dual tree.

[0597] Clause 180. The method of clause 147, wherein an indication of whether a current

block is coded with the second target coding mode is signalled using one or more syntax elements.

[0598] Clause 181. The method of clause 180, wherein the one or more syntax elements are binarized with at least one of: fixed length coding, truncated unary coding, unary coding, EG coding, or coded a flag.

[0599] Clause 182. The method of clause 180, wherein the one or more syntax elements are bypass coded or context coded.

[0600] Clause 183. The method of clause 182, wherein context of the one or more syntax elements depends on coded information.

[0601] Clause 184. The method of clause 183, wherein the coded information comprises at least one of the followings: a block dimension, a block size, a slice type, a picture type, information of a neighbouring block, information of another coding tool used for the current block, or information of a temporal layer.

[0602] Clause 185. The method of clause 184, wherein the neighbouring block is adjacent or non-adjacent.

[0603] Clause 186. The method of clause 180, wherein the one or more syntax elements are signalled at one of the followings: a sequence header, a picture header, a sequence parameter set (SPS), a video parameter set (VPS), a dependency parameter set (DPS), a decoding capability information (DCI), a picture parameter set (PPS), an adaptation parameter sets (APS), a slice header, or a tile group header.

[0604] Clause 187. The method of clause 180, wherein the one or more syntax elements are coded in a predictive way.

[0605] Clause 188. The method of clause 187, wherein one or more syntax elements of the current block are predicted by one or more syntax elements of a neighboring block.

[0606] Clause 189. The method of any of clauses 1-188, wherein the video unit comprises

at least one of the followings: a colour component, a sub-picture, a slice, a tile, a coding tree unit (CTU), a CTU row, a group of CTU, a coding unit (CU), a prediction unit (PU), a transform unit (TU), a coding tree block (CTB), a coding block (CB), a prediction block (PB), a transform block (TB), a block, a sub-block of a block, a sub-region within a block, a region containing more than one sample or pixel.

[0607] Clause 190. The method of any of clauses 1-188, wherein an indication of whether to and/or how to apply at least one of: the first target coding mode, or the second target coding mode is indicated at one of the followings: sequence level, group of pictures level, picture level, slice level, or tile group level.

[0608] Clause 191. The method of any of clauses 1-188, wherein an indication of whether to and/or how to apply at least one of: the first target coding mode, or the second target coding mode is indicated in one of the followings: a sequence header, a picture header, a sequence parameter set (SPS), a video parameter set (VPS), a dependency parameter set (DPS), a decoding capability information (DCI), a picture parameter set (PPS), an adaptation parameter sets (APS), a slice header, or a tile group header.

[0609] Clause 192. The method of any of clauses 1-188, wherein whether to and/or how to apply at least one of: the first target coding mode, or the second target coding mode depends on at least one of the followings: a message indicated in one of: DPS, SPS, VPS, PPS, APS, picture header, slice header, tile group header, coding tree unit (CTU), coding unit (CU), CTU row, group of CTUs, TU, PU block, video coding unit, a position of a CU block, a position of a PU block, a position of a TU block, a position of a video coding unit, block dimension of a current block, block dimension of a neighbouring block of the current block, block shape of a current block, block shape of a neighbouring block of the current block, a coded mode of a block, an indication of a colour format, a coding tree structure, a slice group type, a tile group type, a slice picture type, a tile picture type, a colour component, an ID of a temporal layer, a profile of a standard, a level of a standard, or a tier of a standard.

[0610] Clause 193. The method of clause 192, wherein the coded mode of the block is at least one of: IBC inter mode, non-IBC inter mode, or non-IBC subblock mode.

[0611] Clause 194. The method of clause 192, wherein the indication of a colour format is 4:2:0 or 4:4:4.

[0612] Clause 195. The method of clause 192, wherein the colour component is applied on a chroma component or a luma component.

[0613] Clause 196. The method of any of clauses 1-188, wherein the syntax element is binarized as a flag, a fixed length code, an EG(x) code, a unary code, a truncated unary code, or a truncated binary code.

[0614] Clause 197. The method of clause 196, wherein the syntax element is signed or unsigned.

[0615] Clause 198. The method of any of clauses 1-188, wherein the syntax element is coded with at least one context model, or bypass coded.

[0616] Clause 199. The method of any of clauses 1-188, wherein the syntax element is signaled in at least one of following conditions: if a corresponding function is applicable, or if dimensions of the block satisfy a condition.

[0617] Clause 200. The method of clause 199, wherein the dimensions are width and/or height.

[0618] Clause 201. The method of any of clauses 1-188, wherein the syntax element is signaled at one of the followings: a block level, a sequence level, a group of pictures level, a picture level, a slice level, or a tile group level.

[0619] Clause 202. The method of any of clauses 1-188, wherein the syntax element is signaled in one of the followings: a coding structure of CTU, a coding structure of CU, a coding structure of TU, a coding structure of PU, a coding structure of CTB, a coding structure of CB, a coding structure of TB, a coding structure of PB, a sequence header, a

picture header, a sequence parameter set (SPS), a video parameter set (VPS), a dependency parameter set (DPS), a decoding capability information (DCI), a picture parameter set (PPS), an adaptation parameter sets (APS), a slice header, or a tile group header.

[0620] Clause 203. The method of any of clauses 1-188, wherein at least one of: the first target coding mode, or the second target coding mode is combined with at least one of the following coding tools: affine, multiple transform selection (MTS), low-frequency non-separable transform (LFNST), merge mode with motion vector difference (MMVD), matrix based intra prediction (MIP), intra sub-partitions (ISP), cross-component linear model (CCLM), convolutional cross-component model (CCCM), symmetric motion vector differences (SMVD), bi-directional optical flow (BDOF), decoder side motion vector refinement (DMVR), history-based MVP (HMVP), template matching, intra block copy (IBC), or Palette.

[0621] Clause 204. The method of any of clauses 1-188, wherein at least one of: the first target coding mode, or the second target coding mode is excluded with at least one of the following coding tools: affine, multiple transform selection (MTS), LFNST, merge mode with motion vector difference (MMVD), MIP, ISP, cross-component linear model (CCLM), convolutional cross-component model (CCCM), SMVD, bi-directional optical flow (BDOF), decoder side motion vector refinement (DMVR), history-based MVP (HMVP), template matching, intra block copy (IBC), or Palette.

[0622] Clause 205. The method of clause 204, wherein if at least one of: the first target coding mode, or the second target coding mode is used, the excluded coding tool is disabled without signaling.

[0623] Clause 206. The method of clause 204, wherein if the excluded coding tool is used, the at least one of: the first target coding mode, or the second target coding mode is disabled without signaling.

[0624] Clause 207. The method of any of clauses 1-206, wherein the conversion includes

encoding the video unit into the bitstream.

[0625] Clause 208. The method of any of clauses 1-206, wherein the conversion includes decoding the video unit from the bitstream.

[0626] Clause 209. An apparatus for video processing comprising a processor and a non-transitory memory with instructions thereon, wherein the instructions upon execution by the processor, cause the processor to perform a method in accordance with any of clauses 1-208.

[0627] Clause 210. A non-transitory computer-readable storage medium storing instructions that cause a processor to perform a method in accordance with any of clauses 1-208.

[0628] Clause 211. A non-transitory computer-readable recording medium storing a bitstream of a video which is generated by a method performed by an apparatus for video processing, wherein the method comprises: deriving a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to a video unit of the video being coded with a first target coding mode; and generating the bitstream based on the LIC parameter.

[0629] Clause 212. A method for storing a bitstream of a video, comprising: deriving a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to a video unit of the video being coded with a first target coding mode; generating the bitstream based on the LIC parameter; and storing the bitstream in a non-transitory computer-readable recording medium.

[0630] Clause 213. A non-transitory computer-readable recording medium storing a bitstream of a video which is generated by a method performed by an apparatus for video processing, wherein the method comprises: applying a plurality of local illumination

compensation (LIC) models to a video unit of the video, in response to the video unit of the video being coded with a second target coding mode; and generating the bitstream based on the plurality of LIC models.

[0631] Clause 214. A method for storing a bitstream of a video, comprising: applying a plurality of local illumination compensation (LIC) models to a video unit of the video, in response to the video unit of the video being coded with a second target coding mode; generating the bitstream based on the plurality of LIC models; and storing the bitstream in a non-transitory computer-readable recording medium.

Example Device

[0632] Fig. 54 illustrates a block diagram of a computing device 5400 in which various embodiments of the present disclosure can be implemented. The computing device 5400 may be implemented as or included in the source device 110 (or the video encoder 114 or 200) or the destination device 120 (or the video decoder 124 or 300).

[0633] It would be appreciated that the computing device 5400 shown in Fig. 54 is merely for purpose of illustration, without suggesting any limitation to the functions and scopes of the embodiments of the present disclosure in any manner.

[0634] As shown in Fig. 54, the computing device 5400 includes a general-purpose computing device 5400. The computing device 5400 may at least comprise one or more processors or processing units 5410, a memory 5420, a storage unit 5430, one or more communication units 5440, one or more input devices 5450, and one or more output devices 5460.

[0635] In some embodiments, the computing device 5400 may be implemented as any user terminal or server terminal having the computing capability. The server terminal may be a server, a large-scale computing device or the like that is provided by a service provider. The user terminal may for example be any type of mobile terminal, fixed terminal, or portable terminal, including a mobile phone, station, unit, device, multimedia

computer, multimedia tablet, Internet node, communicator, desktop computer, laptop computer, notebook computer, netbook computer, tablet computer, personal communication system (PCS) device, personal navigation device, personal digital assistant (PDA), audio/video player, digital camera/video camera, positioning device, television receiver, radio broadcast receiver, E-book device, gaming device, or any combination thereof, including the accessories and peripherals of these devices, or any combination thereof. It would be contemplated that the computing device 5400 can support any type of interface to a user (such as “wearable” circuitry and the like).

[0636] The processing unit 5410 may be a physical or virtual processor and can implement various processes based on programs stored in the memory 5420. In a multi-processor system, multiple processing units execute computer executable instructions in parallel so as to improve the parallel processing capability of the computing device 5400. The processing unit 5410 may also be referred to as a central processing unit (CPU), a microprocessor, a controller or a microcontroller.

[0637] The computing device 5400 typically includes various computer storage medium. Such medium can be any medium accessible by the computing device 5400, including, but not limited to, volatile and non-volatile medium, or detachable and non-detachable medium. The memory 5420 can be a volatile memory (for example, a register, cache, Random Access Memory (RAM)), a non-volatile memory (such as a Read-Only Memory (ROM), Electrically Erasable Programmable Read-Only Memory (EEPROM), or a flash memory), or any combination thereof. The storage unit 5430 may be any detachable or non-detachable medium and may include a machine-readable medium such as a memory, flash memory drive, magnetic disk or another other media, which can be used for storing information and/or data and can be accessed in the computing device 5400.

[0638] The computing device 5400 may further include additional detachable/non-detachable, volatile/non-volatile memory medium. Although not shown in Fig. 54, it is possible to provide a magnetic disk drive for reading from and/or writing into a detachable

and non-volatile magnetic disk and an optical disk drive for reading from and/or writing into a detachable non-volatile optical disk. In such cases, each drive may be connected to a bus (not shown) via one or more data medium interfaces.

[0639] The communication unit 5440 communicates with a further computing device via the communication medium. In addition, the functions of the components in the computing device 5400 can be implemented by a single computing cluster or multiple computing machines that can communicate via communication connections. Therefore, the computing device 5400 can operate in a networked environment using a logical connection with one or more other servers, networked personal computers (PCs) or further general network nodes.

[0640] The input device 5450 may be one or more of a variety of input devices, such as a mouse, keyboard, tracking ball, voice-input device, and the like. The output device 5460 may be one or more of a variety of output devices, such as a display, loudspeaker, printer, and the like. By means of the communication unit 5440, the computing device 5400 can further communicate with one or more external devices (not shown) such as the storage devices and display device, with one or more devices enabling the user to interact with the computing device 5400, or any devices (such as a network card, a modem and the like) enabling the computing device 5400 to communicate with one or more other computing devices, if required. Such communication can be performed via input/output (I/O) interfaces (not shown).

[0641] In some embodiments, instead of being integrated in a single device, some or all components of the computing device 5400 may also be arranged in cloud computing architecture. In the cloud computing architecture, the components may be provided remotely and work together to implement the functionalities described in the present disclosure. In some embodiments, cloud computing provides computing, software, data access and storage service, which will not require end users to be aware of the physical locations or configurations of the systems or hardware providing these services. In various

embodiments, the cloud computing provides the services via a wide area network (such as Internet) using suitable protocols. For example, a cloud computing provider provides applications over the wide area network, which can be accessed through a web browser or any other computing components. The software or components of the cloud computing architecture and corresponding data may be stored on a server at a remote position. The computing resources in the cloud computing environment may be merged or distributed at locations in a remote data center. Cloud computing infrastructures may provide the services through a shared data center, though they behave as a single access point for the users. Therefore, the cloud computing architectures may be used to provide the components and functionalities described herein from a service provider at a remote location. Alternatively, they may be provided from a conventional server or installed directly or otherwise on a client device.

[0642] The computing device 5400 may be used to implement video encoding/decoding in embodiments of the present disclosure. The memory 5420 may include one or more video coding modules 5425 having one or more program instructions. These modules are accessible and executable by the processing unit 5410 to perform the functionalities of the various embodiments described herein.

[0643] In the example embodiments of performing video encoding, the input device 5450 may receive video data as an input 5470 to be encoded. The video data may be processed, for example, by the video coding module 5425, to generate an encoded bitstream. The encoded bitstream may be provided via the output device 5460 as an output 5480.

[0644] In the example embodiments of performing video decoding, the input device 5450 may receive an encoded bitstream as the input 5470. The encoded bitstream may be processed, for example, by the video coding module 5425, to generate decoded video data. The decoded video data may be provided via the output device 5460 as the output 5480.

[0645] While this disclosure has been particularly shown and described with references to preferred embodiments thereof, it will be understood by those skilled in the art that

various changes in form and details may be made therein without departing from the spirit and scope of the present application as defined by the appended claims. Such variations are intended to be covered by the scope of this present application. As such, the foregoing description of embodiments of the present application is not intended to be limiting.

I/We Claim:

1. A method for video processing, comprising:
deriving, for a conversion between a video unit of a video and a bitstream of the video, a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to the video unit being coded with a first target coding mode; and
performing the conversion based on the LIC parameter.
2. The method of claim 1, wherein at least one of: a reconstructed sample of the neighbouring row or a reconstructed sample of a column adjacent to the current block is excluded to derive the LIC parameter.
3. The method of claim 1, wherein a template comprises one or more neighbouring reconstructed samples, wherein W^T represents a width of the template and H^T represents a height of the template.
4. The method of claim 3, wherein a sample of an above template is used to derive the LIC parameters.
5. The method of claim 3, wherein a sample of a left template is used to derive the LIC parameters.
6. The method of claim 3, wherein a sample of a left-bottom template is used to derive the LIC parameters.
7. The method of claim 3, wherein a sample of a right-above template is used to derive the LIC parameters.
8. The method of claim 3, wherein a sample of a left-above template is used to derive the LIC parameters.

9. The method of claim 3, wherein a plurality of the following templates: above template, left template, right-above template, left-bottom template, and left-above template are combined into one template that is used to derive the LIC parameter.

10. The method of claim 3, wherein a L-shape template is used to derive the LIC parameter.

11. The method of claim 3, wherein a non-adjacent template is used to derive the LIC parameter.

12. The method of claim 3, wherein at least one of: W^T or H^T equals to 1.

13. The method of claim 3, wherein the template comprises a plurality of rows and/or a plurality of columns.

14. The method of claim 3, wherein samples of the template are in the same coding tree unit (CTU), the same CTU row, the same CTU tile, or the same CTU slice of the video unit.

15. The method of claim 14, wherein samples of the template in a different CTU, a different CTU row, a different CTU tile, or a different CTU slice of the video unit are excluded for deriving LIC parameters.

16. The method of claim 1, wherein the template comprises at least one of: a neighbouring reconstructed sample of a row except for a nearest row, or a neighbouring reconstructed sample of a column except for a nearest column, wherein line X represents at least one of: the row, or the column.

17. The method of claim 16, wherein X is an integer larger than 0.

18. The method of claim 16, wherein a left-above sample of the line X is used.

19. The method of claim 16, wherein samples of a plurality of rows or columns are used.

20. The method of claim 16, wherein at least one of: the row or the column is in the same CTU, the same CTU row, the same CTU tile, or the same CTU slice of the video unit.

21. The method of claim 1, wherein if above samples are not available, a left sample is used to derive the LIC parameter.

22. The method of claim 1, wherein if left samples are not available, an above sample is used to derive the LIC parameter.

23. The method of claim 22, wherein if a sample is located in a different video unit or the sample is not reconstructed or a cross-filtering video unit is disallowed, the sample is not available.

24. The method of claim 23, wherein if the sample is out of at least one of: a current picture, a current sub-picture, a current tile, a current slice, a current brick, a current CTU, a current processing unit, or any other current video unit, the sample is located in the different video unit.

25. The method of claim 23, wherein the first target coding mode is not used.

26. The method of claim 1, wherein a determination of a template is signalled, wherein the template is used to derive the LIC parameter.

27. The method of claim 26, wherein one or more syntax elements are used to indicate which template is used to derive the LIC parameter.

28. The method of claim 27, wherein an index of the template is signalled.

29. The method of claim 28, wherein the index of the template comprises an index of a line number.

30. The method of claim 28, wherein the index of the template comprises a predefined template.

31. The method of 30, wherein the predefined template comprises at least one of: a left template, an above template, or a left-above template.

32. The method of claim 27, wherein the one or more syntax elements are signalled if a condition is satisfied.

33. The method of claim 32, wherein the condition comprises at least one of:
whether LIC is allowed to be used,
whether LIC is used, or
whether one or more template samples are available.

34. The method of claim 26, wherein reordering is used in the determination of the template.

35. The method of claim 34, wherein if a set of templates are at least one of: predefined, signalled, or derived, and the reordering is performed for the set of templates, an indication of first M templates after reordering is signalled, wherein M is an integer number.

36. The method of claim 34, wherein at least one of: template matching cost, or bilateral matching cost is used in the reordering.

37. The method of claim 36, wherein different LIC parameters are derived using different templates, and

wherein a prediction of a current template is refined using the LIC parameter and used to calculate template matching (TM) cost.

38. The method of claim 37, wherein a current template comprises a neighbouring reconstructed sample.

39. The method of claim 37, wherein a current template is different from a template that is used to derive the LIC parameter.

40. The method of claim 39, wherein the template used to derive LIC parameter is a non-adjacent neighboring sample.

41. The method of claim 1, wherein a determination of a template is derived, wherein the template is used to derive the LIC parameter.

42. The method of claim 41, wherein at least one of: TM cost, or bilateral matching (BM) cost is used to determine the template.

43. The method of claim 41, wherein a reordering approach is used, and a best template is determined as a template with best cost.

44. The method of claim 43, wherein the best cost comprises a lowest TM cost.

45. The method of claim 1, wherein the first target coding mode is applied to advanced motion vector prediction (AMVP) mode.

46. The method of claim 45, wherein the AMVP mode comprises at least one of: regular AMVP mode, affine AMVP mode, TM AMVP mode, or intra block copy (IBC) AMVP mode.

47. The method of claim 45, wherein the first target coding mode is not used for TM AMVP mode.

48. The method of claim 47, wherein the first target coding mode is not used to construct a TM based AMVP candidate list construction.

49. The method of claim 45, wherein the first target coding mode is used for TM AMVP mode but the first target coding mode is not used to construct a TM based AMVP candidate list.

50. The method of claim 1, wherein the first target coding mode is applied to merge mode.

51. The method of claim 50, wherein the merge mode comprises at least one of: regular merge mode, sub-block-based merge mode, TM merge mode, affine merge mode, merge mode with motion vector differences (MMVD), or IBC merge mode.

52. The method of claim 50, wherein at least one of: a template, or an indication of the template is inherited for the merge mode, wherein at least one of: the template, or the indication of the template is used to derive the LIC parameter.

53. The method of claim 50, wherein at least one of: a template, or an indication of the template is signalled for the merge mode.

54. The method of claim 50, wherein at least one of: a template, or an indication of the template is derived for the merge mode.

55. The method of claim 50, wherein the first target coding mode is applied to an adaptive reordering of merge candidates (ARMC) process.

56. The method of claim 55, wherein the first target coding mode is applied to an ARMC process of a coding tool.

57. The method of claim 50, wherein the first target coding mode is not applied to an ARMC process.

58. The method of claim 1, wherein a derivation of the LIC parameter used in the first target coding mode is same as a derivation of the LIC parameter used in LIC.

59. The method of claim 1, wherein a derivation of the LIC parameter used in the first target coding mode is different from a derivation of the LIC parameter used in LIC.

60. The method of claim 1, wherein at least one of: a template used in a current video unit, or an index of the template used in a current video unit is used by a following video unit.

61. The method of claim 60, wherein the following video unit comprises at least one of: a luma video unit, or a chroma video unit.

62. The method of claim 1, wherein the first target coding mode is applied to a coding tool.

63. The method of claim 62, wherein the coding tool comprises an inter coding tool.

64. The method of claim 63, wherein the inter coding tool comprises a specific coding tool, wherein the specific coding tool is at least one of: combined inter and intra prediction (CIIP), bi-prediction with CU-level weight (BCW), merge mode with motion vector difference (MMVD), template matching (TM), affine, decoder side motion vector refinement (DMVR), multi-pass DMVR, prediction refinement with optical flow (PROF), bi-directional optical flow (BDOF), sample based BDOF, adaptive decoder-side motion vector refinement (ADMVR), overlapped block motion compensation (OBMC), TM based OBMC, multi-hypothesis prediction (MHP), geometric partitioning mode (GPM), bilateral matching advanced motion vector prediction (AMVP)-merge mode, template matching AMVP-merge mode, a variant of CIIP, a variant of BCW, a variant of MMVD, a variant of TM, a variant of affine, a variant of DMVR, a variant of multi-pass DMVR, a variant of PROF, a variant of BDOF, a variant of sample based BDOF, a variant of ADMVR, a variant of OBMC, a variant of TM based OBMC, a variant of MHP, a variant of GPM, a variant of bilateral matching AMVP-merge mode, or a variant of template matching AMVP-merge mode.

65. The method of claim 64, wherein the CIIP is at least one of: CIIP-Planar, CIIP-TIMD, CIIP-TM.

66. The method of claim 64, wherein the BCW is BCW index derived by TM.

67. The method of claim 64, wherein the MMVD is at least one of: MMVD or TM based reordering for MMVD.

68. The method of claim 64, wherein the affine is at least one of: affine-MMVD, TM based reordering for affine MMVD.

69. The method of claim 64, wherein the GPM is at least one of: GPM, GPM-TM, GPM-MMVD, or GPM-intra.

70. The method of claim 63, wherein the first target coding mode is not used for one or more inter coding tools.

71. The method of claim 70, wherein the first target coding mode is not used for an affine mode.

72. The method of claim 70, wherein the first target coding mode is not used for an AMVP-merge mode.

73. The method of claim 70, wherein the first target coding mode is not used for a BCW.

74. The method of claim 70, wherein the first target coding mode is not used for an AMVR.

75. The method of claim 70, wherein the first target coding mode is not used for a motion vector resolution.

76. The method of claim 70, wherein the first target coding mode is not used for symmetric motion vector differences (SMVD).

77. The method of claim 62, wherein the first target coding mode is applied to at least one of: uni-prediction, or bi-prediction.

78. The method of claim 1, wherein LIC is not applied to a coding tool.

79. The method of claim 78, wherein the coding tool comprises SMVD.

80. The method of claim 1, wherein an indication of whether to and/or how to apply the first target coding mode depends on coding information.

81. The method of claim 80, wherein the coding information comprises at least one of the followings:

- an indication of whether a coding approach is allowed,
- a block dimension,
- a block size,
- a block depth,
- a slice type,

a picture type,
a partition tree type,
a temporal layer identification,
a block location,
colour format, or
a colour component.

82. The method of claim 81, wherein the coding approach comprises LIC.

83. The method of claim 81, wherein if a block size of a block is less than or equals to a first threshold, the block is allowed to be coded with the first target coding mode.

84. The method of claim 83, wherein the first threshold equals to one of: 64, 128, 256, 512, 1024, 2048, or 4096.

85. The method of claim 81, wherein if a block size of a block is larger than or equals to a second threshold, the block is allowed to be coded with the first target coding mode.

86. The method of claim 85, wherein the second threshold equals to one of: 8, 16, 32, 64, 128, 256, or 512.

87. The method of claim 81, wherein if at least one of: a block width of a block is larger than or equals to a third threshold, or a block height of the block is larger than or equals to a fourth threshold, the block is allowed to be coded with the first target coding mode.

88. The method of claim 87, wherein the third threshold equals to one of: 4, 8, 16, 32, or 64.

89. The method of claim 87, wherein the fourth threshold equals to one of: 4, 8, 16, 32, or 64.

90. The method of claim 81, wherein if at least one of: a block width of a block is less than or equals to a fifth threshold, or a block height of the block is less than or equals to a sixth threshold, the block is allowed to be coded with the first target coding mode.

91. The method of claim 90, wherein the fifth threshold equals to one of: 8, 16, 32, 64, or 128.

92. The method of claim 90, wherein the sixth threshold equals to one of: 8, 16, 32, 64, or 128.

93. The method of claim 81, wherein at least one of: if W is larger than or equals to $S \cdot H$, a left template is not allowed to be used, or if H is larger than or equals to $S \cdot W$, an above template is not allowed to be used, wherein W represents a block width of a block, H represents a block height of a block, S represents a scale ratio.

94. The method of claim 81, wherein at least one of: if W is larger than or equals to $S \cdot H$, an above template is not allowed to be used, or if H is larger than or equals to $S \cdot W$, a left template is not allowed to be used, wherein W represents a block width of a block, H represents a block height of a block, S represents a scale ratio.

95. The method of claim 81, wherein the block size comprises a luma block size.

96. The method of claim 81, wherein the block size comprises a chroma block size.

97. The method of claim 81, wherein the partition tree type comprises at least one of: single tree, dual tree, or local dual tree.

98. The method of claim 81, wherein if a current video unit is at a left boundary of a current picture, a left template is not used.

99. The method of claim 81, wherein if at least one of: a current video unit is at an above boundary of current picture, or the current video is at an above boundary of a current CTU, an above template is not used.

100. The method of claim 81, wherein the first target coding mode is applied to all colour components.

101. The method of claim 81, wherein if the first target coding mode is applied to a chroma component, the first target coding mode is different from a first target coding mode applied to a luma component.

102. The method of claim 81, wherein an indication of whether to and/or how to apply the first target coding mode to a first component depends on an indication of whether to and/or how to apply the first target coding mode to a second component.

103. The method of claim 102, wherein the first component comprises a chroma component, and the second component comprises a luma component.

104. The method of claim 103, wherein the chroma component comprises at least one of: Cb, or Cr.

105. The method of claim 103, wherein the luma component comprises Y.

106. The method of claim 102, wherein a way to apply the first target coding mode to the first component is same as a way to apply the first target coding mode to the second component.

107. The method of claim 102, wherein a way to apply the first target coding mode to the first component is different from a way to apply the first target coding mode to the second component.

108. The method of claim 102, wherein at least one of: a template of a current chroma block, or an indication of the template of the current chroma block is inherited from a luma block.

109. The method of claim 81, wherein the first target coding mode is applied to a luma component, but the first target coding mode is not applied to a chroma component.

110. The method of claim 109, wherein the luma component comprises at least one of: Y in YCbCr colour space, or G in RGB colour space.

111. The method of claim 109, wherein the chroma component comprises at least one of: Cb in YCbCr colour space, Cr in YCbCr colour space, R in RGB colour space, or B in RGB colour space.

112. The method of claim 1, wherein if a condition is satisfied, an indication of the first target coding mode is signalled.

113. The method of claim 112, wherein the condition comprises at least one of the followings:

- a block dimension,
- a block size,
- a block depth,
- a slice type,
- a picture type,
- a partition tree type,
- a temporal layer identification,
- a block location,
- colour format, or
- a colour component.

114. The method of claim 113, wherein if a block size of a block is less than or equals to a seventh threshold, the indication of the first target coding mode is not signalled.

115. The method of claim 114, wherein the seventh threshold equals to one of: 64, 128, 256, 512, 1024, 2048, or 4096.

116. The method of claim 113, wherein if a block size of a block is less than or equals to an eighth threshold, the indication of the first target coding mode is not signalled.

117. The method of claim 116, wherein the eighth threshold equals to one of: 16, 32, 64, 128, 256, or 512.

118. The method of claim 113, wherein if at least one of: a block width of a block is larger than or equals to a ninth threshold, or a block height of the block is larger than or equals to a tenth threshold, the indication of the first target coding mode is signalled.

119. The method of claim 118, wherein the ninth threshold equals to one of: 4, 8, 16, 32, or 64.

120. The method of claim 118, wherein the tenth threshold equals to one of: 4, 8, 16, 32, or 64.

121. The method of claim 113, wherein if at least one of: a block width of a block is less than or equals to an eleventh threshold, or a block height of the block is less than or equals to a twelfth threshold, the indication of the first target coding mode is signalled.

122. The method of claim 121, wherein the eleventh threshold equals to one of: 8, 16, 32, 64, or 128.

123. The method of claim 121, wherein the twelfth threshold equals to one of: 8, 16, 32, 64, or 128.

124. The method of any of claims 114-123, wherein at least one of thresholds: the seventh threshold, the eighth threshold, the ninth threshold, the tenth threshold, the eleventh threshold, or the twelfth threshold depends on at least one of: slice type, or picture type.

125. The method of claim 113, wherein the block size comprises a luma block size.

126. The method of claim 113, wherein the block size comprises a chroma block size.

127. The method of claim 113, wherein the partition tree type comprises at least one of: single tree, dual tree, or local dual tree.

128. The method of claim 113, wherein if a current video unit is at the left boundary of a current picture, the indication of the first target coding mode is not signalled.

129. The method of claim 113, wherein if at least one of: a current video unit is at the above boundary of current picture, or the current video is at an above boundary of a current CTU, the indication of the first target coding mode is not signalled.

130. The method of claim 1, wherein an indication of whether a current block is coded with the first target coding mode is signalled using one or more syntax elements.

131. The method of claim 130, wherein the one or more syntax elements are binarized with at least one of: fixed length coding, truncated unary coding, unary coding, EG coding, or coded a flag.

132. The method of claim 130, wherein the one or more syntax elements are bypass coded or context coded.

133. The method of claim 132, wherein context of the one or more syntax elements depends on coded information.

134. The method of claim 133, wherein the coded information comprises at least one of the followings:

- a block dimension,
- a block size,
- a slice type,
- a picture type,
- information of a neighbouring block,
- information of another coding tool used for the current block, or
- information of a temporal layer.

135. The method of claim 134, wherein the neighbouring block is adjacent or non-adjacent.

136. The method of claim 130, wherein the one or more syntax elements are signalled at one of the followings:

- a sequence header,
- a picture header,

- a sequence parameter set (SPS),
- a video parameter set (VPS),
- a dependency parameter set (DPS),
- a decoding capability information (DCI),
- a picture parameter set (PPS),
- an adaptation parameter sets (APS),
- a slice header, or
- a tile group header.

137. The method of claim 130, wherein the one or more syntax elements are coded in a predictive way.

138. The method of claim 137, wherein one or more syntax elements of the current block are predicted by one or more syntax elements of a neighboring block.

139. The method of claim 130, wherein the one or more syntax elements are coded if a condition is satisfied.

140. The method of claim 139, wherein if a first syntax element indicates that the first target coding mode is applicable, a second syntax element is signalled to indicate whether the first target coding mode is used.

141. The method of claim 140, wherein the first syntax element is signalled at one of the followings:

- a sequence header,
- a picture header,
- a sequence parameter set (SPS),
- a video parameter set (VPS),
- a dependency parameter set (DPS),
- a decoding capability information (DCI),
- a picture parameter set (PPS),
- an adaptation parameter sets (APS),
- a slice header, or
- a tile group header.

142. The method of claim 140, wherein the second syntax element is for a block.

143. The method of claim 139, wherein if a second syntax element indicates that the first target coding mode is used, a third syntax element is signalled to indicate a way of performing the first target coding mode.

144. The method of claim 1, wherein the first target coding mode is applied to another coding tool.

145. The method of claim 144, wherein the other coding tool comprises at least one of: a variant of LIC, or another coding tool using a neighbouring sample to derive one or more parameters.

146. The method of claim 145, wherein the variant of LIC comprises LIC with filtering which uses a plurality of parameters.

147. A method for video processing, comprising:
applying, for a conversion between a video unit of a video and a bitstream of the video, a plurality of local illumination compensation (LIC) models to the video unit, in response to the video unit being coded with a second target coding mode; and
performing the conversion based on the plurality of LIC models.

148. The method of claim 147, wherein neighbouring reconstructed samples are divided into N sets, and parameters of each LIC model are derived from each set, wherein N is an integer larger than 1.

149. The method of claim 148, wherein a way to divide the neighbouring reconstructed samples into the N sets is based on coding information.

150. The method of claim 149, wherein a sample value is used to divide the neighbouring reconstructed samples into the N sets.

151. The method of claim 150, wherein one or more thresholds based on the sample value are used.

152. The method of claim 151, wherein if N equals to 2, a first threshold is used to divide the neighbouring reconstructed samples into two sets.

153. The method of claim 152, wherein the first threshold equals to a mean of all the neighbouring reconstructed samples.

154. The method of claim 151, wherein if N equals to 3, a first threshold and a second threshold are used to divide the neighbouring reconstructed samples into three sets.

155. The method of claim 151, wherein the one or more thresholds are based on at least one of: a colour component, or colour format.

156. The method of claim 150, wherein the way to divide the neighbouring reconstructed samples into the N sets is based on at least one of: a block size, or a block dimension.

157. The method of claim 148, wherein a way to divide the neighbouring reconstructed samples into the N sets is signalled.

158. The method of claim 147, wherein the second target coding mode is applied to a coding tool.

159. The method of claim 158, wherein the coding tool comprises an inter coding tool.

160. The method of claim 159, wherein the inter coding tool comprises a specific coding tool, wherein the specific coding tool is at least one of: combined inter and intra prediction (CIIP), bi-prediction with CU-level weight (BCW), merge mode with motion vector difference (MMVD), template matching (TM), affine, decoder side motion vector refinement (DMVR), multi-pass DMVR, prediction refinement with optical flow (PROF), bi-directional optical flow (BDOF), sample based BDOF, adaptive decoder-side motion vector refinement (ADMVR), overlapped block motion compensation (OBMC), TM based OBMC, multi-hypothesis

prediction (MHP), geometric partitioning mode (GPM), bilateral matching advanced motion vector prediction (AMVP)-merge mode, template matching AMVP-merge mode, a variant of CIIP, a variant of BCW, a variant of MMVD, a variant of TM, a variant of affine, a variant of DMVR, a variant of multi-pass DMVR, a variant of PROF, a variant of BDOF, a variant of sample based BDOF, a variant of ADMVR, a variant of OBMC, a variant of TM based OBMC, a variant of MHP, a variant of GPM, a variant of bilateral matching AMVP-merge mode, or a variant of template matching AMVP-merge mode.

161. The method of claim 160, wherein the CIIP is at least one of: CIIP-Planar, CIIP-TIMD, CIIP-TM.

162. The method of claim 160, wherein the BCW is BCW index derived by TM.

163. The method of claim 160, wherein the MMVD is at least one of: MMVD or TM based reordering for MMVD.

164. The method of claim 160, wherein the affine is at least one of: affine-MMVD, TM based reordering for affine MMVD.

165. The method of claim 160, wherein the GPM is at least one of: GPM, GPM-TM, GPM-MMVD, or GPM-intra.

166. The method of claim 159, wherein the second target coding mode is not used for one or more inter coding tools.

167. The method of claim 166, wherein the second target coding mode is not used for an affine mode.

168. The method of claim 166, wherein the second target coding mode is not used for an AMVP-merge mode.

169. The method of claim 166, wherein the second target coding mode is not used for a BCW.

170. The method of claim 166, wherein the second target coding mode is not used for an AMVR.

171. The method of claim 166, wherein the second target coding mode is not used for a motion vector resolution.

172. The method of claim 166, wherein the second target coding mode is not used for symmetric motion vector differences (SMVD).

173. The method of claim 158, wherein the second target coding mode is applied to at least one of: uni-prediction, or bi-prediction.

174. The method of claim 147, wherein an indication of whether to and/or how to apply the second target coding mode depends on coding information.

175. The method of claim 174, wherein the coding information comprises at least one of the followings:

- an indication of whether a coding approach is allowed,
- a block dimension,
- a block size,
- a block depth,
- a slice type,
- a picture type,
- a partition tree type,
- a temporal layer identification,
- a block location,
- colour format, or
- a colour component.

176. The method of claim 175, wherein the partition tree type comprises at least one of: single tree, dual tree, or local dual tree.

177. The method of claim 147, wherein if a condition is satisfied, the second target coding mode is signalled.

178. The method of claim 177, wherein the condition comprises at least one of the followings:

- a block dimension,
- a block size,
- a block depth,
- a slice type,
- a picture type,
- a partition tree type,
- a temporal layer identification,
- a block location,
- colour format, or
- a colour component.

179. The method of claim 178, wherein the partition tree type comprises at least one of: single tree, dual tree, or local dual tree.

180. The method of claim 147, wherein an indication of whether a current block is coded with the second target coding mode is signalled using one or more syntax elements.

181. The method of claim 180, wherein the one or more syntax elements are binarized with at least one of: fixed length coding, truncated unary coding, unary coding, EG coding, or coded a flag.

182. The method of claim 180, wherein the one or more syntax elements are bypass coded or context coded.

183. The method of claim 182, wherein context of the one or more syntax elements depends on coded information.

184. The method of claim 183, wherein the coded information comprises at least one of the followings:

- a block dimension,
- a block size,

- a slice type,
- a picture type,
- information of a neighbouring block,
- information of another coding tool used for the current block, or
- information of a temporal layer.

185. The method of claim 184, wherein the neighbouring block is adjacent or non-adjacent.

186. The method of claim 180, wherein the one or more syntax elements are signalled at one of the followings:

- a sequence header,
- a picture header,
- a sequence parameter set (SPS),
- a video parameter set (VPS),
- a dependency parameter set (DPS),
- a decoding capability information (DCI),
- a picture parameter set (PPS),
- an adaptation parameter sets (APS),
- a slice header, or
- a tile group header.

187. The method of claim 180, wherein the one or more syntax elements are coded in a predictive way.

188. The method of claim 187, wherein one or more syntax elements of the current block are predicted by one or more syntax elements of a neighboring block.

189. The method of any of claims 1-188, wherein the video unit comprises at least one of the followings:

- a colour component,
- a sub-picture,
- a slice,
- a tile,

- a coding tree unit (CTU),
- a CTU row,
- a group of CTU,
- a coding unit (CU),
- a prediction unit (PU),
- a transform unit (TU),
- a coding tree block (CTB),
- a coding block (CB),
- a prediction block (PB),
- a transform block (TB),
- a block,
- a sub-block of a block,
- a sub-region within a block,
- a region containing more than one sample or pixel.

190. The method of any of claims 1-188, wherein an indication of whether to and/or how to apply at least one of: the first target coding mode, or the second target coding mode is indicated at one of the followings:

- sequence level,
- group of pictures level,
- picture level,
- slice level, or
- tile group level.

191. The method of any of claims 1-188, wherein an indication of whether to and/or how to apply at least one of: the first target coding mode, or the second target coding mode is indicated in one of the followings:

- a sequence header,
- a picture header,
- a sequence parameter set (SPS),
- a video parameter set (VPS),
- a dependency parameter set (DPS),
- a decoding capability information (DCI),
- a picture parameter set (PPS),

an adaptation parameter sets (APS),
a slice header, or
a tile group header.

192. The method of any of claims 1-188, wherein whether to and/or how to apply at least one of: the first target coding mode, or the second target coding mode depends on at least one of the followings:

a message indicated in one of: DPS, SPS, VPS, PPS, APS, picture header, slice header, tile group header, coding tree unit (CTU), coding unit (CU), CTU row, group of CTUs, TU, PU block, video coding unit,

a position of a CU block,
a position of a PU block,
a position of a TU block,
a position of a video coding unit,
block dimension of a current block,
block dimension of a neighbouring block of the current block,
block shape of a current block,
block shape of a neighbouring block of the current block,
a coded mode of a block,
an indication of a colour format,
a coding tree structure,
a slice group type,
a tile group type,
a slice picture type,
a tile picture type,
a colour component,
an ID of a temporal layer,
a profile of a standard,
a level of a standard, or
a tier of a standard.

193. The method of claim 192, wherein the coded mode of the block is at least one of: IBC inter mode, non-IBC inter mode, or non-IBC subblock mode.

194. The method of claim 192, wherein the indication of a colour format is 4:2:0 or 4:4:4.

195. The method of claim 192, wherein the colour component is applied on a chroma component or a luma component.

196. The method of any of claims 1-188, wherein the syntax element is binarized as a flag, a fixed length code, an EG(x) code, a unary code, a truncated unary code, or a truncated binary code.

197. The method of claim 196, wherein the syntax element is signed or unsigned.

198. The method of any of claims 1-188, wherein the syntax element is coded with at least one context model, or bypass coded.

199. The method of any of claims 1-188, wherein the syntax element is signaled in at least one of following conditions:

- if a corresponding function is applicable, or
- if dimensions of the block satisfy a condition.

200. The method of claim 199, wherein the dimensions are width and/or height.

201. The method of any of claims 1-188, wherein the syntax element is signaled at one of the followings:

- a block level,
- a sequence level,
- a group of pictures level,
- a picture level,
- a slice level, or
- a tile group level.

202. The method of any of claims 1-188, wherein the syntax element is signaled in one of the followings:

- a coding structure of CTU,

- a coding structure of CU,
- a coding structure of TU,
- a coding structure of PU,
- a coding structure of CTB,
- a coding structure of CB,
- a coding structure of TB,
- a coding structure of PB,
- a sequence header,
- a picture header,
- a sequence parameter set (SPS),
- a video parameter set (VPS),
- a dependency parameter set (DPS),
- a decoding capability information (DCI),
- a picture parameter set (PPS),
- an adaptation parameter sets (APS),
- a slice header, or
- a tile group header.

203. The method of any of claims 1-188, wherein at least one of: the first target coding mode, or the second target coding mode is combined with at least one of the following coding tools:

- affine,
- multiple transform selection (MTS),
- low-frequency non-separable transform (LFNST),
- merge mode with motion vector difference (MMVD),
- matrix based intra prediction (MIP),
- intra sub-partitions (ISP),
- cross-component linear model (CCLM),
- convolutional cross-component model (CCCM),
- symmetric motion vector differences (SMVD),
- bi-directional optical flow (BDOF),
- decoder side motion vector refinement (DMVR),
- history-based MVP (HMVP),
- template matching,

intra block copy (IBC), or
Palette.

204. The method of any of claims 1-188, wherein at least one of: the first target coding mode, or the second target coding mode is excluded with at least one of the following coding tools:

affine,
multiple transform selection (MTS),
LFNST,
merge mode with motion vector difference (MMVD),
MIP,
ISP,
cross-component linear model (CCLM),
convolutional cross-component model (CCCM),
SMVD,
bi-directional optical flow (BDOF),
decoder side motion vector refinement (DMVR),
history-based MVP (HMVP),
template matching,
intra block copy (IBC), or
Palette.

205. The method of claim 204, wherein if at least one of: the first target coding mode, or the second target coding mode is used, the excluded coding tool is disabled without signaling.

206. The method of claim 204, wherein if the excluded coding tool is used, the at least one of: the first target coding mode, or the second target coding mode is disabled without signaling.

207. The method of any of claims 1-206, wherein the conversion includes encoding the video unit into the bitstream.

208. The method of any of claims 1-206, wherein the conversion includes decoding the video unit from the bitstream.

209. An apparatus for video processing comprising a processor and a non-transitory memory with instructions thereon, wherein the instructions upon execution by the processor, cause the processor to perform a method in accordance with any of claims 1-208.

210. A non-transitory computer-readable storage medium storing instructions that cause a processor to perform a method in accordance with any of claims 1-208.

211. A non-transitory computer-readable recording medium storing a bitstream of a video which is generated by a method performed by an apparatus for video processing, wherein the method comprises:

deriving a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to a video unit of the video being coded with a first target coding mode; and

generating the bitstream based on the LIC parameter.

212. A method for storing a bitstream of a video, comprising:

deriving a local illumination compensation (LIC) parameter using at least one of: a reconstructed sample out of a neighbouring row, or a reconstructed sample out of a column reconstructed sample adjacent to a current block, in response to a video unit of the video being coded with a first target coding mode;

generating the bitstream based on the LIC parameter; and

storing the bitstream in a non-transitory computer-readable recording medium.

213. A non-transitory computer-readable recording medium storing a bitstream of a video which is generated by a method performed by an apparatus for video processing, wherein the method comprises:

applying a plurality of local illumination compensation (LIC) models to a video unit of the video, in response to the video unit of the video being coded with a second target coding mode; and

generating the bitstream based on the plurality of LIC models.

214. A method for storing a bitstream of a video, comprising:

applying a plurality of local illumination compensation (LIC) models to a video unit of the video, in response to the video unit of the video being coded with a second target coding mode;

generating the bitstream based on the plurality of LIC models; and

storing the bitstream in a non-transitory computer-readable recording medium.

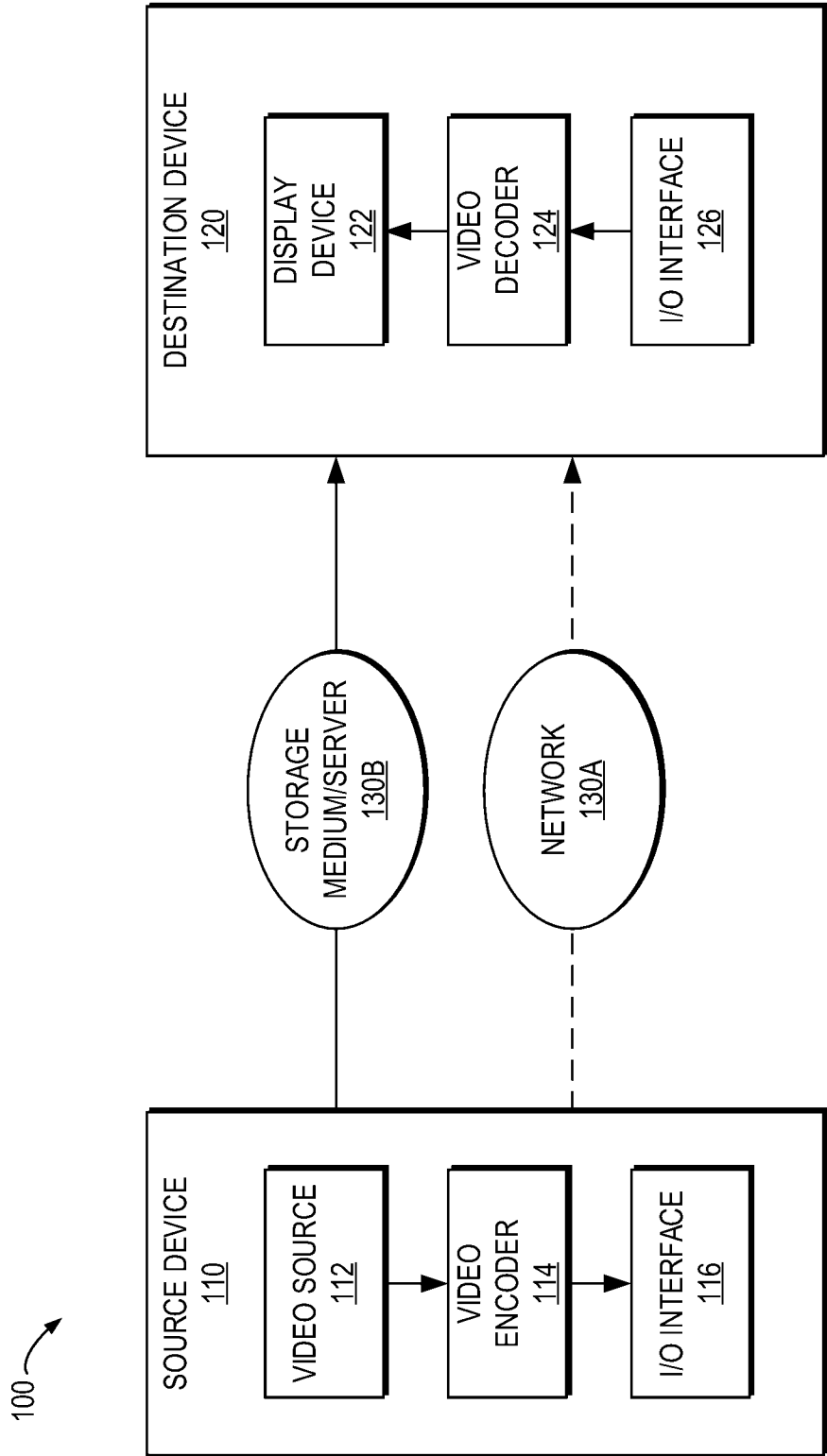


Fig. 1

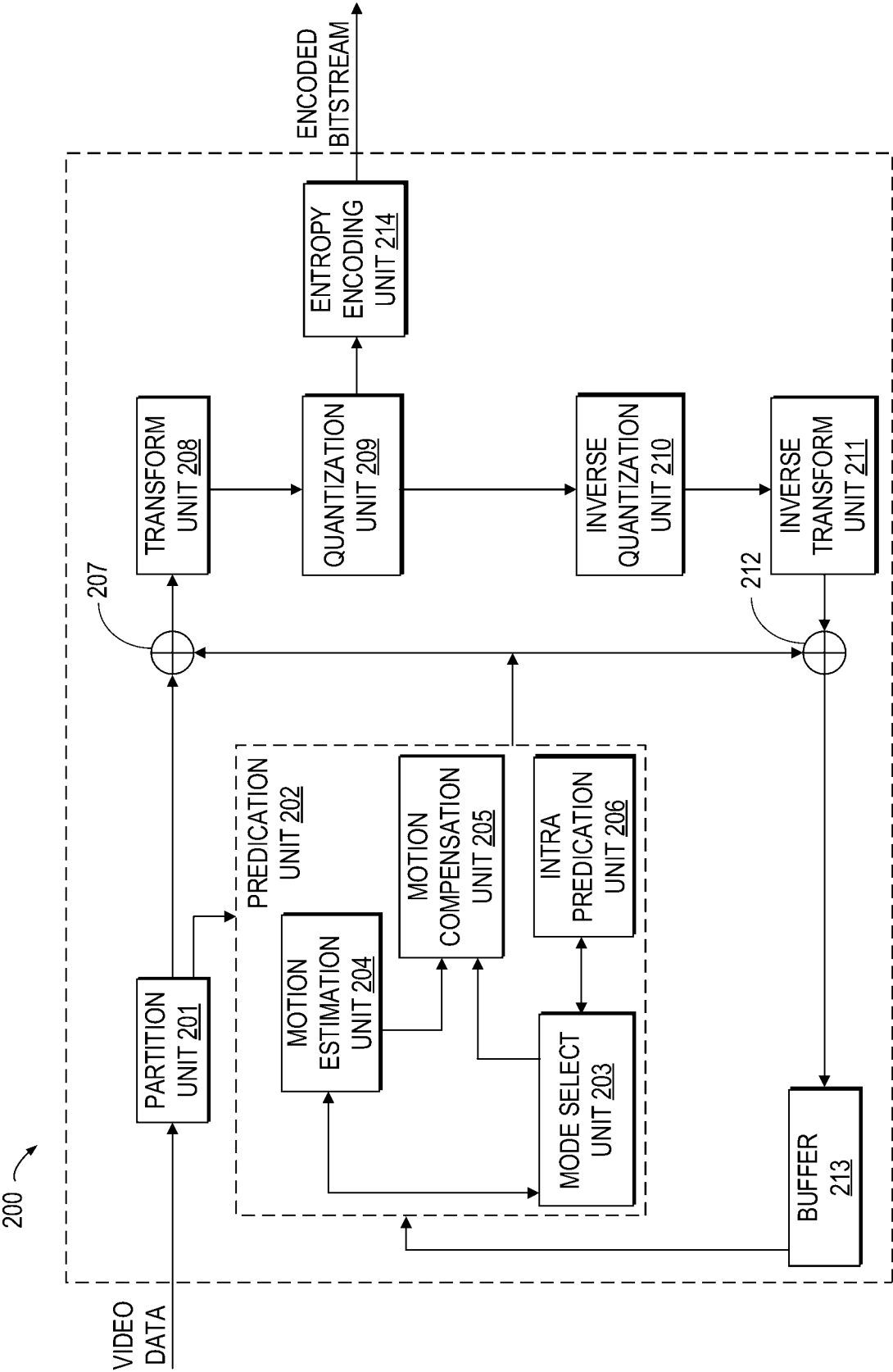


Fig. 2

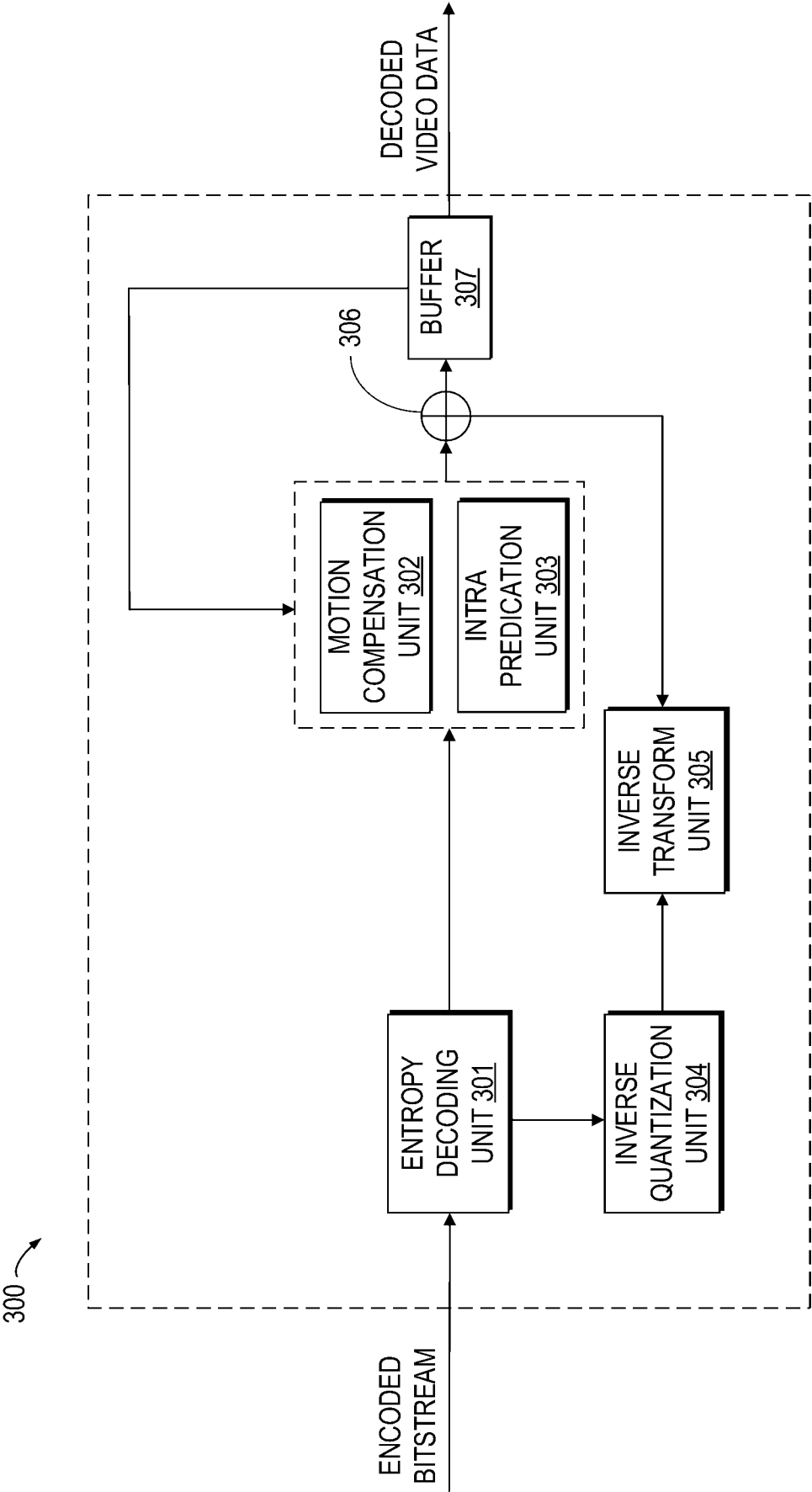


Fig. 3

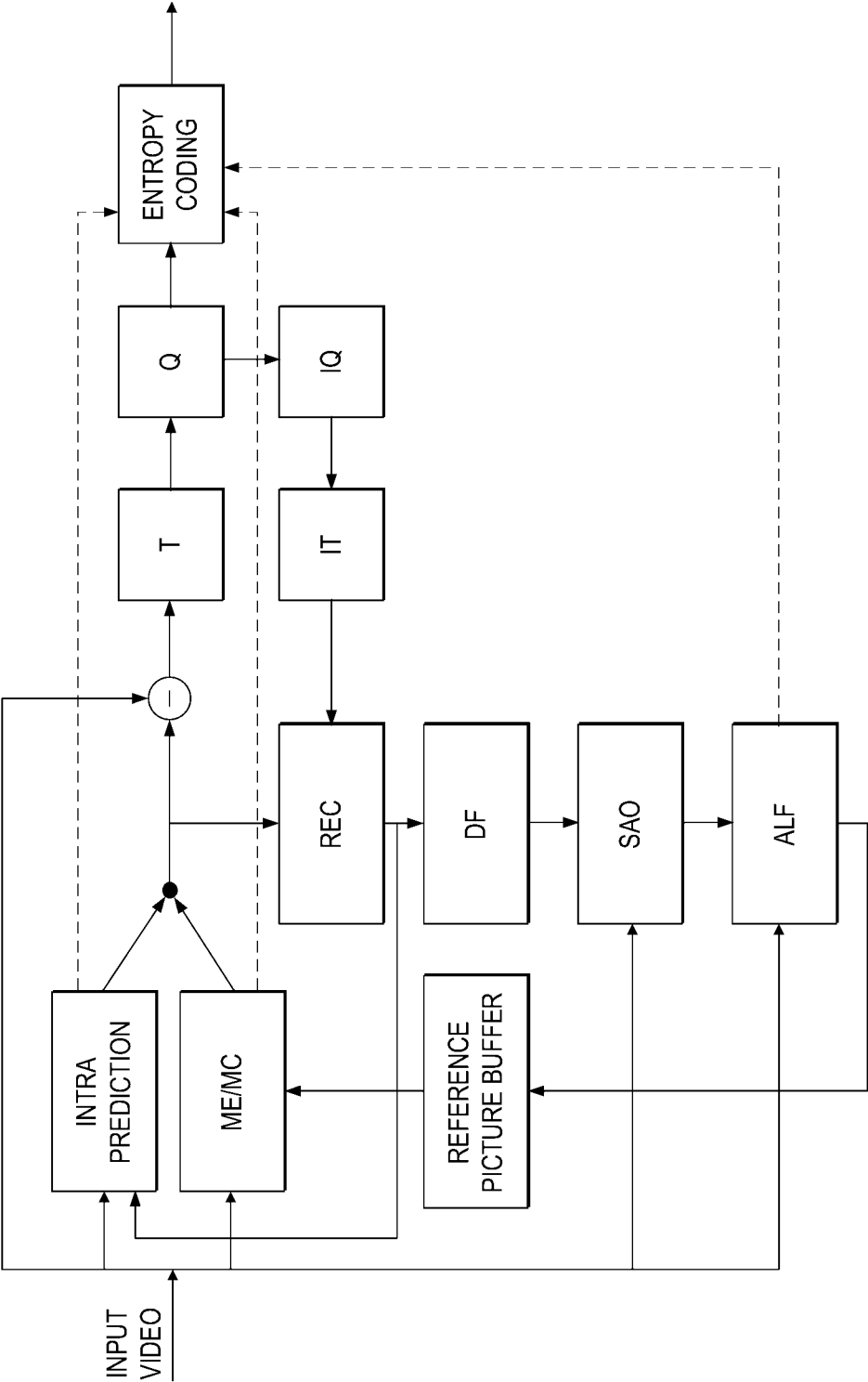


Fig. 4

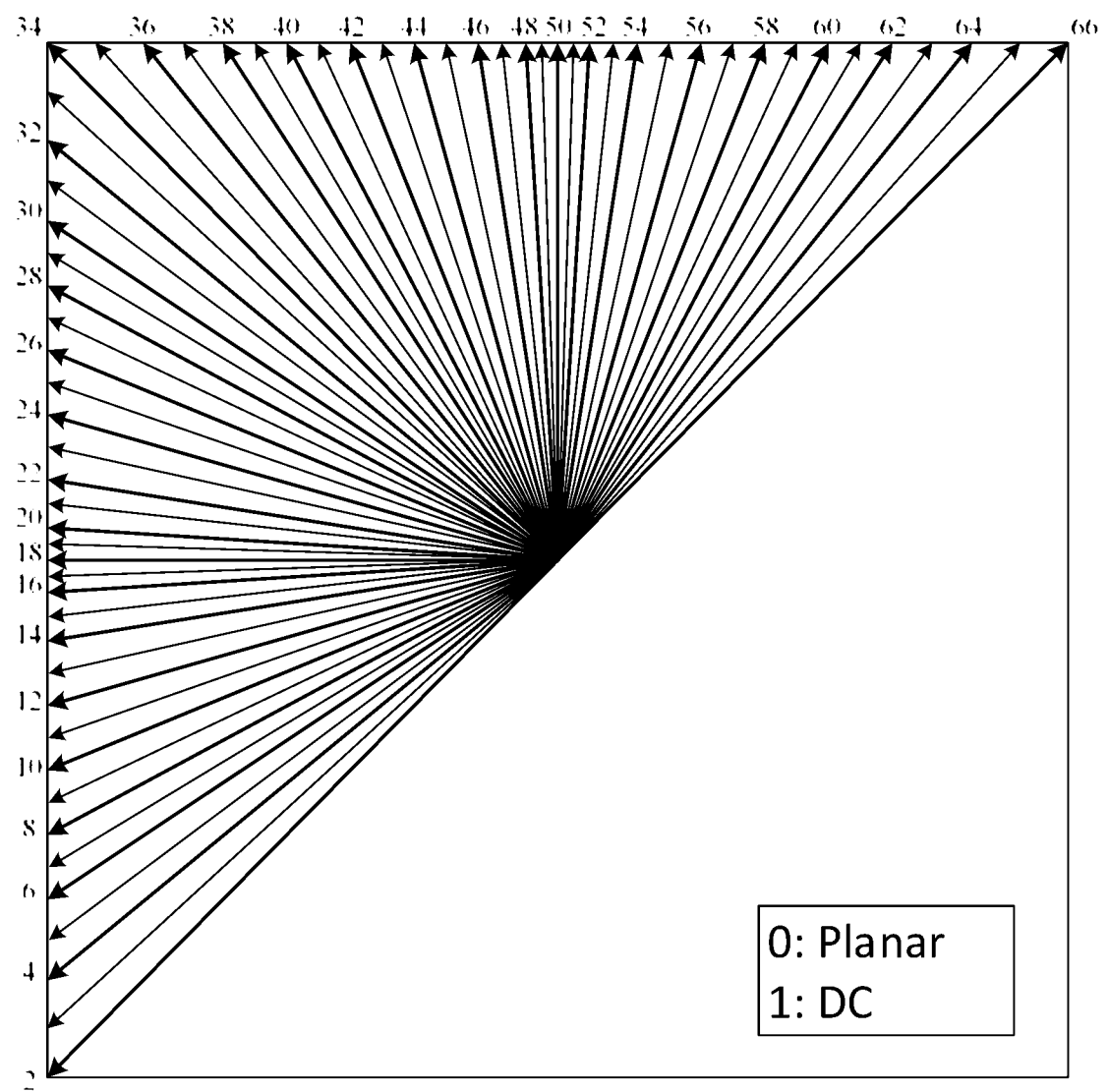


Fig. 5

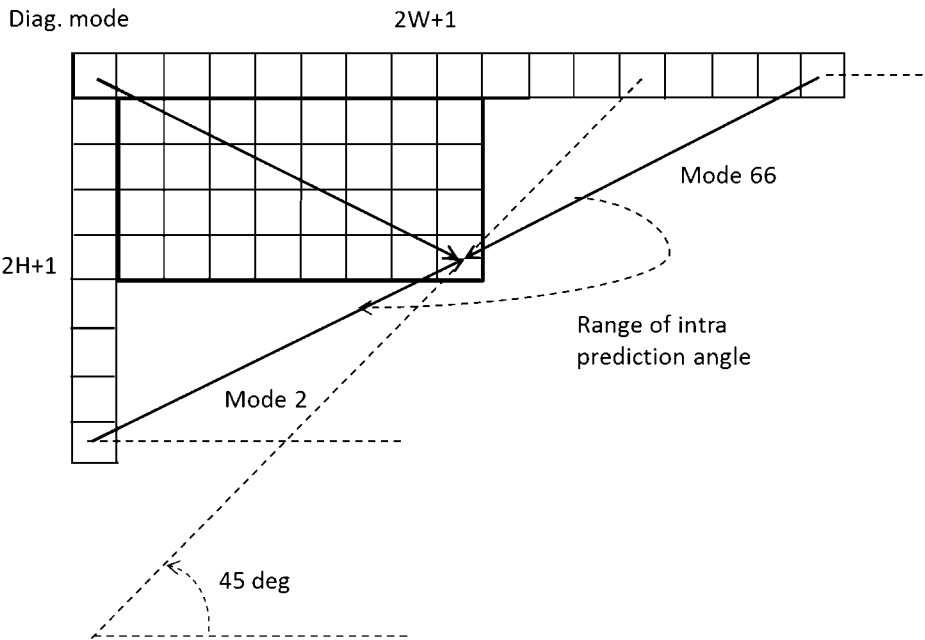


Fig. 6A

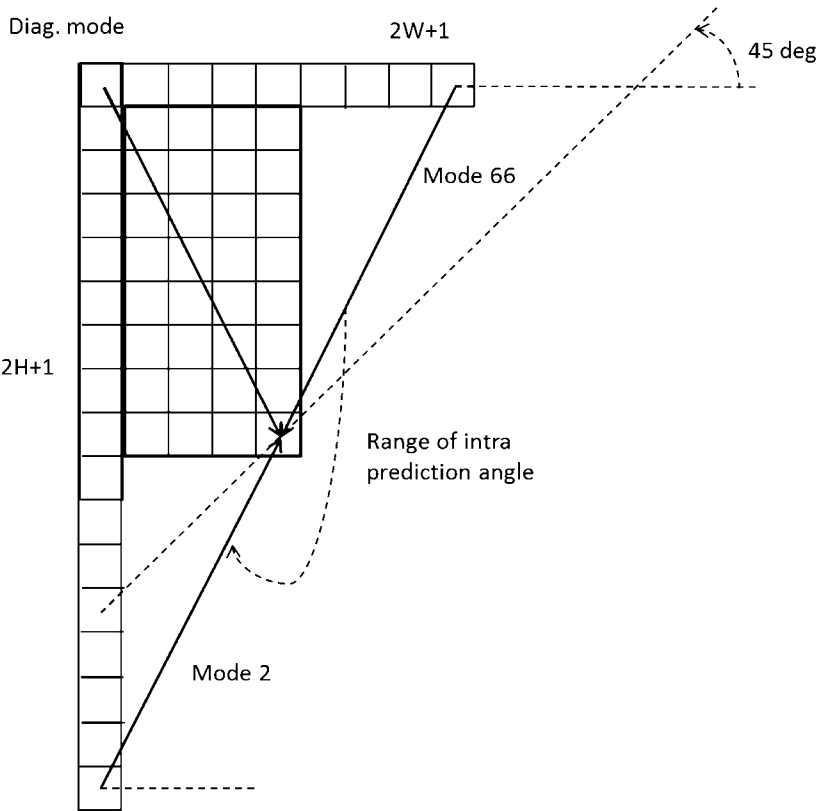


Fig. 6B

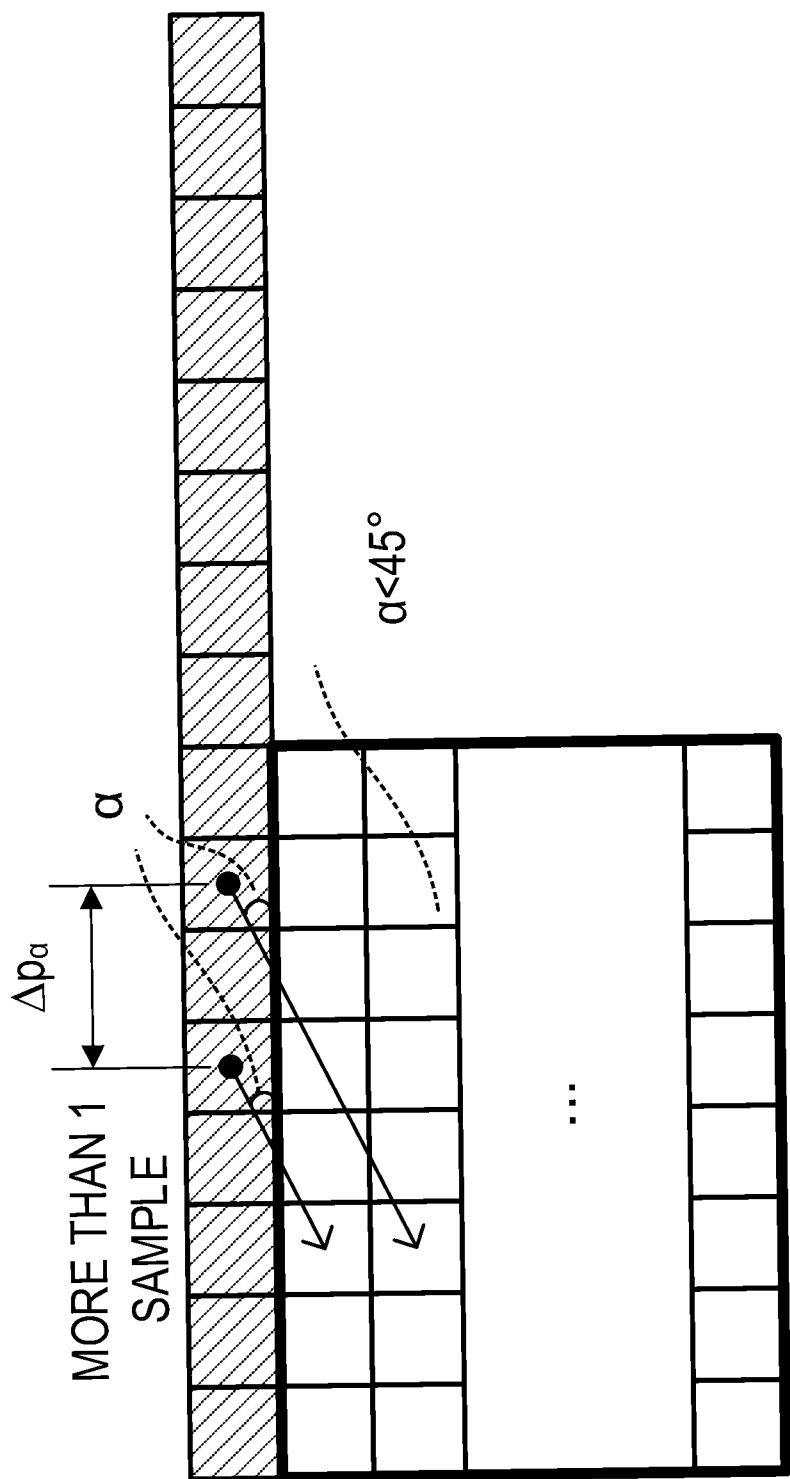


Fig. 7

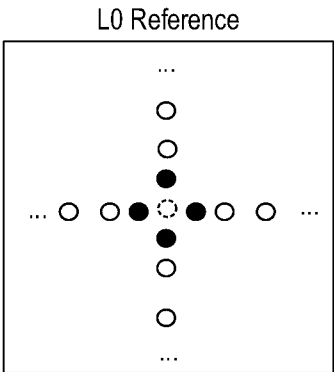


Fig. 8A

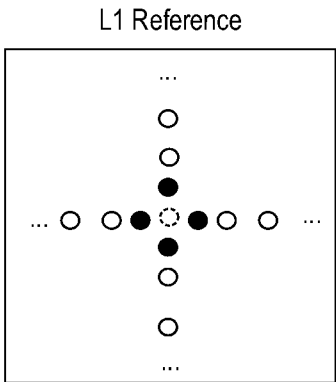


Fig. 8B

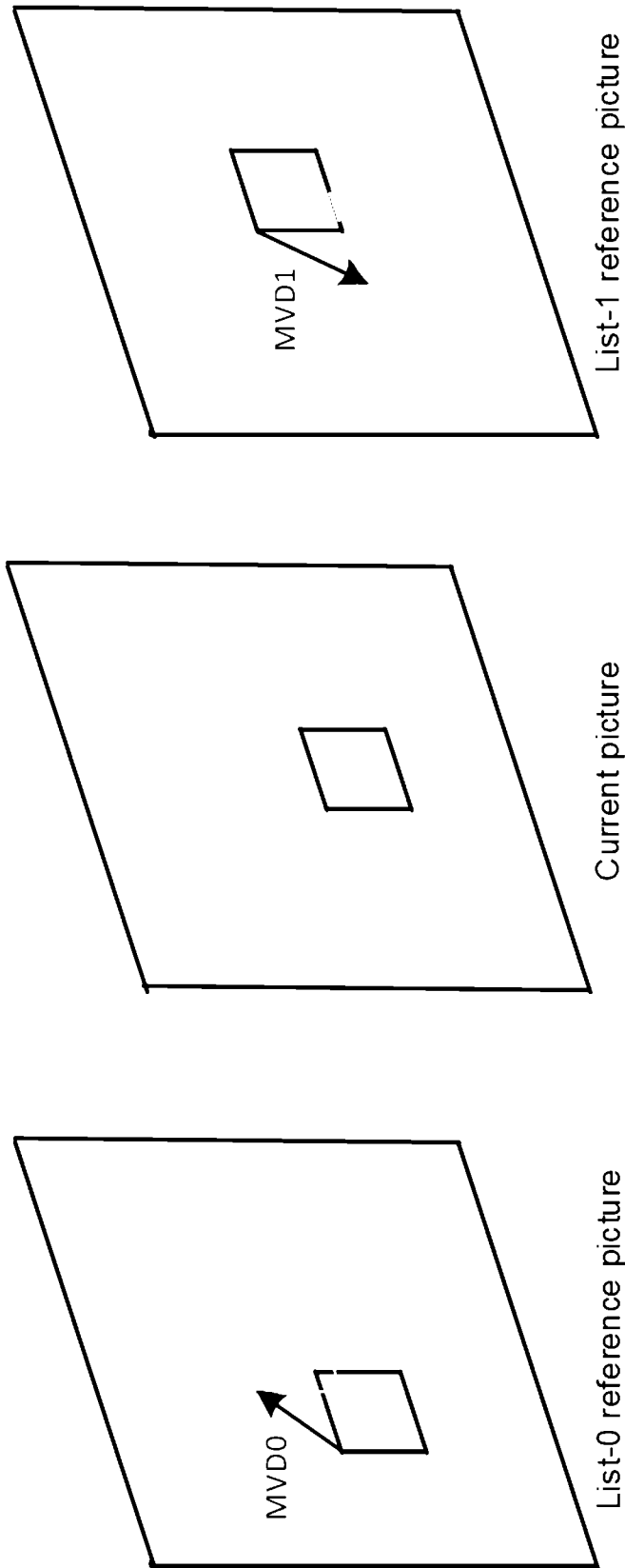


Fig. 9

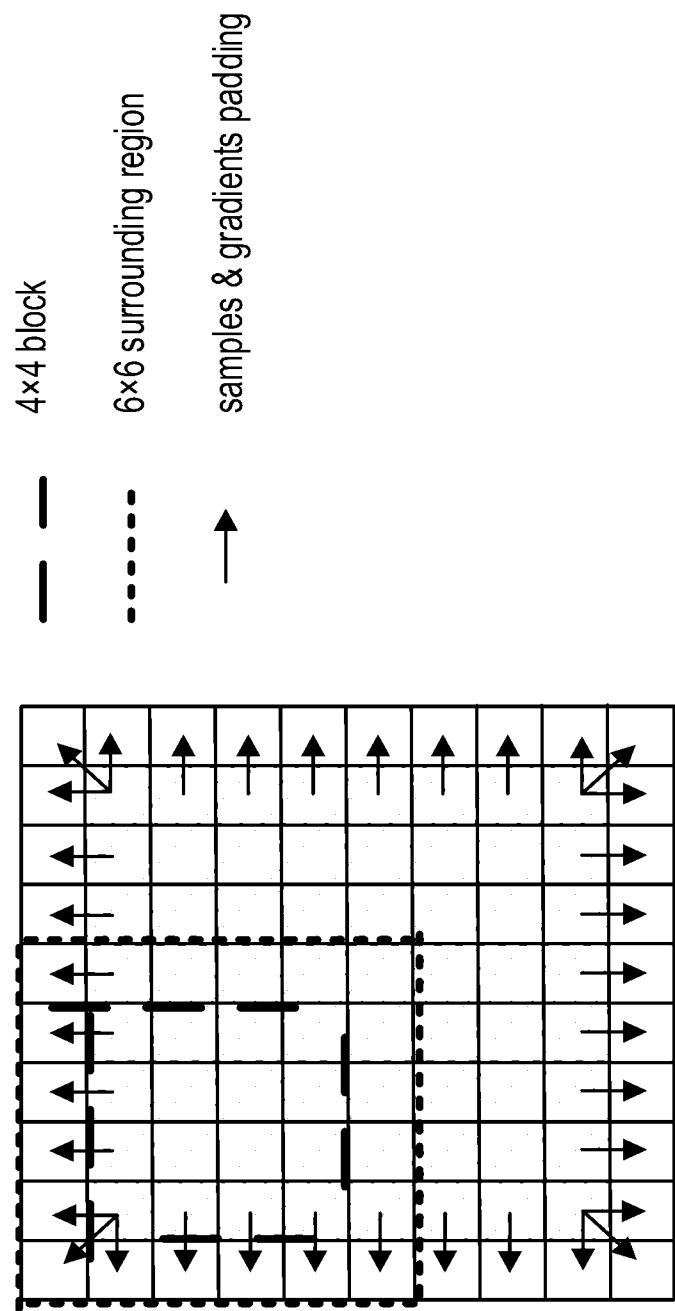


Fig. 10

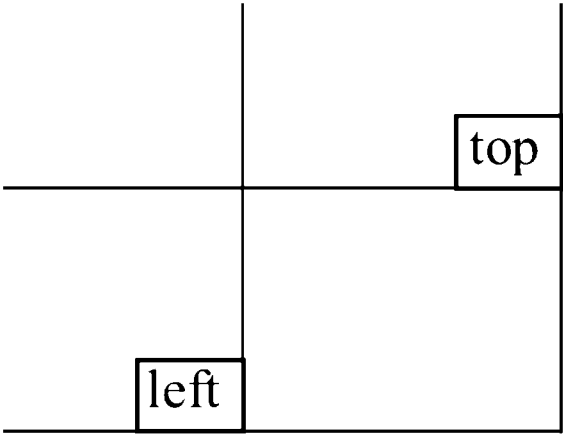


Fig. 11

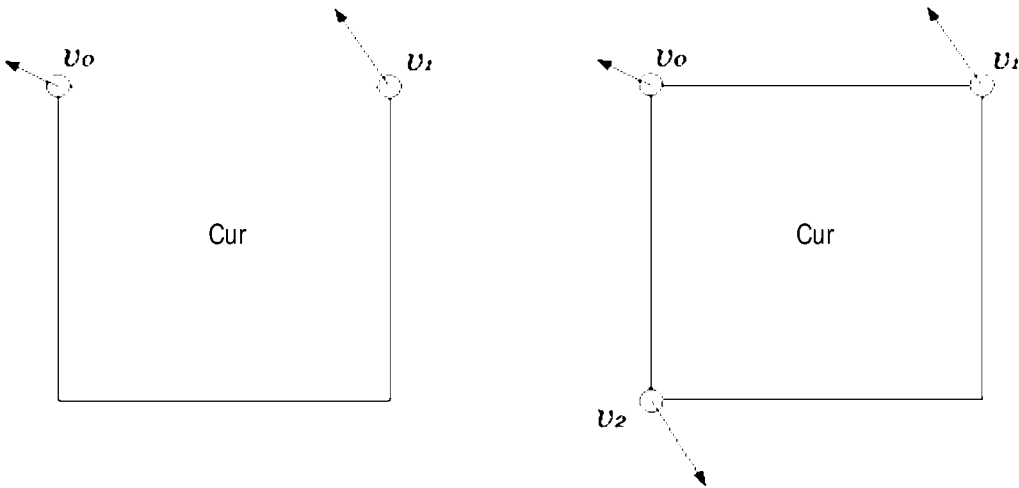


Fig. 12

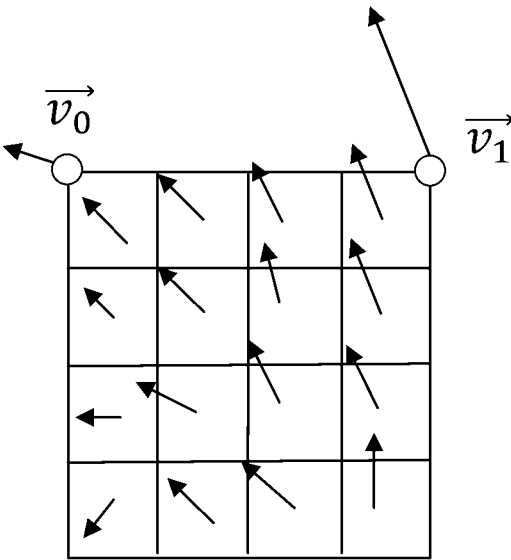


Fig. 13

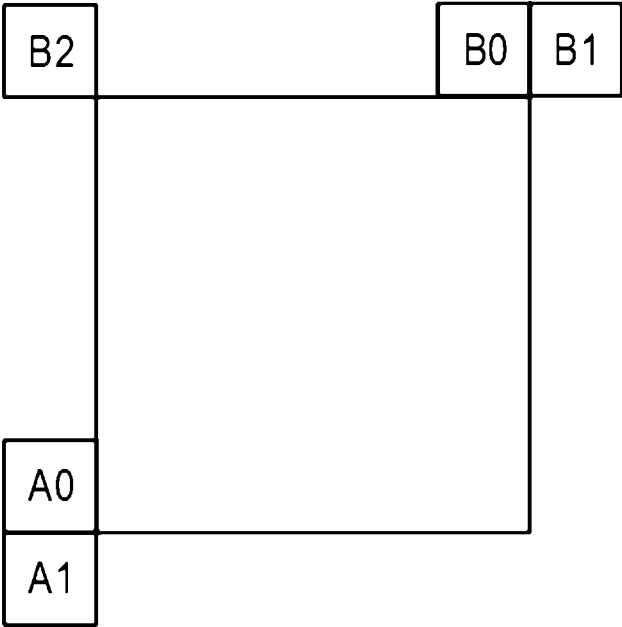


Fig. 14

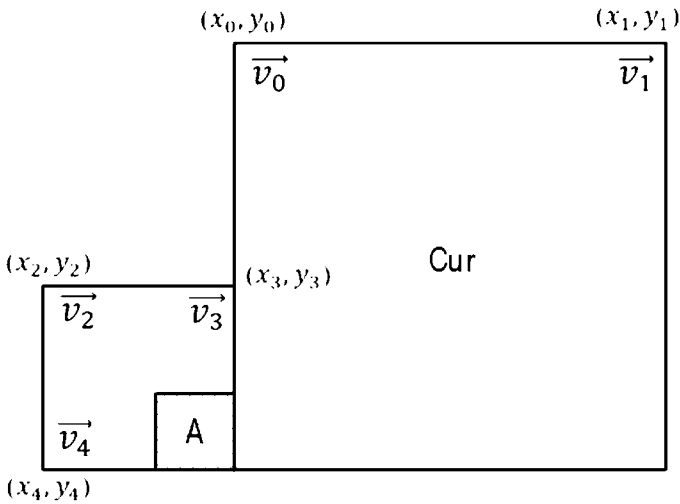


Fig. 15

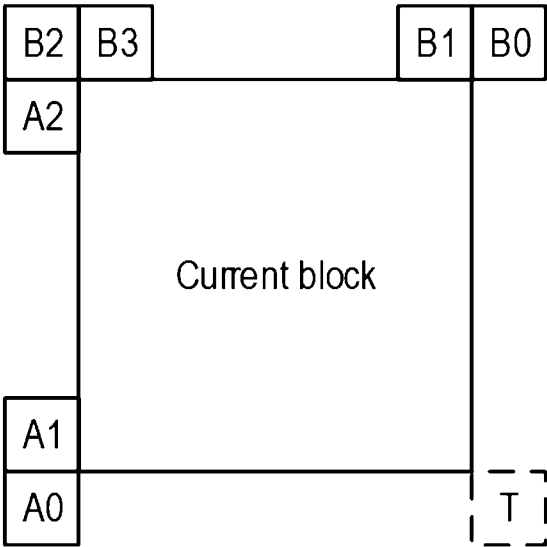


Fig. 16

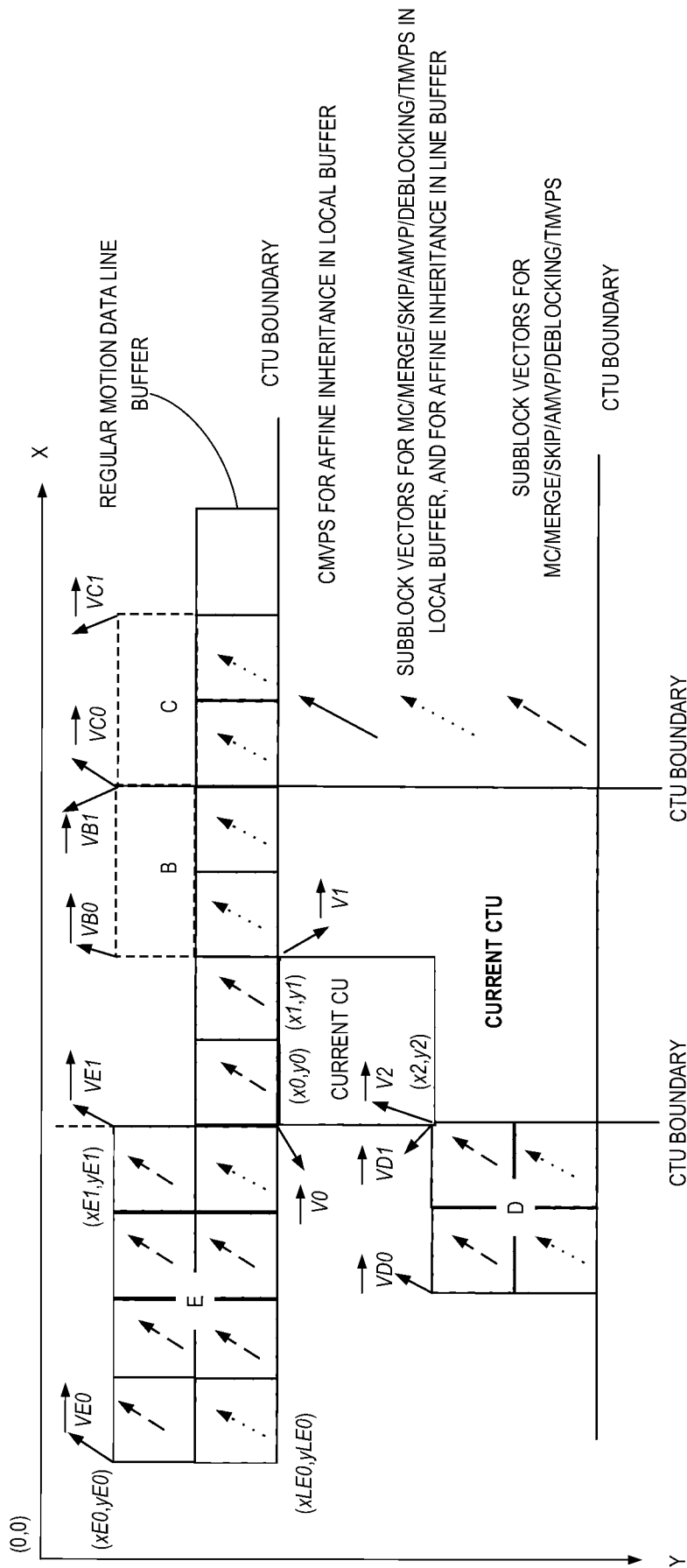


Fig. 17

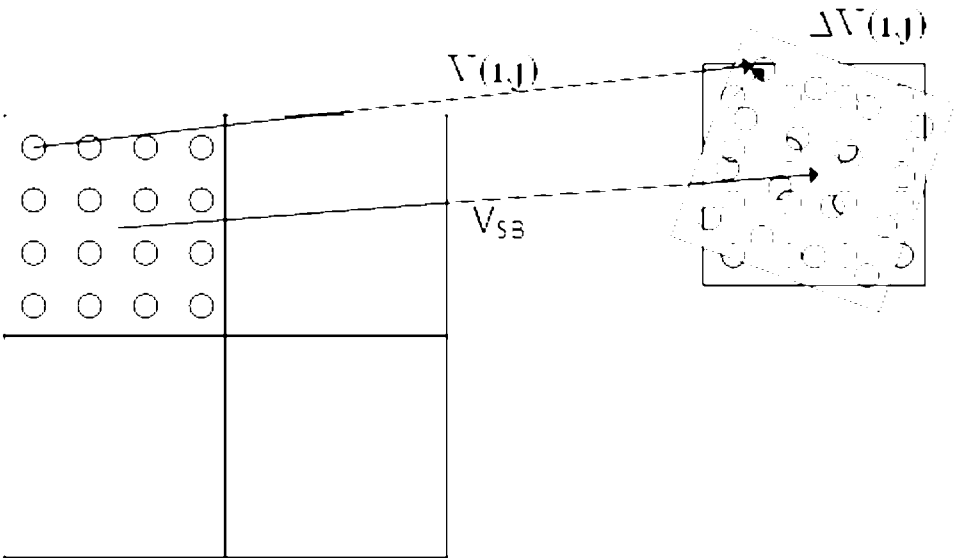


Fig. 18

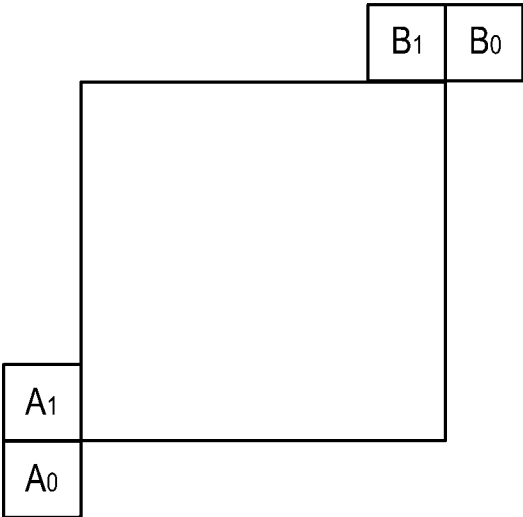


Fig. 19A

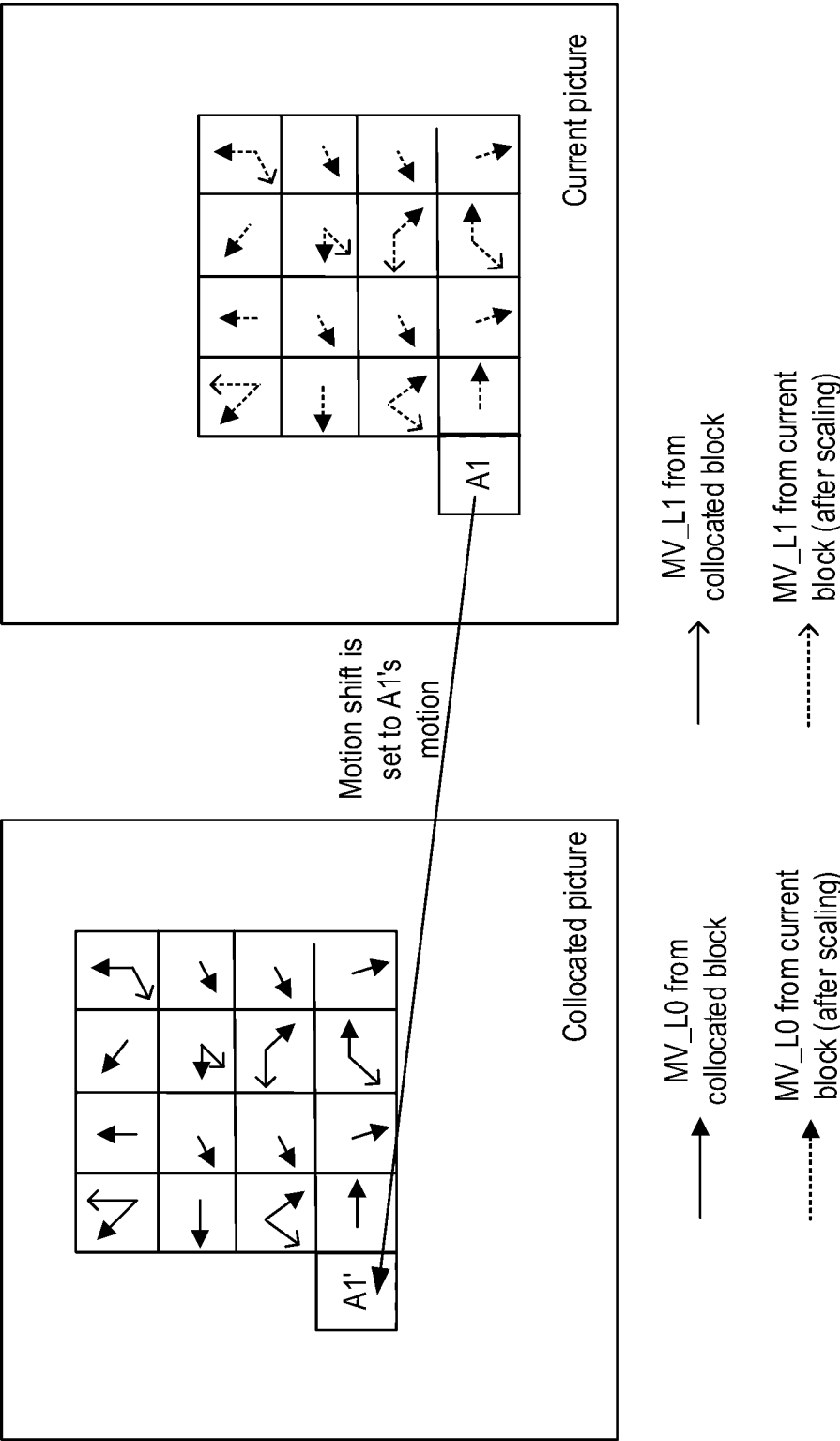


Fig. 19B

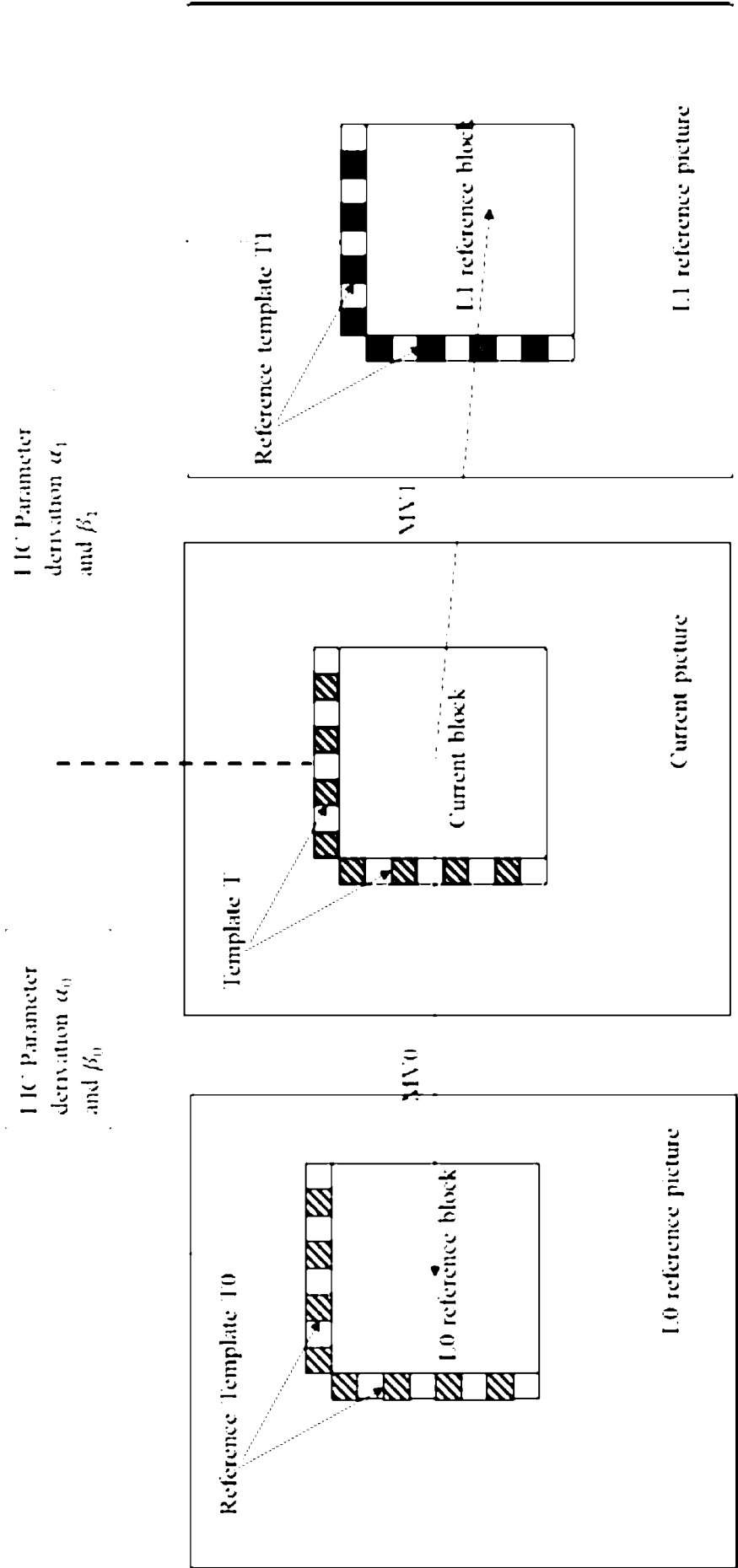


Fig. 20

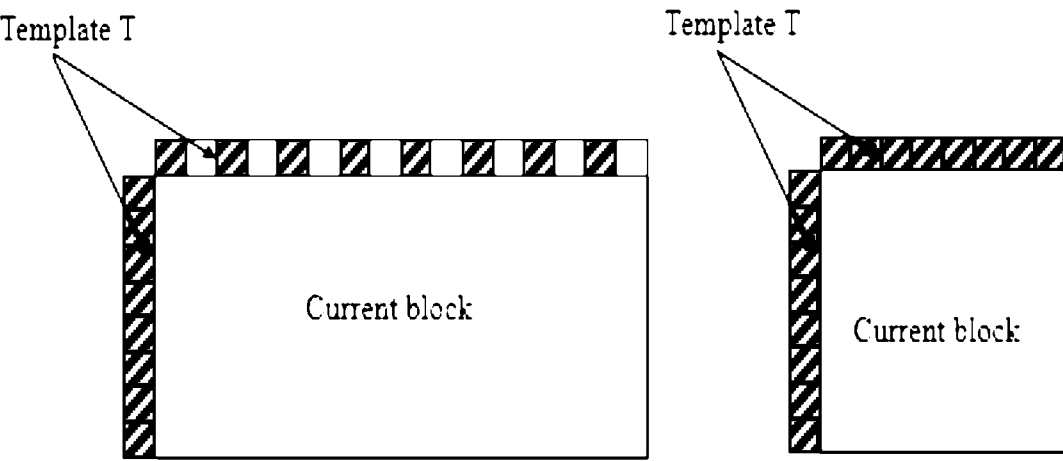


Fig. 21

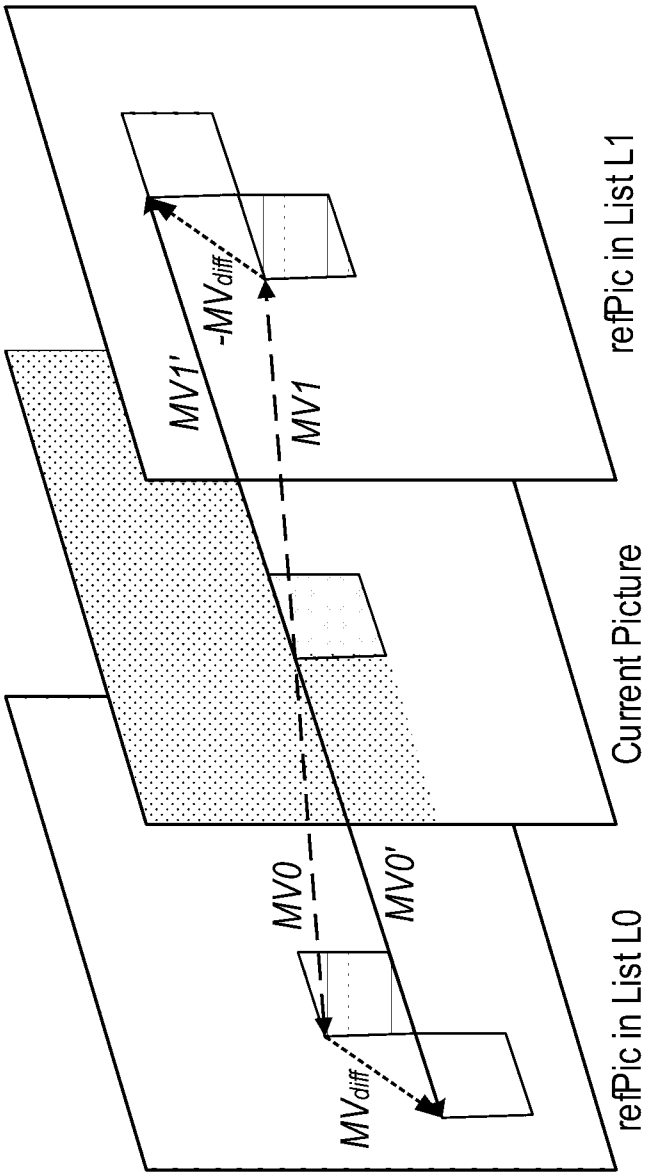


Fig. 22

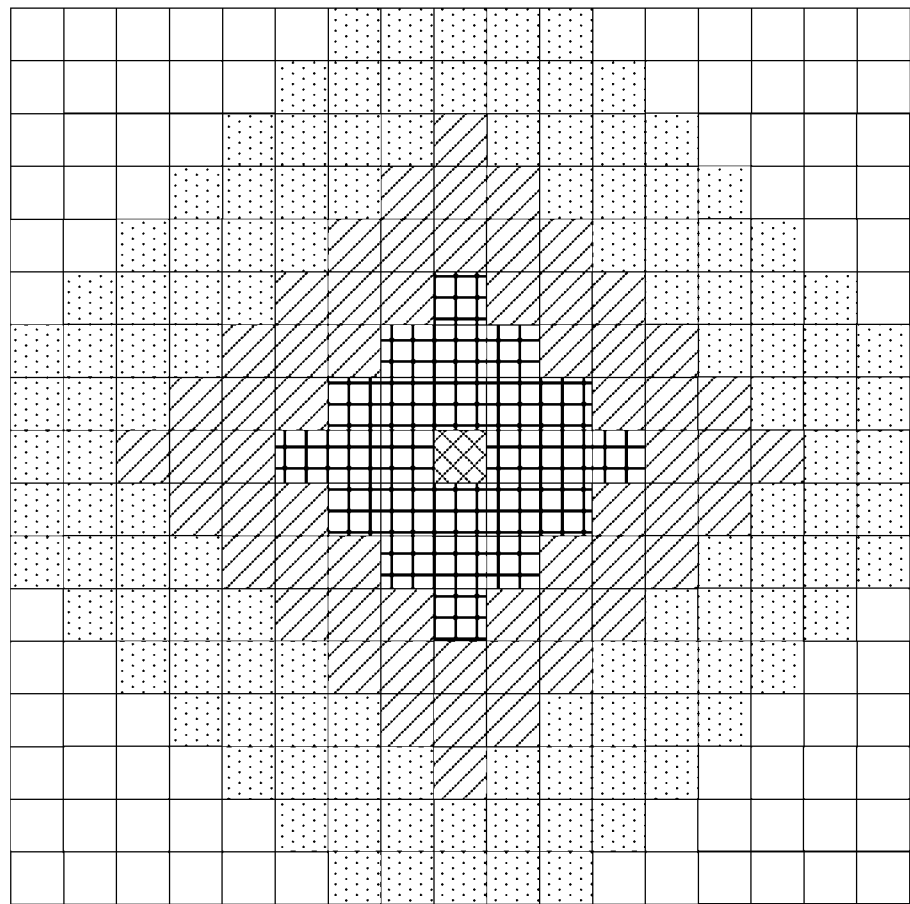


Fig. 23

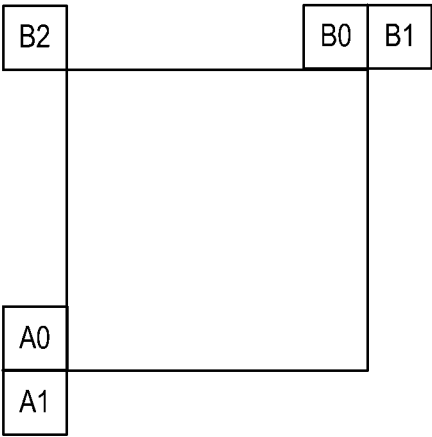


Fig. 24

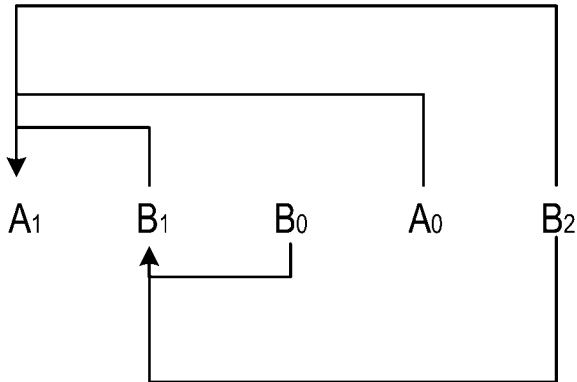


Fig. 25

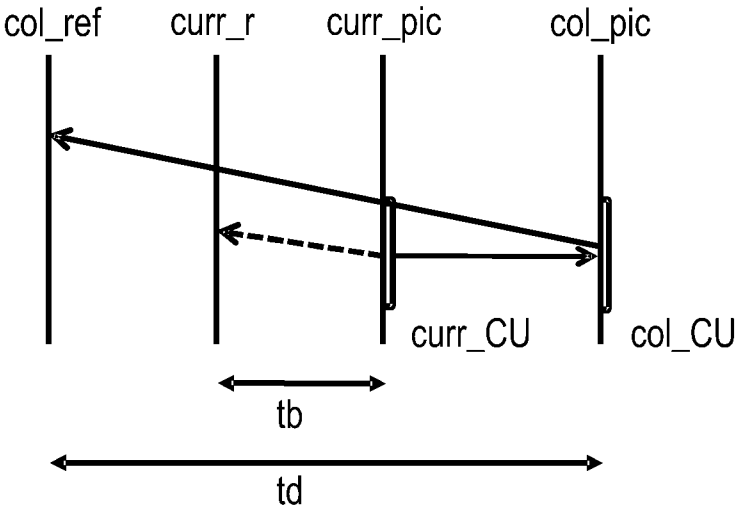


Fig. 26

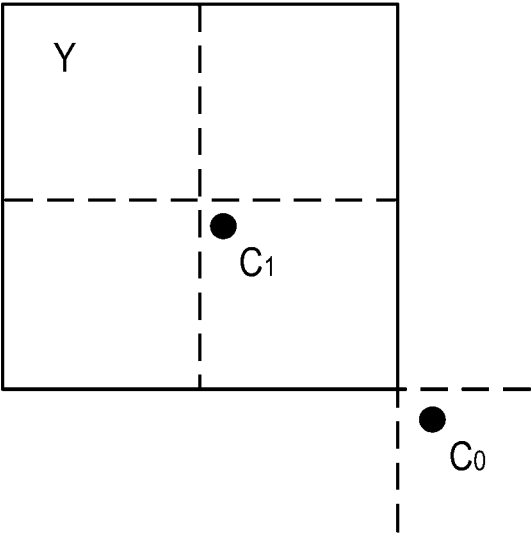


Fig. 27

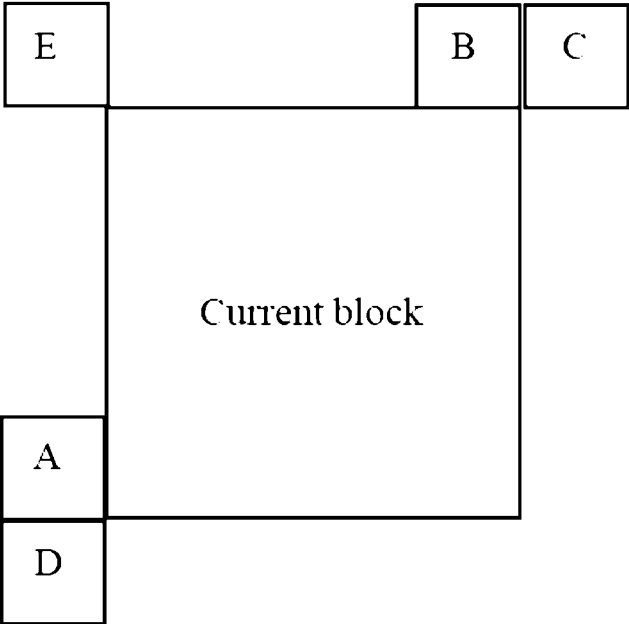


Fig. 28

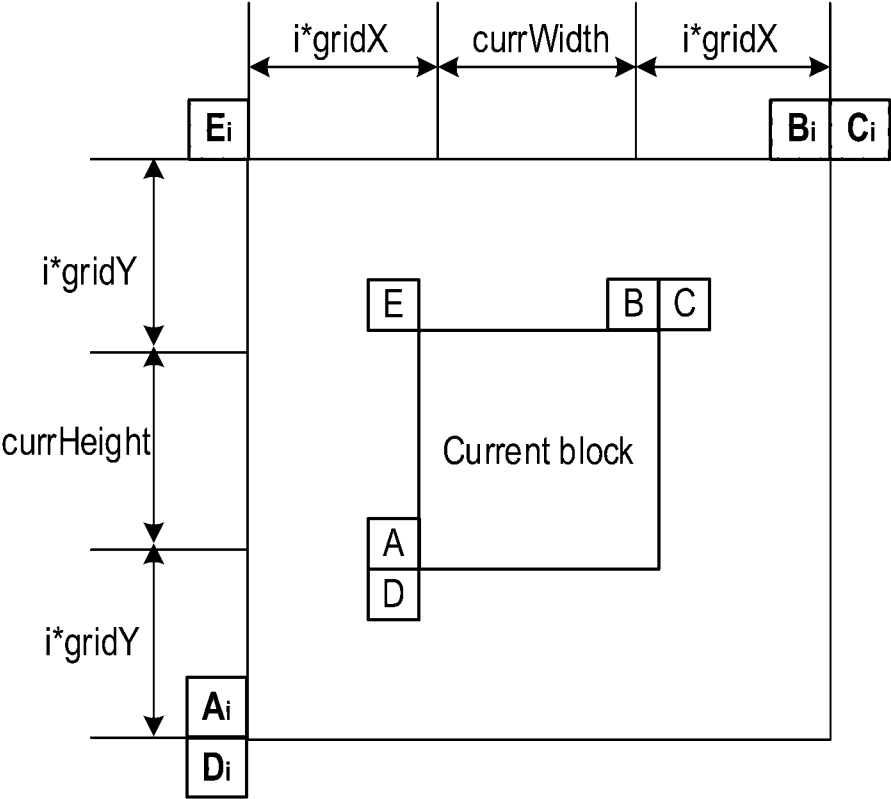


Fig. 29

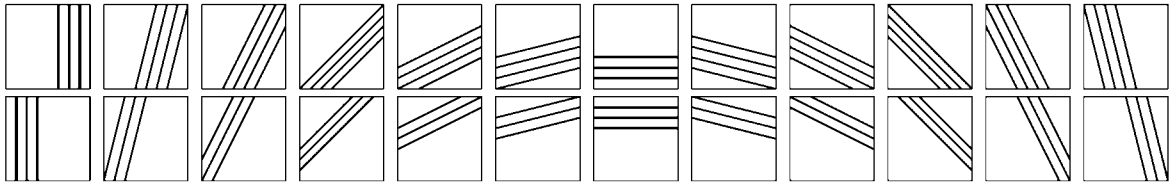


Fig. 30

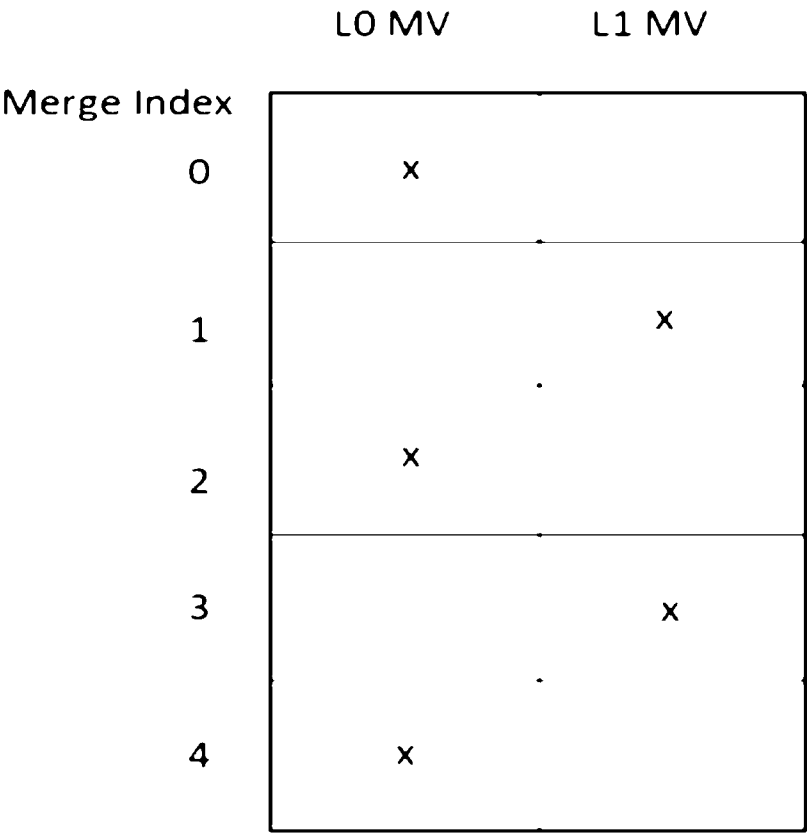


Fig. 31

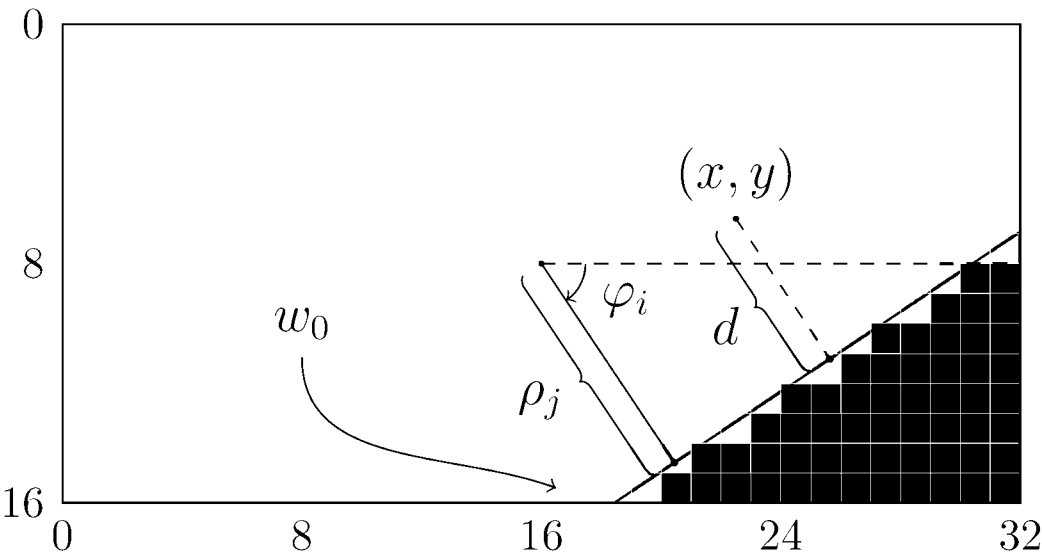


Fig. 32

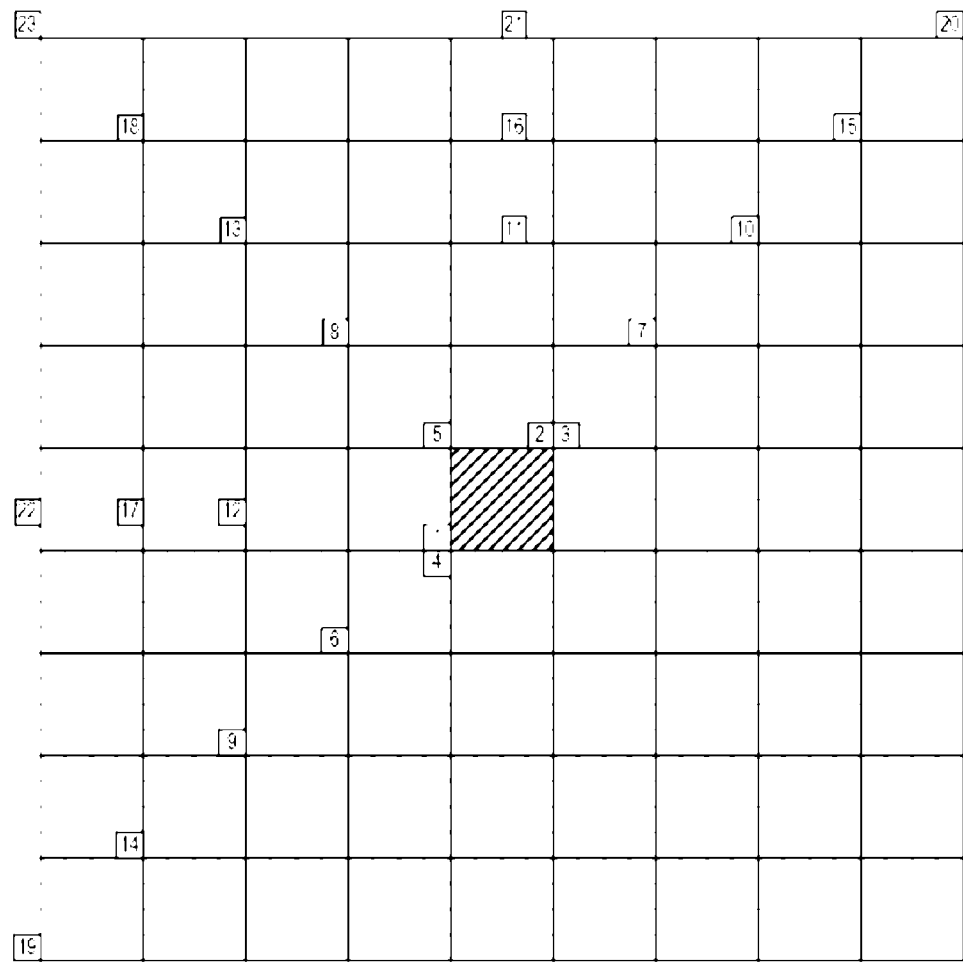


Fig. 33

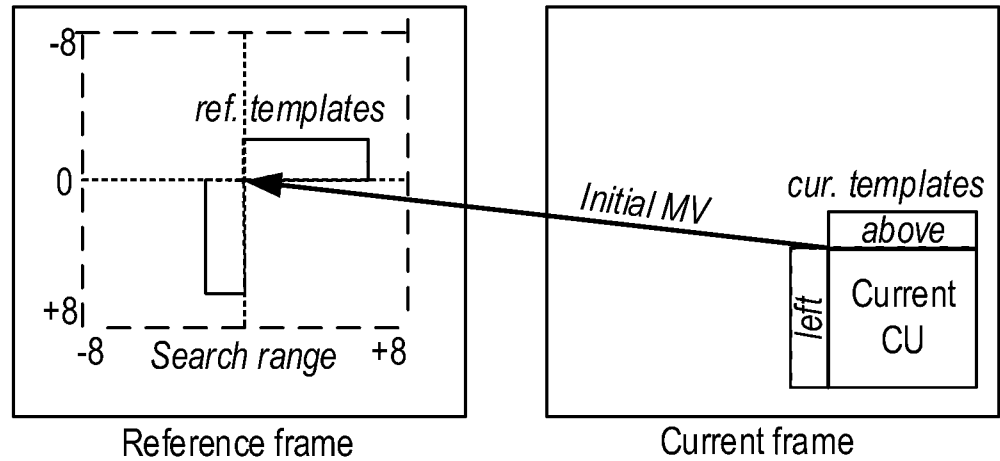


Fig. 34

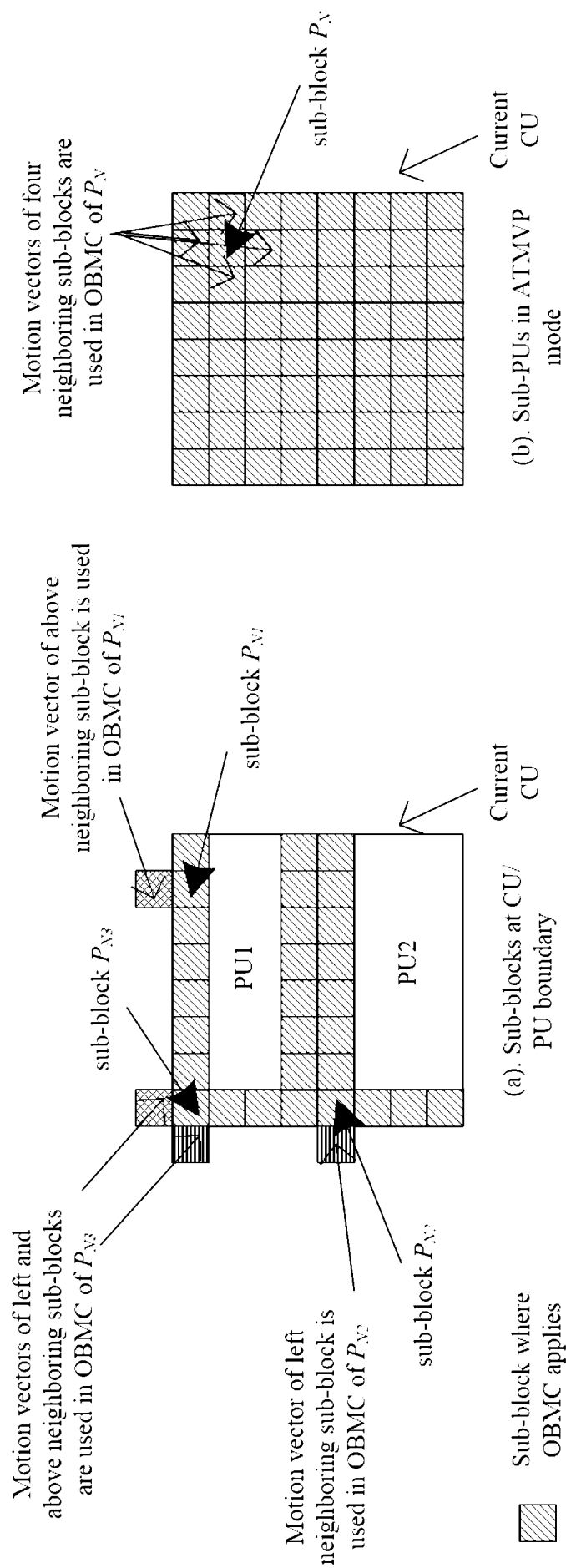


Fig. 35

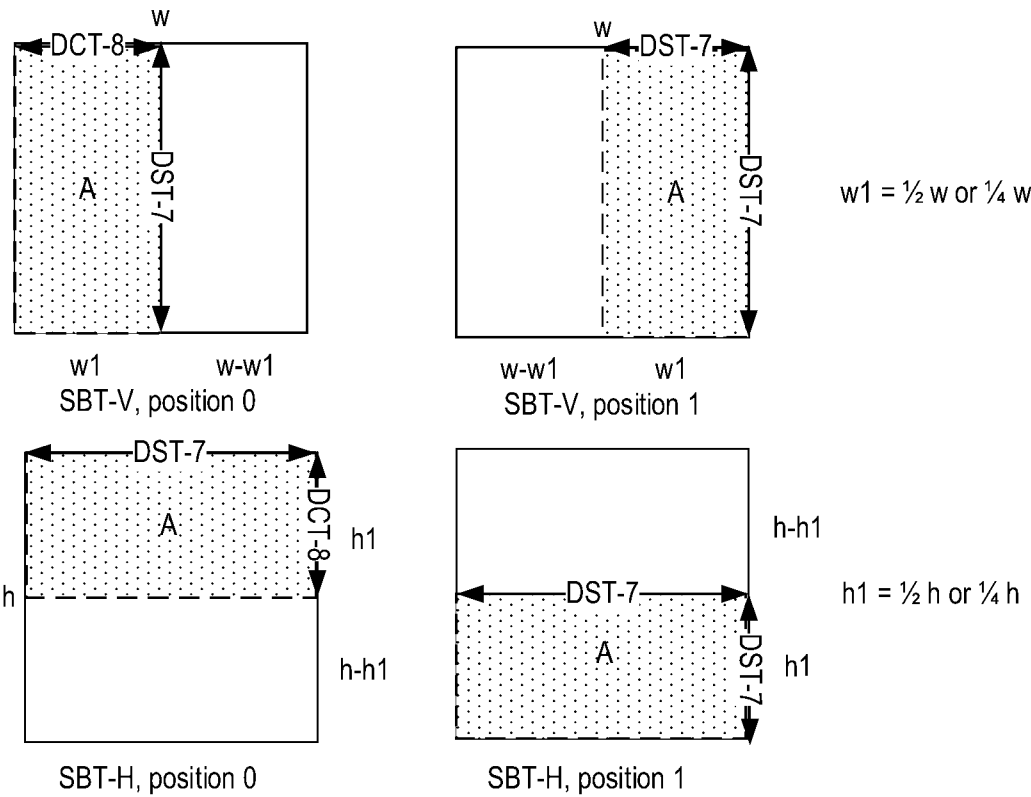


Fig. 36

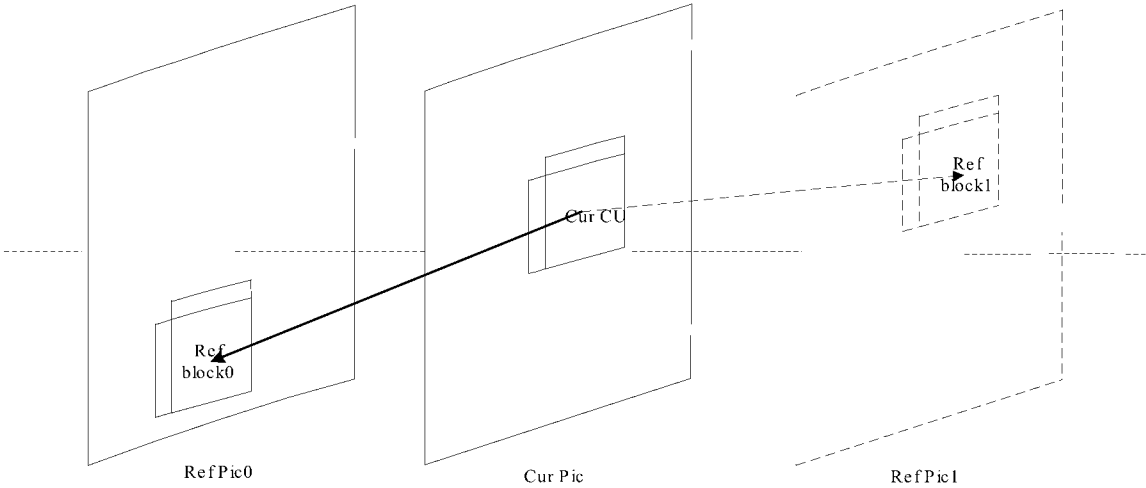


Fig. 37

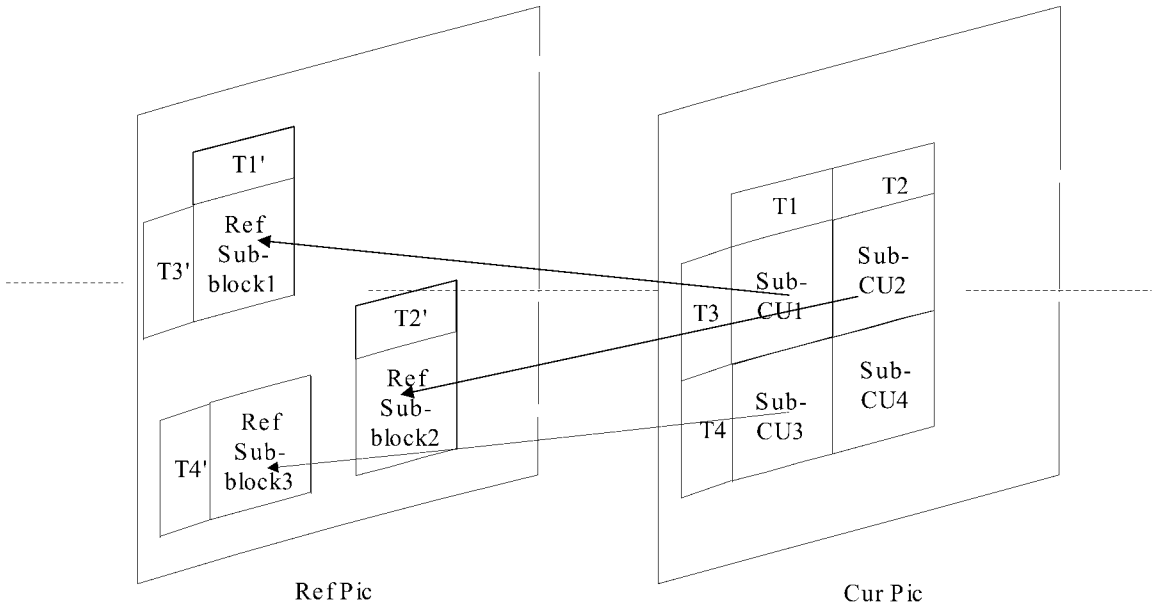


Fig. 38

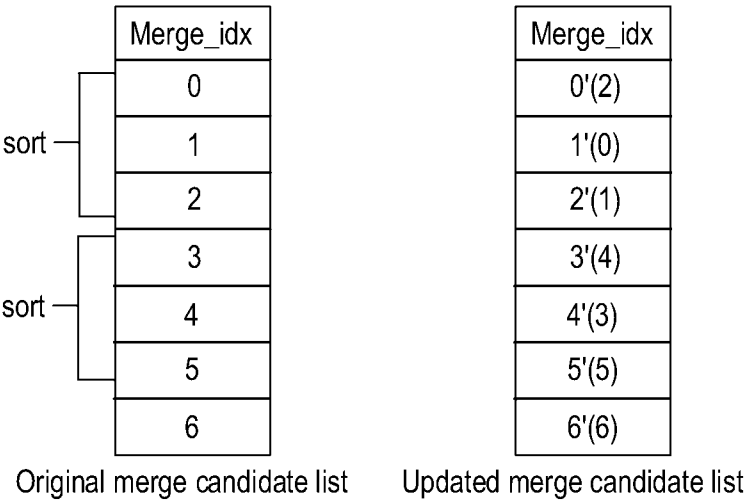
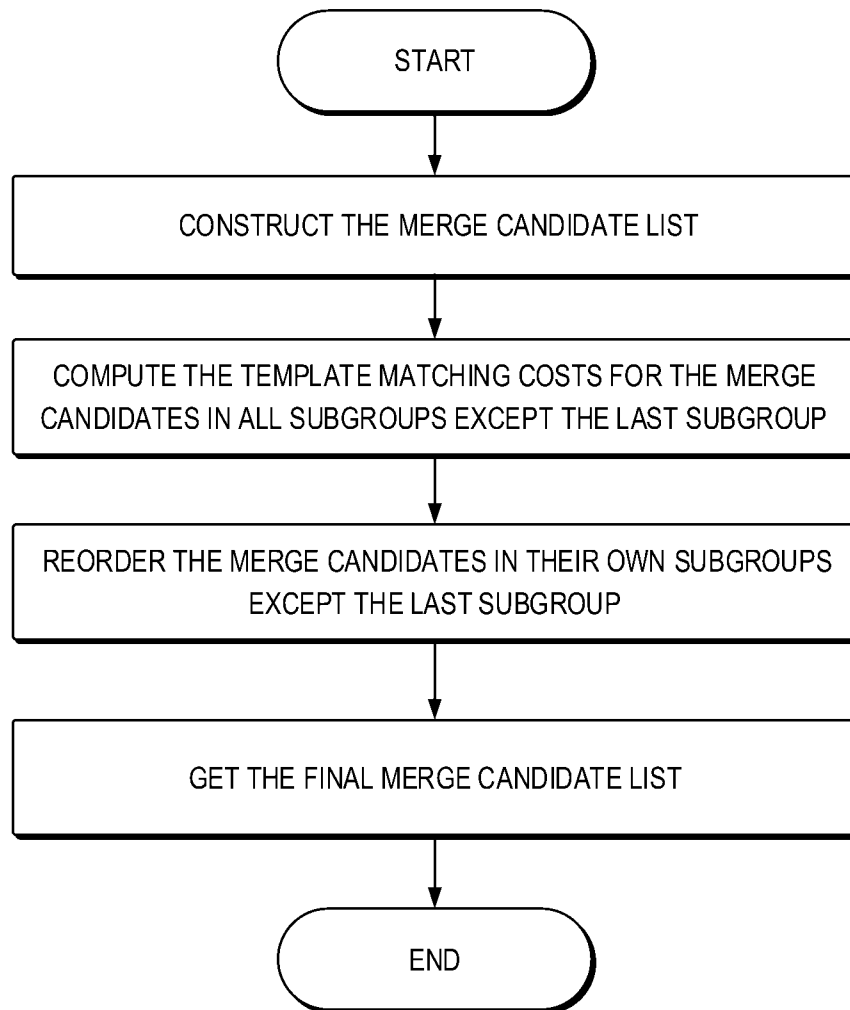


Fig. 39

**Fig. 40**

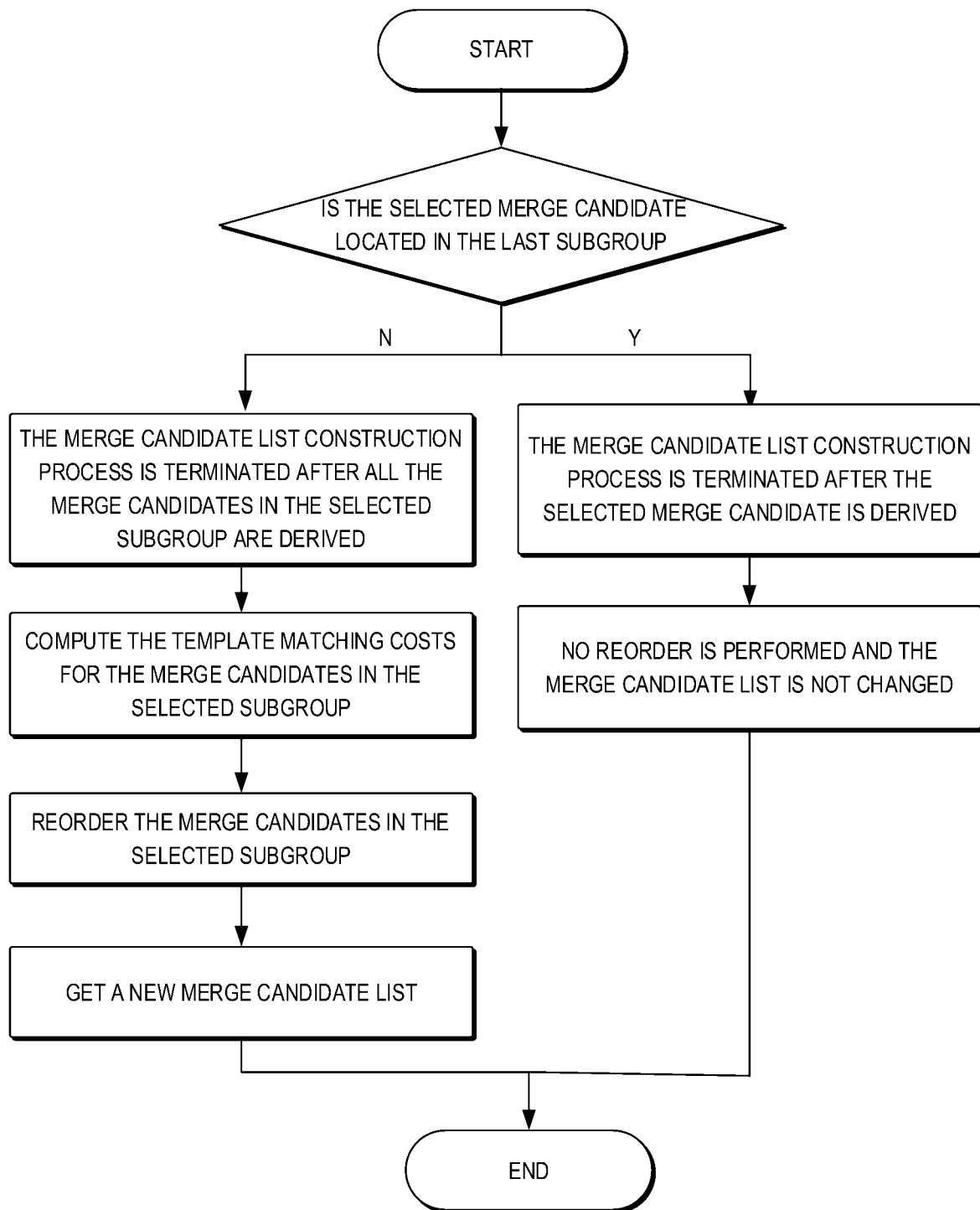


Fig. 41

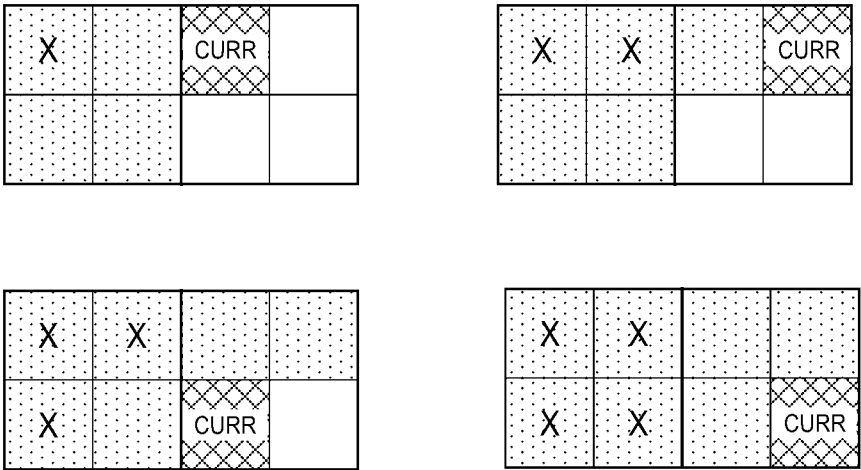


Fig. 42

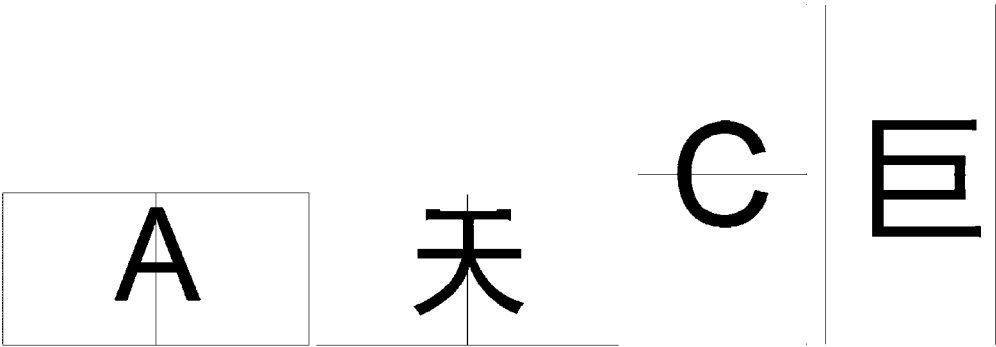


Fig. 43

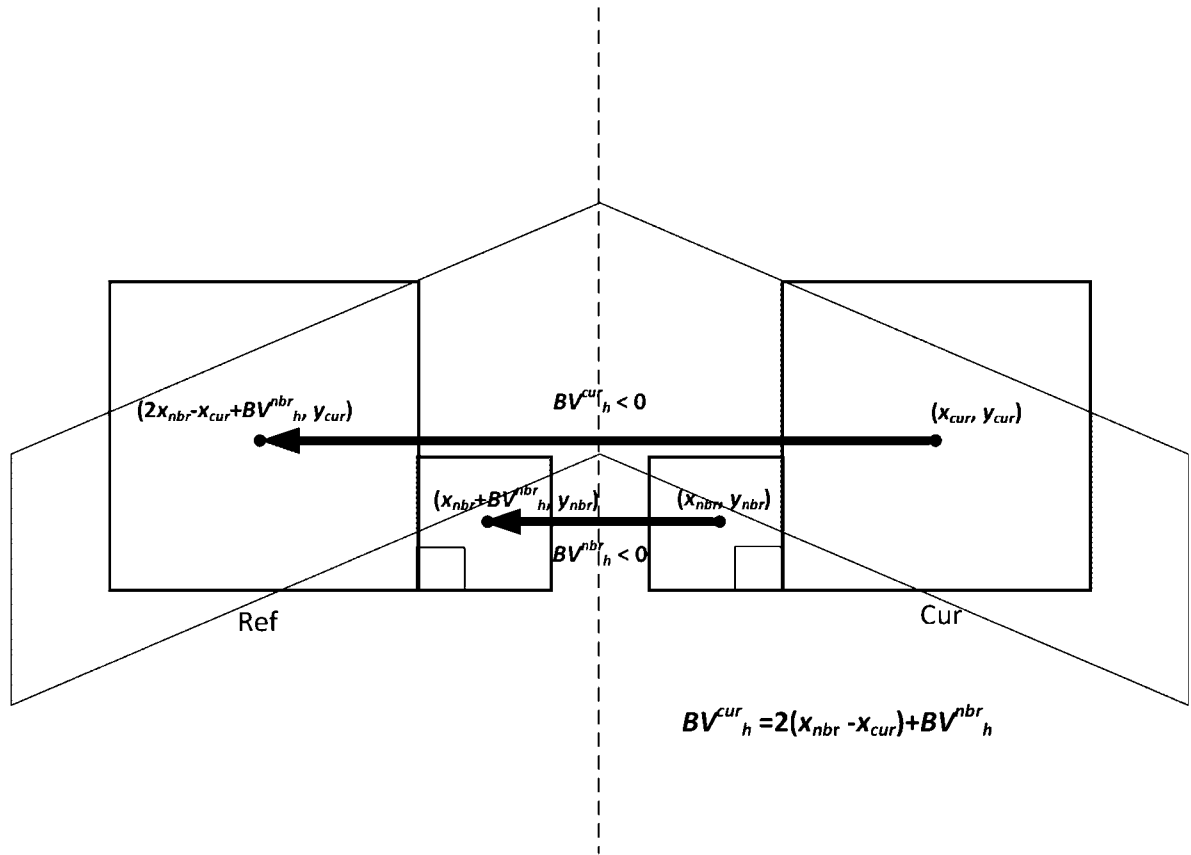


Fig. 44A

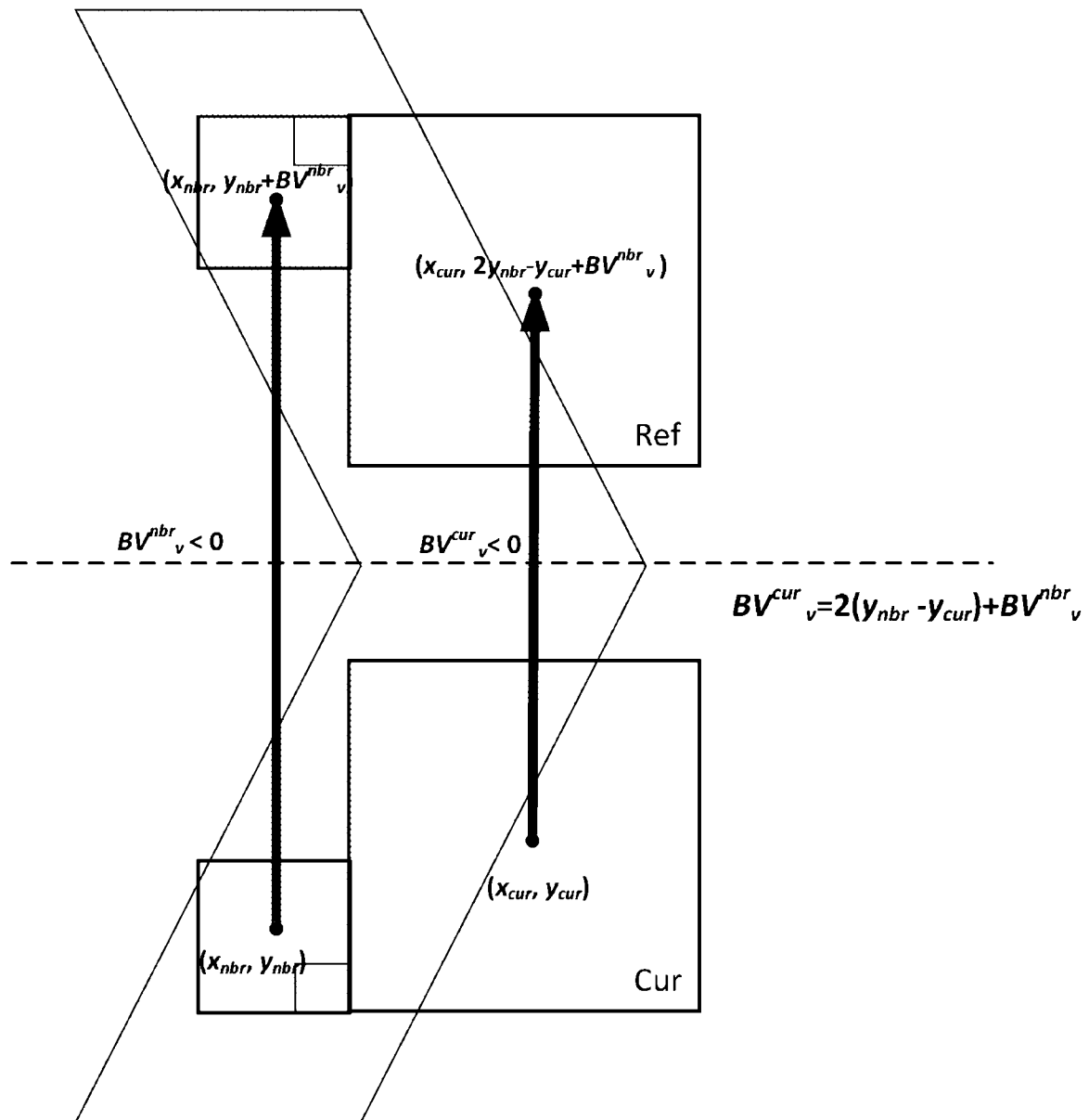


Fig. 44B

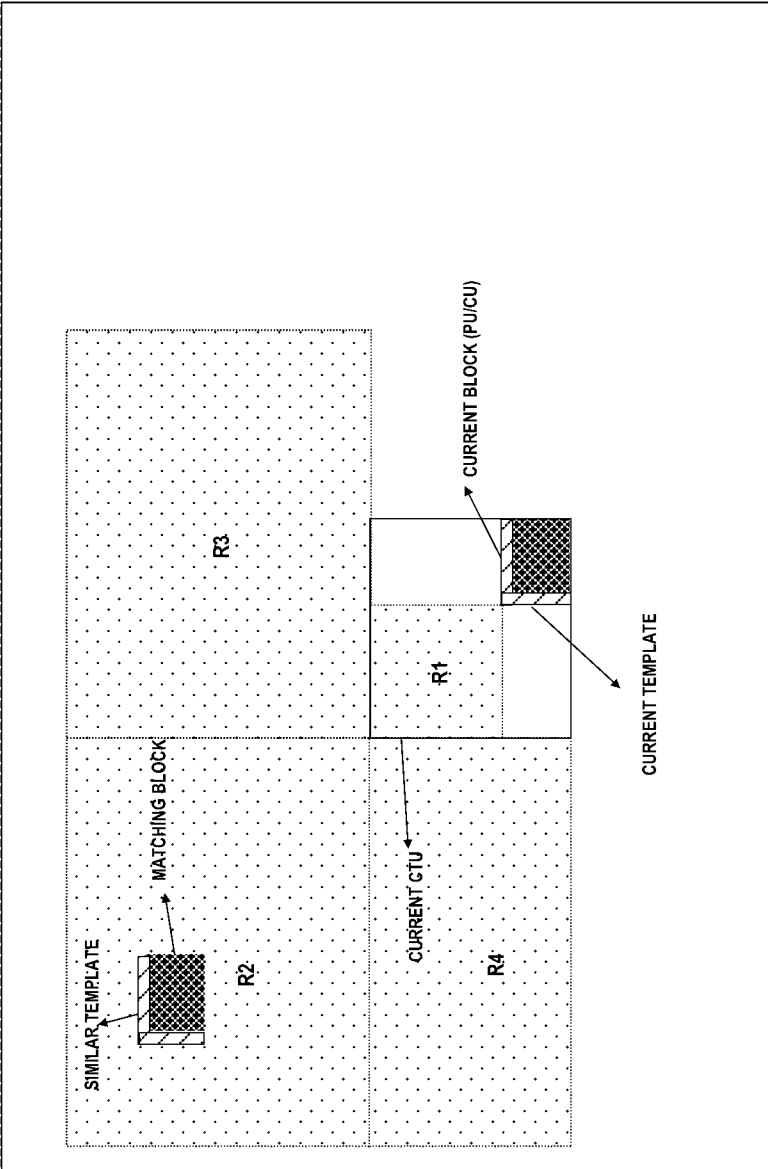


Fig. 45

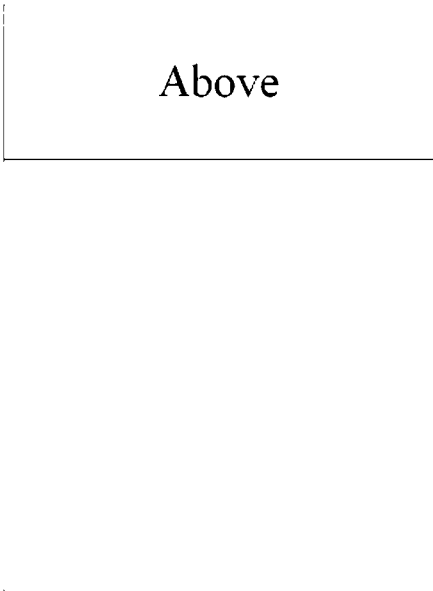


Fig. 46A



Fig. 46B

Left-
Bottom

Fig. 46C

Right-Above

Fig. 46D

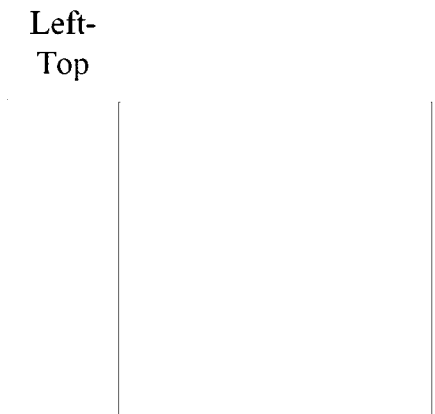


Fig. 46E

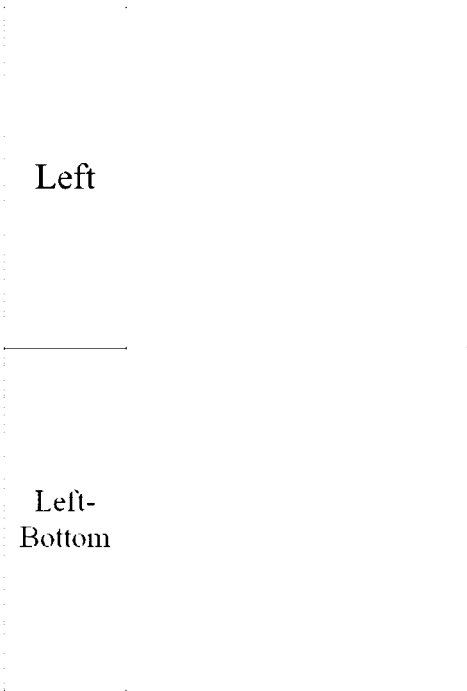


Fig. 47A



Fig. 47B

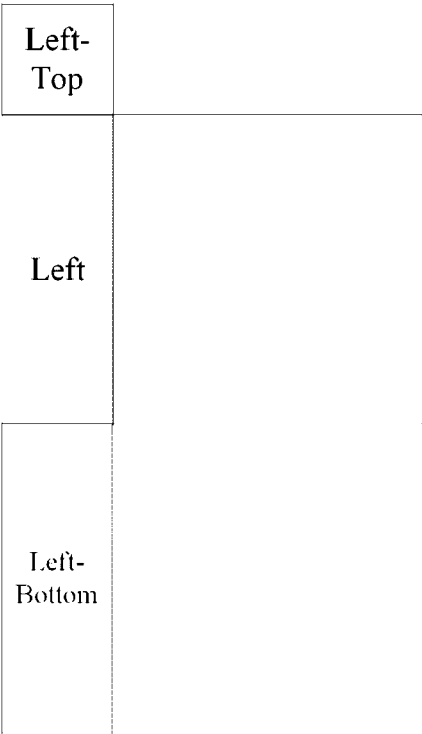


Fig. 47C

Above	Right-Above
-------	-------------

Fig. 47D

Left- Top	Above
--------------	-------

Fig. 47E

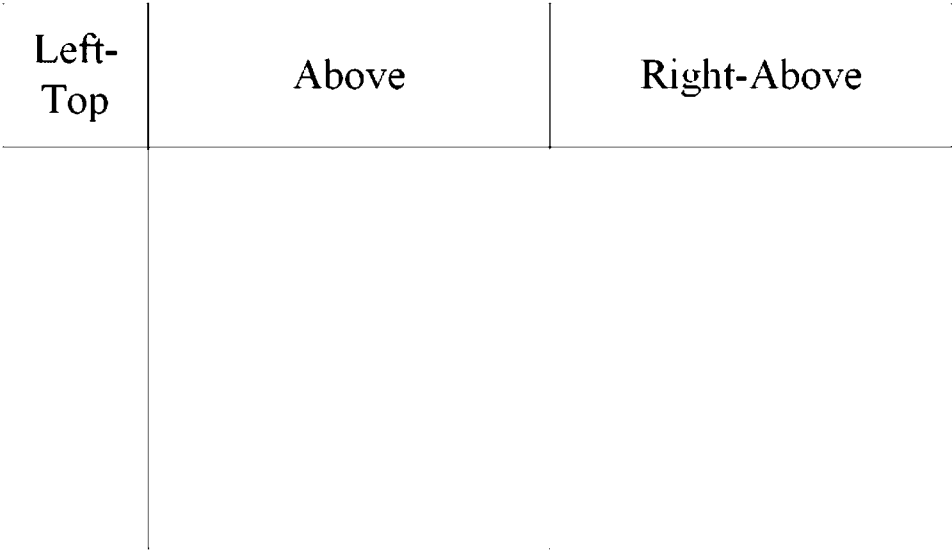


Fig. 47F

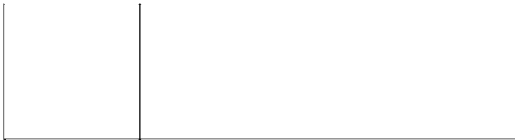


Fig. 48



Fig. 49

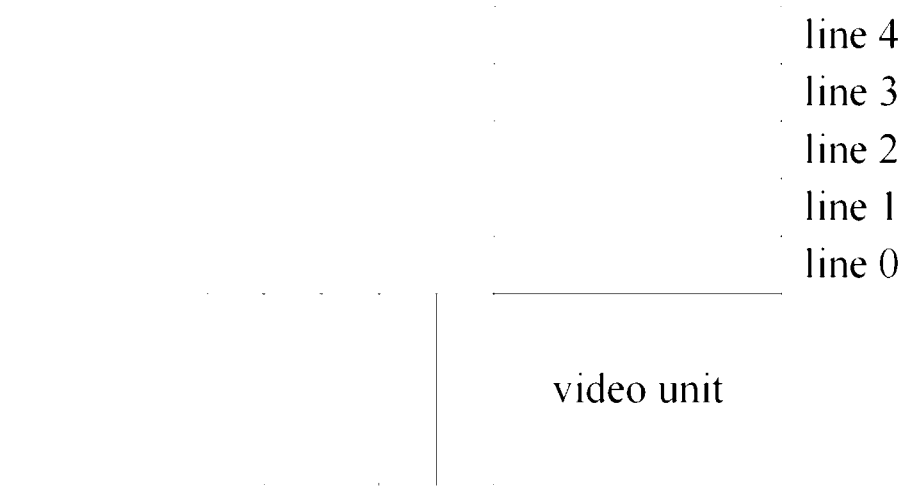


Fig. 50

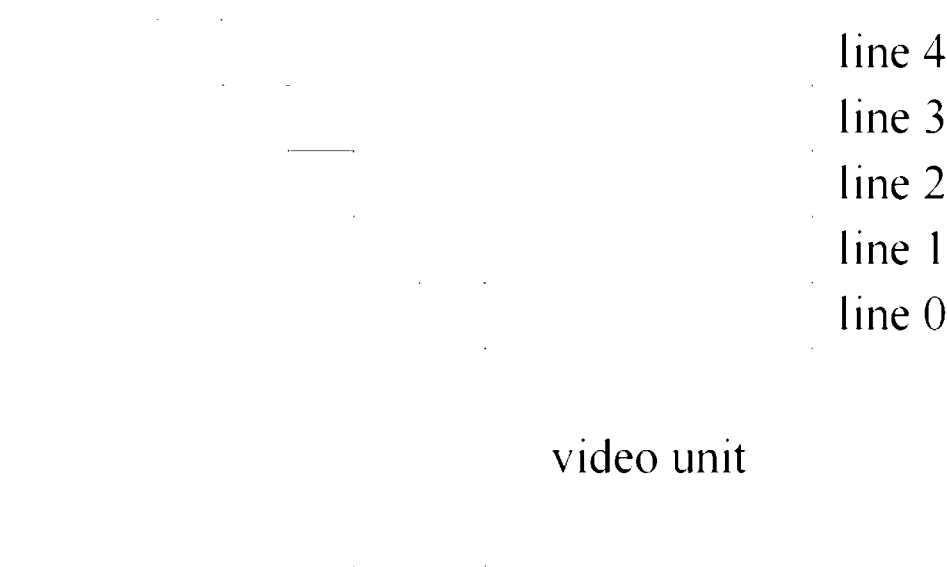


Fig. 51

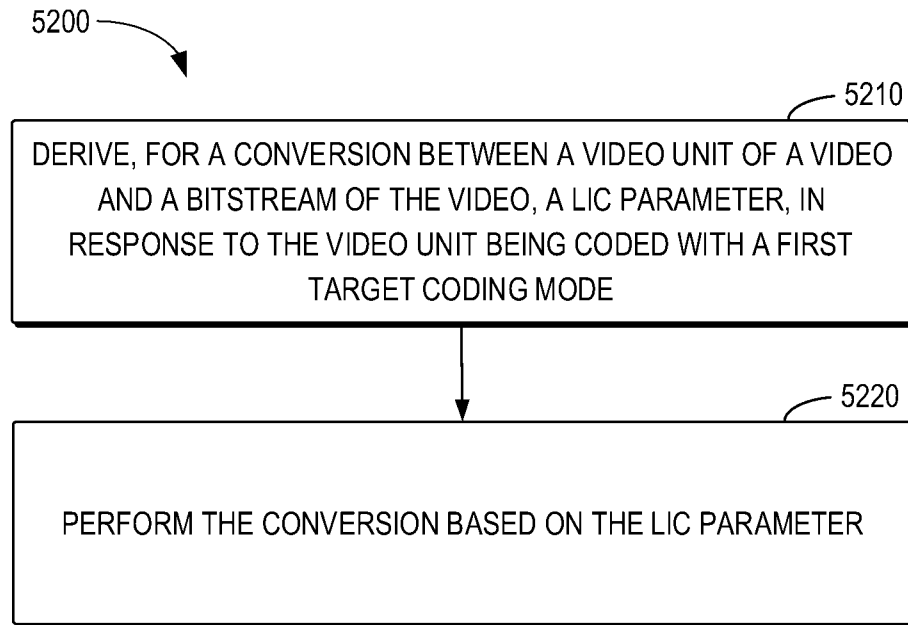


Fig. 52

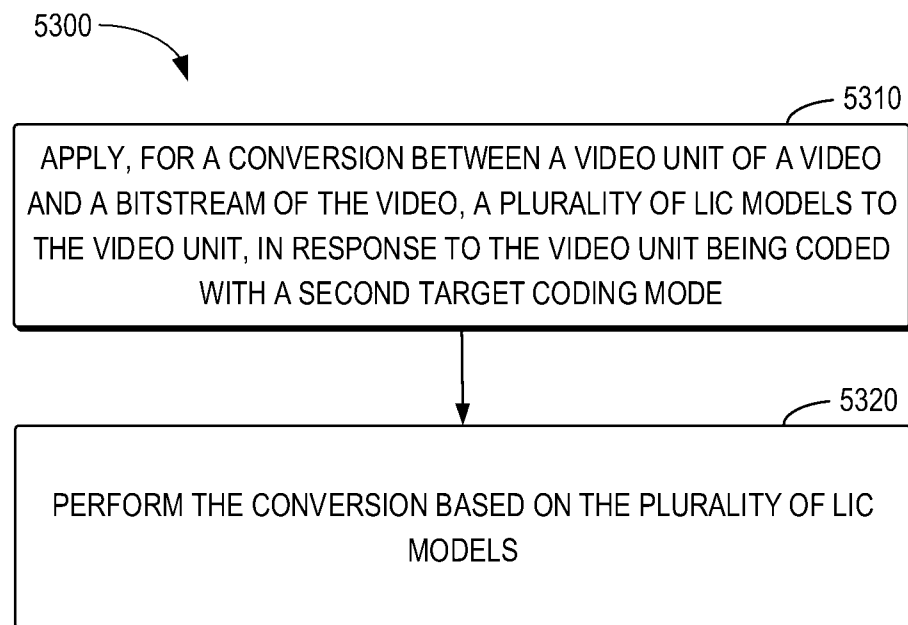


Fig. 53

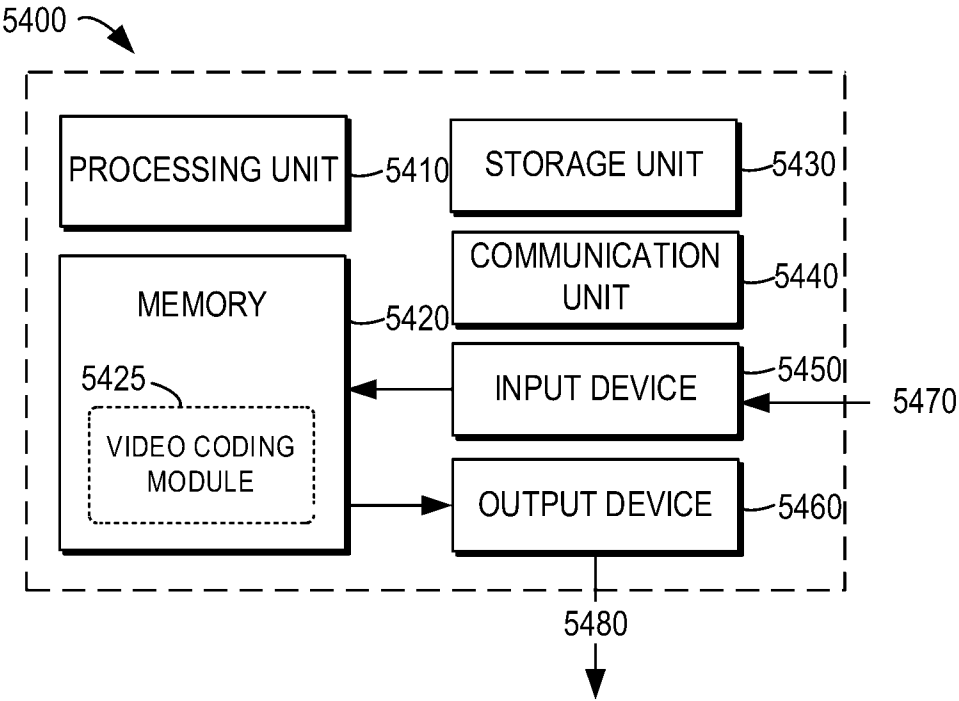


Fig. 54

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2024/101758

A. CLASSIFICATION OF SUBJECT MATTER

H04N 19/149(2014.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC: H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNABS, CNTXT, CNKI, DWPI, VEN, USTXT, WOTXT, EPTXT: video, image, code, coding, mode, bitstream, bit, LIC, local, illumination, brightness, compensation, sample, row, column, line, adjacent, neighbouring, block, reconstructed, reconstruction, parameter

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	CN 109792526 A (QUALCOMM INC.) 21 May 2019 (2019-05-21) the whole document	1-214
A	CN 112970263 A (BEIJING BYTEDANCE NETWORK TECH CO., LTD. et al.) 15 June 2021 (2021-06-15) the whole document	1-214
A	CN 111201793 A (QUALCOMM INC.) 26 May 2020 (2020-05-26) the whole document	1-214
A	US 2021227209 A1 (BEIJING BYTEDANCE NETWORK TECH CO., LTD. et al.) 22 July 2021 (2021-07-22) the whole document	1-214
A	US 2019246114 A1 (APPLE INC.) 08 August 2019 (2019-08-08) the whole document	1-214

☐ Further documents are listed in the continuation of Box C.☒ See patent family annex.

* Special categories of cited documents:

“A” document defining the general state of the art which is not considered to be of particular relevance

“D” document cited by the applicant in the international application

“E” earlier application or patent but published on or after the international filing date

“L” document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

“O” document referring to an oral disclosure, use, exhibition or other means

“P” document published prior to the international filing date but later than the priority date claimed

“T” later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

“X” document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

“Y” document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

“&” document member of the same patent family

Date of the actual completion of the international search

26 September 2024

Date of mailing of the international search report

26 September 2024

Name and mailing address of the ISA/CN

CHINA NATIONAL INTELLECTUAL PROPERTY
ADMINISTRATION
6, Xitucheng Rd., Jimen Bridge, Haidian District, Beijing
100088, China

Authorized officer

YANG,Hao

Telephone No. (+86) 010-62411449

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/CN2024/101758

Patent document cited in search report			Publication date (day/month/year)	Patent family member(s)			Publication date (day/month/year)
CN	109792526	A	21 May 2019	WO	2018064492	A1	05 April 2018
				JP	2019534622	A	28 November 2019
				BR	112019006580	A2	02 July 2019
				WO	2018064495	A1	05 April 2018
				EP	3520407	A1	07 August 2019
				EP	3520407	B1	18 November 2020
				US	2018098087	A1	05 April 2018
				US	10631002	B2	21 April 2020
				US	2018098062	A1	05 April 2018
				US	10778999	B2	15 September 2020
				EP	3520406	A1	07 August 2019
				EP	3520406	B1	01 February 2023
				KR	20190055104	A	22 May 2019
CN	112970263	A	15 June 2021	WO	2020094052	A1	14 May 2020
				JP	2023011591	A	24 January 2023
				JP	7436597	B2	21 February 2024
				WO	2020094054	A1	14 May 2020
				EP	4307678	A2	17 January 2024
				EP	4307678	A3	22 May 2024
				US	2021029372	A1	28 January 2021
				US	11166031	B2	02 November 2021
				WO	2020094049	A1	14 May 2020
				US	2021029366	A1	28 January 2021
				US	11159808	B2	26 October 2021
				US	2021006790	A1	07 January 2021
				US	11070820	B2	20 July 2021
				WO	2020094050	A1	14 May 2020
				US	2021337215	A1	28 October 2021
				US	11457226	B2	27 September 2022
				US	2021243438	A1	05 August 2021
				US	11611763	B2	21 March 2023
				US	2023283786	A1	07 September 2023
				US	2021266537	A1	26 August 2021
				US	11570450	B2	31 January 2023
				KR	20210087450	A	12 July 2021
				JP	2022506152	A	17 January 2022
				JP	7157246	B2	19 October 2022
				WO	2020094051	A1	14 May 2020
				EP	3857890	A1	04 August 2021
				EP	3857890	A4	22 September 2021
				US	2021006803	A1	07 January 2021
				US	11070821	B2	20 July 2021
CN	111201793	A	26 May 2020	TW	201924344	A	16 June 2019
				TWI	718412	B	11 February 2021
				US	2019110058	A1	11 April 2019
				US	10785494	B2	22 September 2020
				US	2020221110	A1	09 July 2020
				US	11297340	B2	05 April 2022
				BR	112020006875	A2	06 October 2020
				SG	11202001991	TA	29 April 2020

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/CN2024/101758

Patent document cited in search report				Publication date (day/month/year)		Patent family member(s)			Publication date (day/month/year)		
					KR	20200058445	A	27 May 2020			
					KR	102261696	B1	04 June 2021			
					WO	2019074622	A1	18 April 2019			
					AU	2018349463	A1	02 April 2020			
					AU	2018349463	B2	20 May 2021			
					EP	3695605	A1	19 August 2020			

US	2021227209	A1	22 July 2021	US	11284069	B2	22 March 2022				
				WO	2020084502	A1	30 April 2020				
				US	2023254479	A1	10 August 2023				
				US	12069248	B2	20 August 2024				
				US	2021235109	A1	29 July 2021				
				US	11659162	B2	23 May 2023				
				WO	2020084507	A1	30 April 2020				

US	2019246114	A1	08 August 2019	US	2020304807	A1	24 September 2020				
				US	11463707	B2	04 October 2022				
				WO	2019152283	A1	08 August 2019				
