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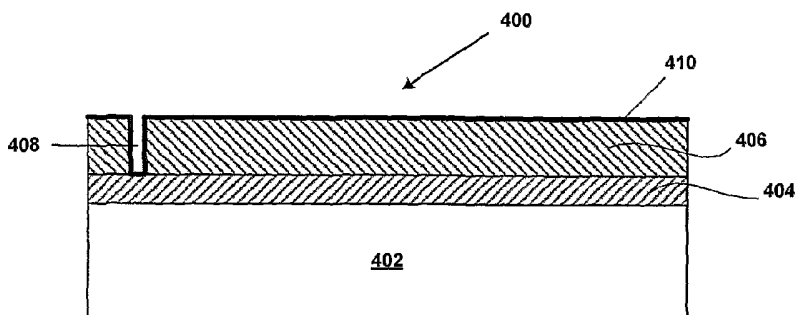
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(54) Title: PERMEATION BARRIERS FOR FLEXIBLE ELECTRONICS



(57) Abstract: Permeation barriers having a conventionally deposited layer and a layer applied by atomic layer deposition (ALD) are disclosed for use with flexible substrates. The ALD layers serve as decoupling layers to minimize defects in subsequently applied layers and/or as defect healing layers to seal defects in underlying layers.

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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER C23C16/56 C23C16/02		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED		
Minimum documentation searched (classification system followed by classification symbols) H01L H05B C23C		
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C. DOCUMENTS CONSIDERED TO BE RELEVANT		
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X	US 2003/143319 A1 (PARK SANG HEE ET AL) 31 July 2003 (2003-07-31)	1,2, 4-12, 14-21
Y	paragraphs [0015], [0018] - [0023], [0033], [0041], [0043], [0066], [0067]; claims 1-7,12-15	3,13
Y	US 2003/129298 A1 (TERA RYONOSUKE ET AL) 10 July 2003 (2003-07-10) paragraphs [0101], [0137], [0140], [0143] - [0145]; claim 1; figure 17	3,13
P,X	WO 2004/105149 A (E.I. DUPONT DE NEMOURS AND COMPANY; CARCIA, PETER, FRANCIS; MCLEAN, RO) 2 December 2004 (2004-12-02) cited in the application page 4, line 35 - page 5, line 9 page 5, line 35 - page 6, line 10	1-4
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