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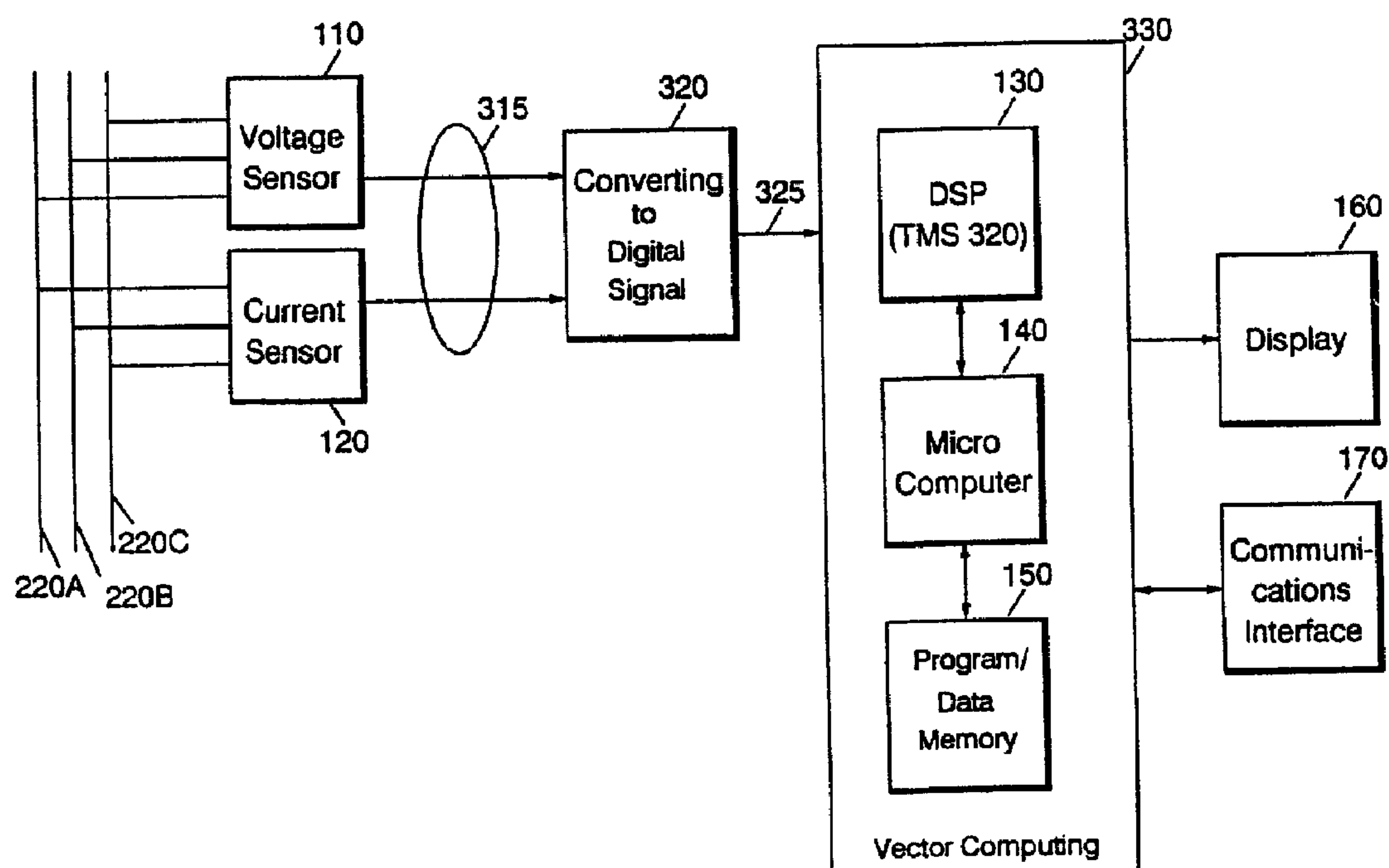
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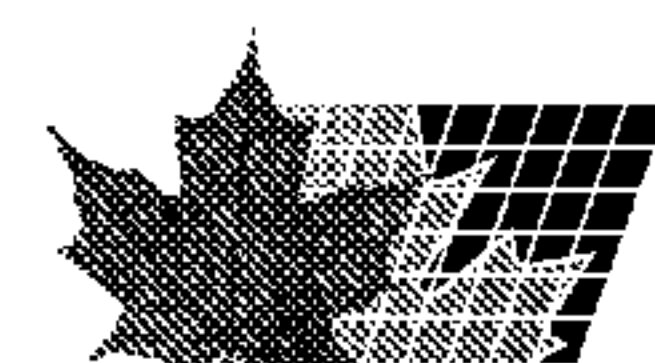
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(57) Abrégé/Abstract:

Line voltage and line current signals are sensed on a power line having at least one conducting path. The sensed line voltages and line currents are converted into a digital signal. A phase-to-neutral voltage signal and phase current signal are computed from the digital signal to thereby define a phase of the power line. An interval of orthogonality is determined from the sensed voltage and current signals, coinciding with passage of an integral number of cycles of a fundamental frequency reference signal which is computed from the computed phase-to-neutral voltage signal. A vector metering quantity is computed for the determined interval of orthogonality from the computed phase-to-neutral voltage signal and the computed phase current signal. The vector metering quantities to be computed may be identified and computed based upon an associated detent. The vector metering quantity is also computed based on an identified circuit topology.



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VECTOR ELECTRICITY METERS AND ASSOCIATED
VECTOR ELECTRICITY METERING METHODS

Abstract of the Disclosure

Line voltage and line current signals are sensed on a power line having at least one conducting path. The sensed line voltages and line currents are converted into a digital signal. A phase-to-neutral voltage signal and phase current signal are computed from the digital signal to thereby define a phase of the power line. An interval of orthogonality is determined from the sensed voltage and current signals, coinciding with passage of an integral number of cycles of a fundamental frequency reference signal which is computed from the computed phase-to-neutral voltage signal. A vector metering quantity is computed for the determined interval of orthogonality from the computed phase-to-neutral voltage signal and the computed phase current signal. The vector metering quantities to be computed may be identified and computed based upon an associated detent. The vector metering quantity is also computed based on an identified circuit topology.

VECTOR ELECTRICITY METERS AND ASSOCIATED VECTOR ELECTRICITY METERING METHODS

Field of the Invention

The present invention relates to electrical power line measurement, and more particularly to apparatus and methods for metering vector electrical quantities for electrical power lines having multiple conducting paths.

Background of the Invention

In the distribution of electrical energy, electric utility companies have typically found it desirable to measure quantities related to the delivery of electrical energy to a consumer which accurately reflect the cost of delivering that energy to that consumer, and thus equitably apportion the cost of delivering energy among all the users of the power system. Early on, utilities realized that billing customers based merely upon measurement of actual energy delivered -- Watt-hours-- fails to accurately reflect the cost of delivering energy to the customer. For example, large industrial users may have inductive loads, such as large induction motors, which induce significant phase shifts between voltages and currents in the power line, thus requiring advancement of generator angles and capacitive compensation by the utility in order to maintain voltage levels, efficiently deliver energy to consumers and preserve stability in the power system. This added generation and capital equipment cost is not reflected in measurements of energy delivered at the customer metering point.

Accordingly, other measures of electric power have been developed. For example, utilities typically bill not only for real load energy as watthours delivered to a user, but also reactive load quadergy as varhours (or reactive volt-ampere hours), and power factor ($\cos \phi$). By measuring both watthours and varhours electric utilities can more accurately apportion the costs of supplying energy to those customers with inductive loads which demand the most from the power delivery network.

Potential errors in power measurement attributable to nonsinusoidal conditions were also recognized early in this century. Nearly sixty years ago, power systems engineers attempted to develop a general unified theoretical model for power systems which accounts for harmonics and distortion. This model is described in an article, "*Definitions of Power and Related Quantities*" by Harvey L. Curtis and Francis B. Silsbee of the National Bureau of Standards, published for the 1935 AIEE Summer Conference. The definitions in the

Curtis and Silsbee article are derived from a three-dimensional vector model of electrical power applicable for all harmonics and phases. These definitions have survived largely intact through the publication of the latest editions of the *"IEEE Std 100, Standard Dictionary of Electrical and Electronic Terms."*

Power vector relationships for a power line are illustrated in Figure 19. The *ANSI/IEEE STD 100 Dictionary of Electrical and Electronic Terms* defines "phasor power" S as the magnitude of a two-dimensional power vector whose rectangular components are "active power" P and "reactive power" Q . In systems of more than one conducting path, i.e., "polyphase" systems and "single-phase" systems with more than one conducting path, phasor power S is the vector sum of active power P and reactive power S for the system, for all harmonics. As will be understood by those skilled in the art, phasor power S is equal to active power P when all load elements are resistive. In one form or another, it is phasor power, or more specifically, phasor volt-ampere-hours, which utilities have traditionally metered and billed. Typically, utilities have measured phasor volt-ampere-hours for the fundamental frequency of the system voltage using conventional watthour meters and varhour meters.

"Apparent power" U is defined as the magnitude of a three-dimensional vector power with orthogonal components of active power P , reactive power Q and a third component, "distortion power" D . Apparent power and apparent volt-ampere-hours provide a more comprehensive measure of the characteristics of a power line. For an isolated two-terminal circuit, apparent power U may be treated as a scalar, the product of the root-mean-square voltage and current in the single conducting path. In a system having more than one conducting wire, however, vector apparent power and vector apparent volt-ampere-hours are vectors, the vector sum of real, reactive and distortion power components for all phases and harmonics. For this reason, vector apparent power and vector apparent volt-ampere-hours have been largely ignored as practical metering quantities because of a lack of techniques to accurately measure their vector components. Instead, utilities have relied on alternative measurements such as quadergy and phasor volt-ampere-hours, for which measurement techniques and equipment could be easily developed.

Conventional sampling electronic watthour meters generally accurately measure energy by accumulating instantaneous power measurements. This is typically achieved by sampling voltage and current on the power line and converting the sampled voltages and currents into digital values which may be multiplied to compute the instantaneous power.

These sampling products are accumulated to yield a measurement of energy transferred by the power line, which can be inherently accurate for all significant harmonics, assuming the sampling rate satisfies the sampling theorem. As defined in *ANSI/IEEE STD 100-1992*, apparent power for a two terminal circuit is:

$$U_X = E_{rms} \times I_{rms}$$

where E_{rms} and I_{rms} are the root-mean-square values of the voltage and current for the circuit. Thus, viewing voltage and currents on a power line as a composite of sinusoidal signals, apparent power (or apparent volt-ampere-hours) for all harmonics on a phase of a power line may be determined by measuring RMS voltage and current.

Measurement of quadergy, however, is more problematic. The measurement of varhours conventionally has been accomplished either by using a second meter in conjunction with a conventional watthour meter or, more recently, a meter with the built-in capability of measuring both watthours and varhours. Typically, the technique for measuring varhours involves phase-shifting the measured line voltage by 90° using phase-shifting transformers (in analog meters) or time delay elements (in digital meters). Both of these methods may entail significant errors arising from disregarding or failing to accurately shift all the significant harmonics of the voltage.

Metering based on arithmetic apparent volt-ampere-hours for the power line has been proposed as an approximation of vector apparent volt-ampere-hours. Arithmetic apparent power for a multi-phase system represents the arithmetic sum of the magnitude of the apparent power for each of the individual phases. Although relatively easy to compute, arithmetic apparent power tends to closely approximate vector apparent power only in cases where the phases of the power line are balanced and symmetric. Even in those cases, its measurement often leads to unexpected results under certain circumstances where the current or voltage waveforms are nonsinusoidal. These characteristics tend to make arithmetic apparent power an unsuitable quantity for electrical metering.

Conventional electricity meters and metering methods may fail to provide accurate measurement of the actual cost of providing electrical energy to consumers where distortion is present. Increasing use of solid state switched motor drives, large switching power supplies and switched loads such as computers lead to distorted current waveforms, generally accompanied by a greater amount of associated distortion power. Distortion power increases demand on utility equipment and increases energy losses. Measures such as phasor volt-

ampere-hours and arithmetic apparent volt-ampere-hours fail to rationally reflect these associated costs.

Errors arising from use of these conventional measurement techniques will become increasingly significant as the cost of delivering energy increases. Utilities, driven by costs and the demands of their customers for billing equity, have an increasing need for accurate metering which reflects the true cost of delivering energy. In order to provide continuity and minimize replacement costs, however, new equipment and methods should be compatible with conventional meter connections and conventional metering formats, as well as with the various circuit topologies employed in electrical services.

Summary of the Invention

In the light of the foregoing, it is therefore an object of the present invention to provide electricity meters and metering methods for metering of electricity on a power line having at least two conductive paths.

It is another object of the present invention to provide electricity meters and metering methods for metering of electricity which are accurate for significant harmonics of the fundamental frequency of the power line.

It is another object of the present invention to provide electricity meters and metering methods for metering of electricity which are compatible with conventional meter connections and capable of utilizing conventional metering formats.

It is another object of the present invention to provide electricity meters and metering methods for metering of electricity which are adaptable to various power line circuit topologies.

These objects and advantages are provided by electricity meters and metering methods for vector metering of electricity which sense line voltage and line current signals on the power line, convert the sensed signals into a digital signal, and compute vector metering quantities for the power line over a determined interval of orthogonality for the sensed line voltages and line currents. Accordingly, accurate measurements of the vector metering quantity may be achieved. The computed vector metering quantity may include vector apparent volt-ampere-hours, vector apparent power, arithmetic apparent volt-ampere-hours, arithmetic apparent power, phasor volt-ampere-hours, distortion volt-ampere-hours, distortion power, quadergy, reactive power, energy, active power, power factor and distortion power factor. Vector computing means for computing vector metering quantities is

preferably implemented using a digital signal processor working in combination with a general-purpose microprocessor, integrated within an electricity meter.

The present invention provides accurate and equitable measurement of electricity through accurate vector metering of electrical power. The present invention also provides flexible, programmable metering which allows billing of the metered customer based on combinations of metered vector electrical quantities. The present invention also is easily adaptable to different electrical service environments, such as 4-wire wye, 3-wire single phase, 3-wire delta and the like, without requiring component changes or elaborate hardware modifications. Meter installation and power line maintenance are also aided.

In particular, vector metering of electricity is achieved in an electricity meter according to the present invention by sensing line voltage and line current signals on a power line. An interval of orthogonality for the sensed voltage and current signals is determined from the sensed voltage signals. The sensed voltage and current signals are converted into a digital signal from which a vector metering parameter is computed for the interval of orthogonality using vector computing means. Preferably, the sensed voltage and current signals are converted into corresponding sequences of line voltage and line current samples, corresponding to a consecutive plurality of sampling times spaced a sampling interval apart. Preferably, the sampling interval is uniform.

A digital phase-to-neutral voltage signal and corresponding phase current signal may be computed from the digital signal prior to computing the vector metering quantity, thus defining a phase of the power line with respect to a real or imputed neutral for the power line. Preferably, the digital phase-to-neutral voltage signal includes a series of digital phase-to-neutral voltage samples, and the digital phase current signal includes a series of digital phase current samples, each sample corresponding to the sampling time for the corresponding digital line voltage sample or line current sample.

The interval of orthogonality is preferably determined by detecting the passage of a predetermined integral number of cycles of a fundamental frequency reference signal which approximates the frequency of a fundamental component of the voltages and currents on the power line. Preferably, an interval of orthogonality represents 60 cycles of the fundamental frequency reference signal for a nominal 60Hz power system, or 50 cycles of the fundamental frequency reference signal for a nominal 50Hz system. The interval of orthogonality is preferably determined by narrow-band filtering the computed phase-to-neutral voltage signals to produce corresponding fundamental frequency phase-to-neutral

voltage signals. These fundamental frequency signals may be linearly combined to produce the fundamental frequency reference signal.

According to one aspect of the present invention, computing a vector metering quantity for the interval of orthogonality includes computing vector apparent volt-ampere-hours. Energy, quadergy and apparent volt-ampere-hours are computed for each phase of the power line for the interval of orthogonality. Vector-apparent volt-ampere-hours for the interval of orthogonality are computed from the computed energy, quadergy and apparent volt-ampere-hours. Distortion volt-ampere-hours for each phase of the power line may be computed from these quantities. As will be understood by those skilled in the art, by computing energy, quadergy and distortion volt-ampere-hours over an interval of orthogonality, the computed energy, quadergy and distortion volt-ampere-hour accurately represent vector components of the power line. Vector algebra may be performed upon these components to yield an accurate measurement of the vector apparent volt-ampere-hours for the power line during the interval.

Quadergy is preferably computed by applying a reactive power filter to the digital phase-to-neutral voltage signal and the digital phase current signal. Preferably, this filter includes digital filters implemented in the vector computing means. The reactive power filter preferably includes multiple phase-shifting filters and multipliers which produce two phase-shifted intermediate power product signals which are summed to produce an output signal closely approximating the quadergy for the power line for all harmonics within a predetermined frequency range.

According to another aspect of the present invention, active, reactive, distortion and vector apparent power are computed for the power line from the computed energy, quadergy, distortion volt-ampere-hours and vector apparent volt-ampere-hours. Preferably, these vector quantities are computed by dividing the computed energy, quadergy, distortion or vector apparent volt-ampere-hours for the interval of orthogonality by the number of sampling times occurring within the interval to yield the corresponding power quantity. A power factor and a distortion power factor may also be computed from the computed energy, quadergy and apparent-volt-amperes per phase during the interval.

In another aspect of the present invention, a neutral current magnitude for the power line is computed from the computed digital phase current signals. A neutral current status may be computed by comparing the computed neutral current magnitude to a predetermined threshold, indicating a unacceptable phase imbalance or other maintenance condition.

Similarly, an effective line voltage may be computed. A line voltage status may be computed by comparing the computed effective line voltage to an expected nominal operating voltage.

A phase angle associated with a fundamental frequency component of a sensed line voltage signal may also be computed using a migratory decimation technique. Samples of a narrow-band filtered version of a digital line voltage signal are selected over a series of consecutive periods of the fundamental frequency reference signal to obtain a set of migratory decimated samples. These samples preferably are selected such that a first selected migratory decimated sample coincides with a first zero-crossing of the fundamental frequency reference signal. The next selected migratory decimated sample is selected from the next period of the fundamental frequency reference signal, delayed a predetermined migratory decimation interval from the point on the fundamental frequency reference signal waveform at which the preceding sample was taken. Samples are similarly taken from succeeding intervals, thus yielding a set of migratory decimated samples approximating a period of the digital voltage signal. Fourier analysis is applied to these migratory decimated samples to compute the phase angle of the fundamental frequency component of the line voltage signal with respect to the reference signal. Thus an accurate measurement of line voltage phase angle is provided, useful for meter installation and maintenance, among other tasks.

In another aspect of the present invention, metering quantities for an interval of orthogonality may be cumulatively recorded, analyzed for minimum and maximum values over a period of interest, and subjected to other analyses for billing and other purposes. An identified metering quantity may be computed, based on an associated detent. Vector metering quantities may also be computed notwithstanding the loss of a sensed line voltage signal, thus providing a means for estimating power when a voltage transformer or other component has failed.

Accordingly, vector electricity meters and methods for vector metering of electricity are provided which can accurately measure vector metering quantities. These meters and methods provide for metering of electricity which accurately and equitably reflects the costs of delivering energy to customers. These meters and methods are also adaptable to various power line circuit topologies, and are compatible with conventional meter connections and formats.

Brief Description of the Drawings

Figure 1 is a schematic diagram illustrating a vector electricity meter according to the present invention.

Figure 2 is a schematic block diagram illustrating a vector electricity meter housed within a meter case according to the present invention.

Figure 3A is a block diagram illustrating operations in a vector electricity meter according to the present invention.

Figure 3B is a block diagram illustrating conversion of sensed line voltage and current signals to digital samples according to the present invention.

Figure 4 is a block diagram illustrating operations in a vector electricity meter according to the present invention.

Figure 5 is a block diagram illustrating determination of an interval of orthogonality according to the present invention.

Figure 6 is a block diagram illustrating computation of a vector metering quantity from sensed line voltage and current signals according to the present invention.

Figure 7 is a block diagram illustrating operations for computing a phase-to-neutral voltage signal and a phase current signal to define a phase of a power line according to the present invention.

Figure 8 is a block diagram illustrating computation of a vector metering quantity based on an indicated circuit topology.

Figure 9 is a table illustrating exemplary operations for computing phase-to-neutral voltage samples and phase current samples based on an indicated circuit topology according to the present invention.

Figure 10 is a block diagram illustrating operations for computing energy per phase during an interval of orthogonality according to the present invention.

Figure 11 is a block diagram illustrating operations for computing apparent volt-ampere-hours per phase for an interval of orthogonality according to the present invention.

Figure 12A is a block diagram illustrating a reactive power filter according to the present invention.

Figure 12B graphically illustrates a transfer function for a reactive power filter according to the present invention.

Figure 13 is a block diagram illustrating operations for computing quadergy per phase for an interval of orthogonality according to the present invention.

Figure 14 is a block diagram illustrating operations for computing vector apparent volt-ampere-hours for a power line for an interval of orthogonality according to the present invention.

Figure 15A is a block diagram illustrating operations for computing a line voltage phase angle according to the present invention.

Figure 15B is a block diagram illustrating decimated sampling for phase angle computation according to the present invention.

Figure 16A is a block diagram illustrating operations for computing an expected nominal line voltage according to the present invention.

Figure 16B is a block diagram illustrating operations for computing an effective line voltage according to the present invention.

Figure 17 is a block diagram illustrating operations for computing a line voltage status according to the present invention.

Figure 18 is a block diagram illustrating operations for computing a neutral current magnitude and neutral current status according to the present invention.

Figure 19 graphically illustrates power vector relationships.

Detailed Description of Preferred Embodiments

The present invention now will be described more fully hereinafter with reference to the accompanying drawings, in which preferred embodiments of the invention are shown. This invention may, however, be embodied in many different forms and should not be construed as limited to the embodiments set forth herein; rather, these embodiments are provided so that this disclosure will be thorough and complete, and will fully convey the scope of the invention to those skilled in the art. Like numbers refer to like elements throughout.

Figures 6-7, 10-11, 13-15A and 16A-18 are flowchart illustrations of methods and systems according to the invention. It will be understood that each block of the flowchart illustrations, and combinations of blocks in the flowchart illustrations, can be implemented by computer program instructions. These computer program instructions may be loaded onto a computer or other programmable apparatus to produce a machine, such that the instructions which execute on the computer or other programmable apparatus create means for implementing the functions specified in the flowchart block or blocks. These computer program instructions may also be stored in a computer-readable memory that can direct a

computer or other programmable apparatus to function in a particular manner, such that the instructions stored in the computer-readable memory produce an article of manufacture including instruction means which implement the function specified in the flowchart block or blocks. The computer program instructions may also be loaded onto a computer or other programmable apparatus to cause a series of operational steps to be performed on the computer or other programmable apparatus to produce a computer implemented process such that the instructions which execute on the computer or other programmable apparatus provide steps for implementing the functions specified in the flowchart block or blocks.

Accordingly, blocks of the flowchart illustrations support combinations of means for performing the specified functions and combinations of steps for performing the specified functions. It will also be understood that each block of the flowchart illustrations, and combinations of blocks in the flowchart illustrations, can be implemented by special purpose hardware-based computer systems which perform the specified functions or steps, or combinations of special purpose hardware and computer instructions.

A Vector Electricity Meter

Figures 1 and 2 illustrate a vector electricity meter according to the present invention. Voltage sensor **110** and current sensor **120** sense voltage and current signals on a power line and input the sensed voltages and currents **315** into converting means **320**. Converting means **320**, such as an analog-to-digital converter (A/D), may include signal processing circuits for rejecting DC components in the sensed voltages and currents **315**, compensating for phase shifts induced by sensing means, and the like, as well as means for sampling the sensed voltages and currents to obtain a digital signal **325**.

The digital signal **325** is input into vector computing means **330**, shown in Figure 1 as including a computer program running on a digital signal processor **130**, a microcomputer **140** and a program/data memory **150**. Preferably, digital signal processor (DSP) **130** performs vector power computations based on the digital signal **325**, under the control of the microcomputer **140**. Digital signal processor **130** preferably is a high-speed processing device having a highly parallel architecture which quickly performs repetitive calculations. Examples of digital signal processor **130** are devices in Texas Instruments Corporation's TMS320x line of digital signal processors. In addition to controlling digital signal processor **130**, the microcomputer **140** may control other peripheral devices, such as a display **160** or a communications interface **170**. Those skilled in the art will understand that computing

means **330** may be implemented using various combinations of hardware and software elements, including other digital signal processing devices or general purpose processors.

Sensing means **310**, converting means **320** and vector computing means **330** preferably are integrated within a standard-type electricity meter **200**, as shown in Figure 2. Meter **200** may include an LCD display **160** and an optical port **170** for communicating metering data to a data recorder or other device. Meter **200** may be directly connected to conductors **220A-C** of a power line **210** directly for low-voltage, low-power installations, or may be buffered through the use of voltage and current transformers, as is well-known to those skilled in the art.

Vector Electricity Metering - Overview

It will be understood by those skilled in the art that vector apparent power (or vector apparent volt-ampere-hours) for a power line having more than one conducting path may be determined by first determining its three orthogonal components: active power (or energy); reactive power (or quadergy); and distortion power (or distortion volt-ampere-hours). In vector terms:

$$\vec{U} = \vec{P} + \vec{Q} + \vec{D}$$

and

$$|\vec{U}| = \sqrt{|\vec{P}|^2 + |\vec{Q}|^2 + |\vec{D}|^2}.$$

In a vector electricity meter according to the present invention, these components are computed over an interval of orthogonality for the sinusoidal components of the periodic voltages and currents on the power line.

Those skilled in the art will understand that an n-dimensional vector **y** can be represented as a sum of basis vectors **x₁**, **x₂**, . . . , **x_n**:

$$y = c_1 x_1 + c_2 x_2 + \dots + c_n x_n \quad (1)$$

If the basis vectors are orthogonal, the inner products of the basis vectors are equal to zero:

$$\langle x_i, x_j \rangle, i \neq j.$$

Forming the inner product of a basis vector **x_i** with both sides of Equation (1) produces:

$$\langle y, x_i \rangle = c_1 \langle x_1, x_i \rangle + c_2 \langle x_2, x_i \rangle + \dots + c_n \langle x_n, x_i \rangle = c_i \langle x_i, x_i \rangle,$$

with

$$c_i = \frac{\langle y, x_i \rangle}{\langle x_i, x_i \rangle}.$$

Thus, a vector can be conveniently expressed using an orthogonal basis because the coordinates c_i can be easily calculated.

The relationship between orthogonality and inner products applies to functions in general. The inner product of two functions may be defined as an integral over an interval:

$$\langle g_i(t), g_j(t) \rangle = \int_{t_1}^{t_2} g_i(t) g_j^*(t) dt.$$

As with the basis vectors, the inner product of the two functions is zero if the functions are orthogonal:

$$\langle g_i(t), g_j(t) \rangle = 0, i \neq j.$$

The interval (t_1, t_2) is an interval of orthogonality for the functions $g_i(t)$ and $g_j(t)$. A function can be expressed as a sum of orthogonal functions over the interval of orthogonality (t_1, t_2) :

$$f(t) = c_1 g_1(t) + c_2 g_2(t) + \dots + c_n g_n(t).$$

Thus, a function represented as a sum of orthogonal functions over an interval of orthogonality can be determined using "vector algebra."

In order to accurately perform vector algebra using measured values for energy, quadergy and apparent volt-ampere-hours for each phase of a power line, these vector metering components preferably are measured for an interval of orthogonality so that the vector sum of these vector metering components is an accurate representation of vector apparent volt-ampere-hours, i.e., the inner products of voltage and current are zero. For a power line having voltages and currents including a sinusoidal fundamental frequency component and multiple harmonics thereof, an interval of orthogonality for all the sinusoidal voltages and currents is an integral number of cycles of the fundamental frequency component.

Having explained why vector metering components preferably are calculated over an interval of orthogonality, operations for vector metering according to the present invention will now be explained. Figure 3A is a block diagram illustrating basic operations for vector metering according to the present invention. Line voltage and line current signals are sensed on the power line by voltage sensor **110** and current sensor **120**. In converting means **320**, the sensed line voltage and line current signals **315** are converted into a digital signal **325**.

The digital signal **325** is then input into vector computing means **330**, where a vector metering quantity is computed from the digital signal **325**.

As shown in Figure 3B, in converting means **320** a digital signal **325** is produced, preferably in the form of series of digital line voltage samples **326** and line current samples **327**. The sensed voltages and current signals **315** are sampled in sampling means **321** at predetermined sampling intervals to produce a plurality of line voltage and current samples **322**. The voltage and current samples are converted into corresponding series of digital line voltage samples **326** and digital line current samples **327** in sample converting means **323**. It will be understood by those skilled in the art that in order to accurately compute vector metering quantities for a power line from a fundamental frequency of 60 Hz up to the twenty-third harmonic of the fundamental, or 1380 Hz, the sampling rate must be greater than 2760 samples per second. Preferably, the digital line voltage samples **326** and the digital line current samples **327** represent samples taken at a sampling rate of 3900 samples per second, or a sampling interval of approximately 26 microseconds.

According to the present invention, the vector computing means **330** of Figure 1 and vector computing operation **330** of Figure 3A may resolve the sensed line voltages and current signals **315** into equivalent three-phase quantities. As illustrated in Figure 7, phases preferably are defined for the power line at Block **700** by taking a digital line voltage sample e_k and a corresponding digital line current sample i_k at Block **710**, computing a digital phase-to-neutral voltage sample at Block **720**, and computing a digital phase current sample at Block **730**.

The computations of Blocks **720** and **730** are dependent upon the circuit topology of the power line. As illustrated in Figure 8, circuit identifying means **810** for identifying a circuit topology are provided, with vector computing means **330** performing corresponding calculations of the digital phase-to-neutral voltage signals and the digital phase current signals based on the indicated circuit topology. Those skilled in the art will understand that circuit identifying means **810** may include memory elements, select resistors, DIP switch settings and the like. Exemplary computations for the vector computing means **330** of Figure 3B for different circuit topologies are illustrated in the table of Figure 9. For the table, V_a , V_b and V_c represent digital line voltage samples **326**, I_a , I_b and I_c represent corresponding digital line current samples **327**, while V_{a0} , V_{b0} and V_{c0} represent the corresponding computed digital phase-to-neutral voltage samples and I_{a0} , I_{b0} and I_{c0} represent the corresponding computed digital phase current samples.

Figure 4 illustrates the vector electricity meter of Figures 1 and 3A, with the addition of interval determining means **420** for determining an interval of orthogonality for voltage and current signals on the power line. As discussed above, vector power computations are performed over an interval of orthogonality for voltage and current signals on the power line. Vector computing means **330** may compute a vector metering quantity for the interval of orthogonality determined by interval determining means **420**.

Figure 5 illustrates operations to determine an interval of orthogonality from the sensed voltage and current signals **315** for use by vector computing means **330**. A fundamental frequency reference signal **535** is produced having a frequency approximately equivalent to the fundamental frequency of the sensed line voltage and current signals **315**. In order to produce the fundamental frequency reference signal **535**, a phase-to-neutral voltage signal **715** defining a phase of the power line is produced from the sensed line voltage signals **315** in producing means **510**. The digital phase-to-neutral voltage signal for each phase is input to a narrow-band filter **520** preferably having a passband approximately centered on the nominal fundamental frequency of the power line to produce a fundamental frequency voltage signal **525**.

The fundamental frequency voltage signals **525** produced are combined in linear combining means **530**, which perform a weighted combination of the signals. Preferably, a first fundamental frequency voltage signal is scaled by approximately one-half, a second fundamental frequency voltage signal is scaled by approximately one-fourth and a third fundamental frequency voltage signal is scaled by approximately one-eighth, and the scaled signals summed to produce the fundamental frequency reference signal **535**. The resulting signal has approximately the same fundamental frequency as the fundamental frequency voltage signals **525**, and remains present even if one of the fundamental frequency voltage signals is not present, as in cases such as, for example, metering a three-wire single phase power line or a three-phase power line for which one line voltage is missing.

For a power line characterized by a fundamental frequency and multiple harmonics thereof, a common interval of orthogonality for the voltages and currents on the power line is an interval equivalent to an integral number of cycles of the lowest frequency component, i.e., an integral number of cycles of the fundamental. Thus, an interval of orthogonality for the power line may be determined by detecting means **540** detecting the passage of a predetermined number of cycles of the digital fundamental frequency reference signal **535**. Preferably, the predetermined number of cycles of the fundamental frequency reference

signal 535 used to determine the interval of orthogonality is such that a long enough interval is provided to allow computations for each interval to be completed before computations for the succeeding interval commence, without making the interval so long as to cause various accumulations performed in vector computing means 330 to overflow. Typically, for a nominal 60 Hz power line, 60 cycles of the fundamental frequency reference signal 535, or nominally one second, define an interval of orthogonality according to the present invention. Similarly, for a nominal 50 Hz power line, 50 cycles of the fundamental frequency reference signal 535 define an interval. It will be understood by those skilled in the art that other integral numbers of cycles of the fundamental frequency reference signal 535 may be used with the present invention, however.

It will be understood by those skilled in the art that functions of producing means 510, narrow-band filtering means 520, linear combining means 530 and detecting means 540 may be integrated with vector computing means 330, for example, in the digital signal processor 130 of Figure 1. Those skilled in the art will also understand that these elements may also be implemented separately in analog circuits, digital circuits and combinations thereof. For example, producing means 510 may include a resistor network, narrow-band filter 520 may include an analog bandpass filter, linear combining means 530 may include analog arithmetic circuits and detecting means 540 may include an analog zero-crossing detector and associated counter which provides an interrupt or other signal to vector computing means 330 to indicate an interval of orthogonality.

Computing Power-Related Vector Metering Quantities

Having described a vector electricity meter and basic operations thereof, this section illustrates the computation of various power-related vector metering quantities in the vector computing means 330 of Figures 1 and 3A. As an example of computing a power-related vector metering quantity, Figure 6 illustrates basic operations for computing vector apparent power for an interval of orthogonality from energy, quadergy and apparent volt-ampere-hours computed for each phase of the power during the interval. Figures 10, 11, 13 and 14 illustrate detailed operations for computing energy per phase, quadergy per phase, apparent volt-ampere-hours per phase, and vector apparent volt-ampere-hours for the power line during the interval, respectively. Figures 12A-12B illustrate a reactive power filter for computing quadergy per phase according to the present invention, and operations for implementing the reactive filter in vector computing means 330.

Referring to Figure 6, energy per phase, quadergy per phase, and apparent volt-ampere-hours per phase are computed for the interval of orthogonality at Blocks **610**, **620**, **630**, respectively. Vector apparent volt-ampere-hours for the power line for the interval are computed at Block **640** from the computed energy, quadergy, and apparent volt-ampere-hours for the phases. Typically, distortion volt-ampere hours are computed from the computed energy, quadergy, and apparent volt-ampere-hours per phase:

$$DistortionVAh = \sqrt{(ApparentVAh)^2 - (Energy)^2 - (Quadergy)^2}.$$

Energy, quadergy and distortion volt-ampere-hours for the power line during the interval may be computed by summing the computed energy, quadergy and distortion volt-ampere-hours per phase. Vector apparent volt-ampere-hours for the power line during the interval may be computed from the computed energy, quadergy and distortion volt-ampere-hours. Those skilled in the art will understand, however, that although these computations may be performed individually, they may also be combined in composite computations.

Figure 10 illustrates operations to compute energy per phase for an interval of orthogonality according to the present invention (Block **1000**). A digital phase-to-neutral voltage sample e_k obtained at Block **1010** is multiplied by the corresponding digital phase current sample i_k at Block **1020**. The product of the voltage and current samples for each sampling time is accumulated at Block **1030**. After the end of the interval of orthogonality at Block **1040**, the accumulated product of the voltage and current samples is multiplied by the number of samples N_s and the sampling interval T_s to compute the energy transferred by the power line during the interval of orthogonality at Block **1050**.

Figure 11 illustrates operations to compute apparent volt-ampere-hours per phase according to the present invention (Block **1100**). A digital phase-to-neutral voltage sample e_k obtained at Block **1110** is squared at Block **1120**, and the resulting product is added at Block **1140** to a sum of the previous squared digital phase-to-neutral voltage samples. Similarly, a digital phase current sample i_k obtained at Block **1110** is squared at Block **1130** and the resulting product accumulated at Block **1150**. After the end of an interval at Block **1160**, the accumulated squared digital phase-to-neutral voltage samples and the accumulated square digital phase current samples for each phase from the interval are multiplied at Block **1170** by the square of the number of samples N_s times the sampling interval T_s to produce a quantity equivalent to the square of the apparent volt-ampere-hours for the phase during the interval of orthogonality. The apparent volt-ampere-hours for the phase during the interval is computed at Block **1180** by taking the square root of this product.

Figure 12A illustrates a reactive power filter **1210** for computing reactive power according to the present invention. It will be understood by those skilled in the art that in order to accurately measure reactive power of a phase for all significant harmonics using a phase-shifted form of a phase voltage signal to multiply a corresponding phase current signal, the phase voltage signal must be shifted equally for all of those harmonics. Conventional varhour meters typically cannot achieve such a uniform shift, usually correctly shifting only the fundamental and certain other frequency components.

A uniform phase shift is achieved according to the present invention for a desired frequency band by inputting the phase voltage signal **E** and the phase current signal **I** into a reactive power filter **1210**. First phase-shifting filter **H₁** and second phase-shifting filter **H₃** preferably are recursive digital filters which induce a first phase shift **d₁**. Similarly, second phase-shifting filter **H₂** and fourth phase-shifting filter **H₄** similarly preferably are recursive digital filters which induce a second phase shift **d₂**. The outputs from each of these filters are multiplied as shown and summed to produce an output signal **Q'**, which represents a product of a function of frequency and the reactive power **Q**:

$$e(t) _ E \angle \alpha;$$

$$i(t) _ I \angle \beta;$$

$$H_1(f) = H_3(f) = G_1 \angle \delta_1;$$

$$H_2(f) = H_4(f) = G_2 \angle \delta_2;$$

$$A = G_1 G_2 [EI \cos(\beta - \alpha) \cos(\delta_2 - \delta_1) - EI \sin(\beta - \alpha) \sin(\delta_2 - \delta_1)];$$

$$A = G_1 G_2 [P \cos(\delta_1 - \delta_2) - Q \sin(\delta_1 - \delta_2)];$$

$$B = G_1 G_2 [EI \cos(\beta - \alpha) \cos(\delta_1 - \delta_2) - EI \sin(\beta - \alpha) \sin(\delta_1 - \delta_2)];$$

$$B = G_1 G_2 [P \cos(\delta_1 - \delta_2) + Q \sin(\delta_1 - \delta_2)];$$

and

$$Q' = GB - GA = 2G G_1 G_2 \sin(\delta_1 - \delta_2) Q.$$

It will be understood by those skilled in the art that if **G₁**, **G₂** and **G** are unity, the result reduces to:

$$Q' = 2 \sin(\delta_1 - \delta_2)$$

or

$$Q' = g(f) Q.$$

Transfer characteristics for the function **g(f)/2** is shown in Figure 12B. The transfer functions of the phase-shifting filters **H₁**, **H₂**, **H₃** and **H₄** are chosen such that the phase

difference closely approximates 90 degrees over a band of frequencies $\mathbf{Df}$, thus making the sine of the phase difference $\mathbf{d_1 - d_2}$ approximately unity and the output of the filter $\mathbf{Q'}$ a close approximation of the reactive power over the frequency range. Preferably, $\mathbf{Df}$ spans the range of significant harmonics of the fundamental frequency of the phase voltage and current signals preferably up to and including the twenty-third harmonic. Output $\mathbf{Q'}$ may be integrated to yield an accurate measurement of quadergy.

Those skilled in the art will understand that the reactive power filter **1210** may be implemented using analog circuitry, specialized digital circuitry or by software running on general-purpose processors. Figure 13 illustrates operations for implementing the reactive power filter **1210** of Figure 12A, which may be performed in the vector computing means **330** of Figure 3A (Block **1300**). A first phase-shifting filter is applied to a digital phase-to-neutral voltage sample e_k obtained at Block **1305** to compute a first phase-shifted digital phase-to-neutral voltage sample e_k' at Block **1310**. A second phase-shifting filter is also applied to the digital phase-to-neutral voltage sample e_k to compute a second phase-shifted digital phase-to-neutral voltage sample e_k'' at Block **1315**. Similarly, a third phase-shifting filter having the same transfer function as the first phase-shifting filter is applied to a corresponding digital phase current sample i_k obtained at Block **1305** to compute a first phase-shifted digital phase current sample i_k' at Block **1320**. A fourth phase-shifting filter having the same transfer function as the second phase-shifting filter is also applied to the digital phase current sample i_k to compute a second phase-shifted digital phase current sample i_k'' at Block **1325**.

The second phase-shifted digital phase-to-neutral voltage sample is multiplied by the first phase-shifted phase current sample to compute a first intermediate power sample q_k' at Block **1330**, and the first phase-shifted digital phase-to-neutral voltage sample is multiplied by the second phase-shifted phase current sample to compute a second intermediate power sample q_k'' at Block **1335**. The second intermediate power sample is subtracted from the first intermediate power sample to compute a reactive power sample q_k at Block **1340**. The reactive power samples are accumulated at Block **1345**. At the end of the interval at Block **1350**, the accumulated reactive power samples are multiplied at Block **1355** by the number of samples N_s and the sampling interval T_s to compute the quadergy for the phase during the interval.

Figure 14 illustrates detailed operations to compute vector apparent volt-ampere-hours from the computed energy, quadergy and apparent volt-ampere-hours for the phases of

the power line (Block 1400). At the end of an interval of orthogonality at Block 1410, the accumulated energy and quadergy for each defined phase of the power line are subtracted as vectors at Block 1420 from the apparent volt-ampere-hours for the phase for the interval to compute the distortion volt-ampere-hours for the phase for the interval. Energy, quadergy and distortion volt-ampere-hours are computed for power line by summing at Blocks 1430, 1440 and 1450, respectively, the computed values for these quantities for all the phases of the power line. Vector apparent volt-ampere-hours is computed at Block 1460 as the square root of the sum of the squares of the energy, quadergy and distortion volt-ampere-hours.

According to the present invention, vector metering quantities computed using the vector computing means 330 of Figure 3A may include energy, quadergy, distortion power, and corresponding per phase metering quantities. As will be understood by those skilled in the art, various power factors such as a power factor, a distortion power factor and the like may also be computed from ratios of the various metering quantities.

Vector computing means 330 may also compute related power quantities. Vector computing means 330 preferably stores the number of samples taken during an interval of orthogonality, thus providing a measure of the length of the interval. Vector apparent power may be computed from the vector apparent volt-ampere-hours computed for the interval of orthogonality by dividing the computed vector apparent volt-ampere-hours by the length of the interval. Similarly, active power may be computed from the computed energy for the interval by dividing the computed energy by the length of the interval and reactive power may be computed from the computed quadergy by dividing the computed quadergy by the length of the interval.

It will be understood by those skilled in the art that the computation of vector metering quantities in vector computing means 330 is not limited to the computing steps of the illustrated embodiments. For instance, according to the present invention, vector apparent power may be computed without first computing vector apparent volt-ampere-hours by first computing active, reactive and distortion power for the interval of orthogonality from the energy, quadergy and apparent volt-ampere-hours for the phases of the power line, and then vector summing these components. Vector apparent volt-ampere-hours may be computed from vector apparent power for the orthogonal interval by multiplying the computed vector apparent power for the interval by the number of samples in the interval.

Electric utilities often bill their customers based on energy plus an additional quantity, such as quadergy or phasor volt-ampere-hours. Typically, a "detent" is also applied

to the measurement of these quantities, e.g., the utility may choose to bill only for delivered energy, for both delivered and received energy, lagging quadergy, and the like. Vector computing means 330 may compute an identified vector metering quantity to be metered. The identified metering quantity may be identified to vector computing means 330 for example, by a metering technician through the communications interface 170 illustrated in Figure 1. Those skilled in the art will understand that the identified quantity may include an associated detent, such as leading quadergy only, received and delivered energy, and the like.

Computing Other Vector Metering Quantities

The vector computing means 330 illustrated in Figure 3A may also compute other vector metering quantities useful for power system safety and maintenance, meter installation and the like, such as a phase angle, an effective line voltage, an expected nominal operating voltage, a line voltage status, a neutral current magnitude, and a neutral current status. Figures 15A-15B illustrate operations for computing a phase angle of a sensed line voltage 315 with respect to the fundamental frequency reference signal 535 of Figure 5. Figures 16A-B illustrate operations for computing an effective line voltage and an expected nominal operating voltage. Figure 17 illustrates operations for using the computed effective line voltage to compute a line voltage status. Finally, Figure 18 illustrates operations for computing a neutral current magnitude and a neutral current status.

Operations for determining a phase angle of a sensed line voltage signal 315 with respect to the fundamental frequency reference signal 535 (see Figure 5) are illustrated in Figures 15A-15B. Digital line voltage samples obtained at Block 1510 are narrow-band filtered at Block 1520 to compute a series of digital fundamental frequency line voltage samples representing a fundamental frequency component signal of the sensed line voltage signal. A set of migratory decimated digital line voltage samples are selected at Block 1530, one each from a succession of cycles of the fundamental frequency reference signal 535.

In particular, as illustrated in Figure 15B, each migratory decimated sample is selected such that it comes from a sampling time delayed a predetermined decimation interval N_1 from the previous decimated sample with respect to the fundamental frequency reference signal 535. Preferably, the first decimated sample e_{d1} is the first sample following the start of the interval of orthogonality, illustrated in Figure 18B as following a first zero crossing of the fundamental frequency reference signal 535. The next decimated sample e_{d2} is selected from samples taken during the next cycle of the fundamental frequency reference

signal **535**, delayed the decimation interval N_1 from the point on the waveform of the fundamental frequency reference signal **535** where the preceding sample e_{d1} was selected.

Preferably, the decimation interval N_1 is such that the set of decimated samples selected during an interval of orthogonality approximately represent a period of the fundamental frequency component signal of the sensed line voltage signal **315**. For a 60 Hz power line sampled at 3900 samples per second, for example, approximately 65 samples are taken in a cycle. If the migratory decimated samples are selected from successive cycles of the fundamental frequency reference signal **535** such that each is delayed a decimation interval N_1 of 13 samples from the preceding sample with respect to the fundamental frequency reference signal, a set of 5 decimated samples may be selected during an interval of 60 cycles of the fundamental frequency reference signal **535**, thus approximating a period of the fundamental frequency component signal of the sensed line voltage **315**. For a 50Hz system at the same sampling rate and decimation interval N_1 , the waveform may be represented by 6 samples.

The phase angle of the fundamental component of the line voltage may be computed after an interval of orthogonality at Block **1540** using Fourier analysis at Block **1550**. Those skilled in the art will understand that

$$\theta = \tan^{-1} \left(\frac{E_c}{E_s} \right)$$

where

$$E_c = \frac{2}{M} \sum_{k=0}^{M-1} e_{dk} \cos \frac{2\pi k N_c N_1 N_s}{F_s^2}$$

and

$$E_s = \frac{2}{M} \sum_{k=0}^{M-1} e_{dk} \sin \frac{2\pi k N_c N_1 N_s}{F_s^2}$$

are the cosine and sine terms, respectively, of the Fourier series representation of the fundamental frequency component signal of the sensed line voltage signal **315**, based on M migratory decimated samples. For these equations, N_s is the number of samples taken during an interval of orthogonality, N_c is the nominal fundamental frequency of the power line (50 or 60 Hz), N_1 is the decimation interval, and f_s is the sampling frequency.

The vector computing means **330** of Figures 1 and 3A may also compute an effective line voltage and a nominal expected operating voltage for the power line from a sensed line voltage signal **315**, and monitor the sensed line voltage signals **315** based on the computed expected nominal operating voltage. Operations for computing an expected nominal operating voltage are illustrated in Figure 16A (Block **1600**). The effective voltage of a sensed line voltage signal **315** is computed at Block **1610** for an interval of orthogonality, preferably soon after or during an initialization of vector computing means **330**. A phase angle for the line voltage may also be computed at Block **1500**, also preferably during an initialization or soon thereafter. The expected nominal operating voltage is selected at Block **1620** from predetermined nominal operating voltages **1630** based on the computed effective voltage and the computed phase angle. Preferably, the selected expected nominal operating voltage is the predetermined nominal operating voltage **1630** closest to the computed effective voltage. The predetermined nominal operating voltages **1630** preferably include standard line voltages such as 120 volts, 240 volts, 277 volts and the like.

The expected nominal operating voltage may also be computed based on the computed phase angle. For example, the computed effective voltage may fall approximately equally between two predetermined nominal operating voltages, such as 240 volts and 277 volts, which typically correspond to a three-wire single phase service and a four-wire three phase wye service, respectively. As will be understood by those skilled in the art, these two circuit topologies may be differentiated by the different phase angles between the line voltages, i.e., the line voltages in the 277 volt service typically will be separated by a nominal phase angle of 120 degrees while the line voltages of the 240 volt service typically are separated by a nominal 60 degrees. Vector computing means **330** may compute the expected nominal operating voltage by selecting the predetermined nominal operating voltage having a phase angle characteristic closest to the computed phase angle.

Figure 16B illustrates detailed operations for computing an effective line voltage (Block **1610**). Line voltage samples obtained at Block **1611** are narrow-band filtered at Block **1612**, the filtered samples squared at Block **1613**, and the squared samples accumulated at Block **1614**. After the end of the interval at Block **1615**, the effective voltage during the interval is computed by taking the square root of the accumulated digital line squared voltage samples divided by the number of samples N_s times the sampling interval T_s at Block **1616**.

Referring now to Figure 17, the computed expected nominal line voltage may be used to compute a line voltage status (Block **1700**). At Block **1710** an effective line

voltage is computed. The computed line voltage status is compared to an expected nominal operating voltage, based on predetermined tolerance **1740**, to compute a line voltage status at Block **1720**. It will be understood by those skilled in the art that although the status computed preferably relates to an overvoltage or undervoltage condition on the power line, other line voltage status may be computed, such as statistical deviation of the computed effective voltage with respect to the expected nominal operating voltage and the like. It will also be understood that the computed status may be communicated from vector computing means **330** to a user, a switchgear control system, and the like, for maintenance, shutdown and other purposes, for example, through the communications interface **170** or display **160** of Figure 1. It will be also be understood that effective line voltage and line voltage status may be computed at every interval of orthogonality or at other predetermined intervals.

Referring now to Figure 18, vector computing means **330** may also compute and monitor a neutral current magnitude from computed digital phase current samples (Block **1800**). Digital phase current samples obtained for each phase at Block **1810** are summed at Block **1820** and the sum squared at Block **1830**. The neutral current magnitude is computed by taking the square root of the squared sum at Block **1830**. A neutral current status may be computed at Block **1840** by comparing the computed neutral current magnitude to a predetermined threshold **1842**. It will be understood by those skilled in the art that the computed status may be communicated to a user, a switchgear control system, and the like, for maintenance, shutdown and other purposes. It will also be understood that neutral current magnitude and status may be computed at every sample time, at every interval of orthogonality, or at other predetermined intervals.

In the drawings and specification, there have been disclosed typical preferred embodiments of the invention and, although specific terms are employed, they are used in a generic and descriptive sense only and not for purposes of limitation, the scope of the invention being set forth in the following claims.

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THAT WHICH IS CLAIMED:

1. A method of metering electricity on a power line having at least two conducting paths, comprising the following steps which are performed by an electricity meter:

sensing a line voltage signal and a line current signal on the power line;
determining an interval of orthogonality for the sensed line voltage and line current signals;
converting the sensed line voltage and line current signals into a digital signal; and
computing a vector metering quantity for the power line for the determined interval of orthogonality from the digital signal.

2. A method according to Claim 1 wherein said step of computing a vector metering quantity is preceded by the step of:

computing from the digital signal, a corresponding digital phase-to-neutral voltage signal and digital phase current signal to thereby define a phase of the power line; and
wherein said step of computing a vector metering quantity comprises the step of computing a vector metering quantity for the power line for the interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

3. A method according to Claim 2 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:

computing vector apparent volt-ampere-hours for the power line for the interval of orthogonality from the digital phase-to-neutral signal and digital phase current signal.

4. A method according to Claim 3 wherein said step of computing vector apparent volt-ampere-hours for the power line for the interval of orthogonality comprises the steps of:

computing from the digital phase-to-neutral voltage signal and the digital phase current signal, computed energy transferred by each defined phase of the power line for the interval of orthogonality;

computing from the digital phase-to-neutral voltage signal and digital phase current signal, quadergy for each defined phase of the power line for the interval of orthogonality

computing from the digital phase-to-neutral voltage signal and digital phase current signal, apparent volt-ampere-hours for each defined phase of the power line for the interval of orthogonality; and

computing vector apparent volt-ampere-hours for the power line for the interval of orthogonality from the computed energy, quadergy and apparent volt-ampere-hours for the defined phases of the power line for the interval of orthogonality.

5. A method according to Claim 3 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality further comprises the step of computing vector apparent power for the power line for the interval of orthogonality from the computed apparent volt-ampere-hours for the power line for the interval of orthogonality.

6. A method according to Claim 2 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:
computing arithmetic apparent volt-ampere-hours for the power line for the interval of orthogonality from the digital phase-to-neutral signal and digital phase current signal.

7. A method according to Claim 6 wherein said step of computing arithmetic apparent volt-ampere-hours for the power line for the interval of orthogonality comprises the steps of:

computing from the digital phase-to-neutral voltage signal and digital phase current signal, apparent volt-ampere-hours for each defined phase of the power line for the interval of orthogonality; and

computing arithmetic apparent volt-ampere-hours for the power line for the interval of orthogonality by summing the computed apparent volt-ampere-hours for the defined phases of the power line for the interval of orthogonality.

8. A method according to Claim 6 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality further comprises the step of computing arithmetic apparent power for the power line for the interval of

orthogonality from the computed arithmetic apparent volt-ampere-hours for the power line for the interval of orthogonality.

9. A method according to Claim 2 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of: computing phasor volt-ampere-hours for the power line for an interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

10. A method according to Claim 9 wherein said step of computing phasor volt-ampere-hours for the power line for the interval of orthogonality comprises the steps of:

computing from the digital phase-to-neutral voltage signal and digital phase current signal, energy for each defined phase of the power line for the interval of orthogonality;

computing from the digital phase-to-neutral voltage signal and digital phase current signal, quadergy for each defined phase of the power line for the interval of orthogonality; and

computing phasor volt-ampere-hours for the power line for the interval of orthogonality from the computed energy and quadergy for the defined phases of the power line for the interval of orthogonality.

11. A method according to Claim 9 wherein the step of computing a vector metering quantity for the power line for the interval of orthogonality further comprises the step of computing phasor power for the power line for the interval of orthogonality from the computed phasor volt-ampere-hours for the power line for the interval of orthogonality.

12. A method according to Claim 2 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:

computing quadergy for the power line for an interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

13. A method according to Claim 12 wherein said step of computing quadergy for the power line for the interval of orthogonality comprises the steps of:

computing from the digital phase-to-neutral voltage signal and digital phase current signal, quadergy for each defined phase of the power line for the interval of orthogonality; and

summing the quadergy for the defined phases of the power line for the interval of orthogonality to thereby compute quadergy for the power line for the interval of orthogonality.

14. A method according to Claim 12 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality further comprises the step of computing reactive power for the power line for the interval of orthogonality from the computed quadergy for the power line for the interval of orthogonality.

15. A method according to Claim 13 wherein said step of computing quadergy for each defined phase of the power line for the interval of orthogonality comprises the following steps, which are performed for each phase-to-neutral voltage signal and corresponding phase current signal:

applying a first phase-shifting filter having a first transfer function to the digital phase-to-neutral voltage signal to thereby compute a first phase-shifted digital phase-to-neutral voltage signal;

applying a second phase-shifting filter having a second transfer function to the digital phase-to-neutral voltage signal to thereby compute a second phase-shifted digital phase-to-voltage signal;

applying a third phase-shifting filter having the first transfer function to the digital phase current signal to thereby compute a first phase-shifted digital current signal;

applying a fourth phase-shifting filter having the second transfer function to the digital phase current signal to thereby compute a second phase-shifted digital current signal;

multiplying the first phase-shifted digital phase-to-neutral voltage signal by the second phase-shifted digital phase current signal to thereby compute a first intermediate power product signal;

multiplying the second phase-shifted digital phase-to-neutral voltage signal by the first phase-shifted digital phase current signal to thereby compute a second intermediate power product signal; and

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computing quadergy for the defined phase of the power line for the interval of orthogonality from the first intermediate power product signal and the second intermediate power product signal.

16. A method according to Claim 15:

wherein said step of applying a first phase-shifting filter having a first transfer function to the digital phase-to-neutral voltage signal comprises the step of applying a first phase-shifting filter having a first phase-versus-frequency transfer function to the digital phase-to-neutral voltage signal to thereby compute a first phase-shifted digital phase-to-neutral voltage signal;

wherein said step of applying a second phase-shifting filter having a second transfer function to the digital phase-to-neutral voltage signal comprises the step of applying a second phase-shifting filter having a second phase-versus frequency transfer function to the digital phase-to-neutral voltage signal to thereby compute a second phase-shifted digital phase-to-neutral voltage signal, the second phase-versus-frequency transfer function differing from the first phase-versus-frequency transfer function by approximately 90 degrees over a frequency range from a first frequency less than a lowest frequency component of a fundamental frequency reference signal to a second frequency greater than a twenty-third harmonic of a highest frequency component of the fundamental frequency reference signal;

wherein said step of applying a third phase-shifting filter having the first transfer function to the digital phase current signal comprises the step of applying a third phase-shifting filter having the first phase-versus-frequency transfer function to the digital phase current signal to thereby compute a first phase-shifted digital phase current signal; and

wherein said step of applying a fourth phase-shifting filter having the second transfer function to the digital phase current signal comprises the step of applying a fourth phase-shifting filter having the second phase-versus-frequency transfer function to the digital phase current signal to thereby compute a second phase-shifted digital phase current signal.

17. A method according to claim 2 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:
computing energy for the power line for the interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

18. A method according to Claim 17 wherein said step of computing energy for the power line for the interval of orthogonality comprises the steps of:

computing from the digital phase-to-neutral voltage signal and digital phase current signal, energy for each defined phase of the power line for the interval of orthogonality; and
summing the energy for the defined phases of the power line for the interval of orthogonality to thereby compute energy for the power line for the interval of orthogonality.

19. A method according to Claim 17 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality further comprises the step of computing active power for the power line for the interval of orthogonality from the computed energy for the power line for the interval of orthogonality.

20. A method according to Claim 2 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:
computing distortion volt-ampere-hours for the power line for the interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

21. A method according to Claim 20 wherein said step of computing distortion volt-ampere-hours for the power line for the interval of orthogonality comprises the steps of:

computing from the digital phase-to-neutral voltage signal and digital phase current signal from the interval of orthogonality, energy transferred by each defined phase of the power line for the interval of orthogonality;

computing from the digital phase-to-neutral voltage signal and digital phase current signal, quadergy for each defined phase of the power line for the interval of orthogonality;

computing from the digital phase-to-neutral voltage signal and digital phase current signal, apparent volt-ampere-hours for each defined phase of the power line for the interval of orthogonality; and

computing distortion volt-ampere-hours for the power line for the interval of orthogonality from the energy, quadergy and apparent volt-ampere-hours for the defined phases of the power line for the interval of orthogonality.

22. A method according to Claim 20 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality further comprises the

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step of computing distortion power for the power line for the interval of orthogonality from the distortion volt-ampere-hours for the power line for the interval of orthogonality.

23. A method according to Claim 2 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:
computing a power factor for the power line for the interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

24. A method according to Claim 23 wherein said step of computing a power factor for the power line for the interval of orthogonality comprises the steps of:
computing from the digital phase-to-neutral voltage signal and digital phase current signal from an interval of orthogonality, energy transferred by each defined phase of the power line for the interval of orthogonality;
computing from the digital phase-to-neutral voltage signal and digital phase current signal, quadergy for each defined phase of the power line for the interval of orthogonality;
and
computing a power factor for the power line for the interval of orthogonality from the energy and quadergy for the defined phases of the power line for the interval of orthogonality.

25. A method according to Claim 2 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:
computing a distortion power factor for the power line for an interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

26. A method according to Claim 25 wherein said step of computing a distortion power factor for the power line for the interval of orthogonality comprises the steps of:
computing from the digital phase-to-neutral voltage signal and digital phase current signal, computed energy for each defined phase of the power line for the interval of orthogonality;
computing from the digital phase-to-neutral voltage signal and digital phase current signal, quadergy for each defined phase of the power line for the interval of orthogonality;

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computing from the digital phase-to-neutral voltage signal and digital phase current signal, apparent volt-ampere-hours for each defined phase of the power line for the interval of orthogonality; and

computing a distortion power factor for the power line for the interval of orthogonality from the computed energy, quadergy and apparent volt-ampere-hours for the defined phases of the power line for the interval of orthogonality.

27. A method according to claim 1 wherein said step of determining an interval of orthogonality comprises the step of:

producing a phase-to-neutral voltage signal from the sensed line voltage and line current signals to thereby define a phase of the power line; and

determining an interval of orthogonality from the phase-to-neutral voltage signal.

28. A method according to claim 27 wherein said step of determining an interval of orthogonality from the phase-to-neutral voltage comprises the steps of:

producing a fundamental frequency reference signal from the phase-to-neutral voltage signal; and

detecting passage of a predetermined number of cycles of the fundamental frequency reference signal to thereby determine an interval of orthogonality.

29. A method according to claim 28 wherein said step of detecting passage of a predetermined number of cycles of the fundamental frequency reference signal comprises the step of detecting passage of the fundamental frequency reference signal through a predetermined threshold value.

30. A method according to claim 28 wherein said step of producing a fundamental frequency reference signal comprises the steps of:

narrow-band filtering each phase-to-neutral voltage signal to produce a corresponding fundamental frequency phase-to-neutral voltage; and

linearly combining the fundamental frequency phase-to-neutral voltage signals to produce a fundamental frequency reference signal.

31. A method according to Claim 30 wherein the step of linearly combining the fundamental frequency phase-to-neutral voltage signals comprises the steps of:

scaling a first fundamental frequency phase-to-neutral voltage signal by one-half to produce a first scaled fundamental frequency voltage signal;

scaling a second fundamental frequency phase-to-neutral voltage signal by one-fourth to produce a second scaled fundamental frequency phase-to-neutral voltage signal;

scaling a third fundamental frequency phase-to-neutral voltage by one-eighth to produce a third scaled fundamental frequency phase-to-neutral voltage signal; and

summing the scaled fundamental frequency phase-to-neutral voltage signals to produce a fundamental frequency reference signal.

32. A method according to Claim 2 wherein said step of computing a digital phase-to-neutral voltage signal and digital phase current signal is preceded by the step of:

identifying a circuit topology of the power line; and

wherein the step of computing a digital phase-to-neutral voltage signal and digital phase current signal comprises the step of computing a digital phase-to-neutral voltage signal and digital phase current signal based upon the identified circuit topology of the power line.

33. A method according to Claim 2 wherein said step of computing a vector metering quantity comprises the step of:

computing a neutral current magnitude for the power line from the computed digital phase current signal.

34. A method according to Claim 33 wherein said step of computing a vector metering quantity further comprises the step of:

comparing the computed neutral current magnitude to a predetermined threshold to thereby compute a neutral current status.

35. A method according to Claim 2 wherein said step of computing a vector metering quantity comprises the step of:

computing an effective line voltage for a line of the power line for the interval of orthogonality from the digital signal.

36. A method according to Claim 35 wherein said step of computing a vector metering quantity further comprises the step of:

comparing the computed effective line voltage to an expected nominal operating voltage to thereby compute a line voltage status.

37. A method according to Claim 2 wherein said step of computing a vector metering quantity comprises the step of:

computing an expected nominal operating voltage from the digital signal.

38. A method according to Claim 37 wherein said step of computing an expected nominal operating voltage comprises the steps of:

computing an effective line voltage for a line of the power line for an interval of orthogonality from the digital signal; and

selecting from a plurality of predetermined nominal operating voltages, an expected nominal operating voltage closest to the computed effective line voltage.

39. A method according to Claim 38 wherein said step of computing an expected nominal operating voltage further comprises the step of:

computing a phase angle for the line of the power line from the digital signal;

and wherein said step of selecting an expected nominal operating voltage comprises the step of selecting from a plurality of predetermined nominal operating voltages, each of the predetermined nominal operating voltages having an associated nominal phase angle, an expected nominal operating voltage approximating the computed effective line voltage and having an associated nominal phase angle closest to the computed phase angle.

40. A method according to Claim 2 wherein the step of computing a vector metering quantity is preceded by the step of:

identifying a vector metering quantity to be metered; and

wherein the step of computing a vector metering quantity comprises the step of computing the identified vector metering quantity.

41. A method according to Claim 2 wherein said step of computing a vector metering quantity is preceded by the step of:

identifying a vector metering quantity to be metered and an associated metering detent; and

wherein said step of computing a vector metering quantity comprises the step of computing the identified vector metering quantity based upon the associated metering detent.

42. A method according to Claim 2 wherein said step of computing a digital phase-to-neutral voltage sample and digital phase current sample is preceded by the step of:
detecting absence of a voltage on a conducting path of the power line; and
wherein the step of computing a digital phase-to-neutral voltage sample and digital phase current sample comprises the step of computing a digital phase-to-neutral voltage sample and digital phase current sample notwithstanding absence of the voltage on the conducting path power line.

43. A method of metering electricity on a power line having at least two conducting paths, comprising the following steps which are performed by an electricity meter:
sensing a line voltage signal and a line current signal on the power line;
determining from the sensed line voltage and line current signals, an interval of orthogonality for the sensed line voltage and line current signals on the power line;
sampling the sensed line voltage and line current signals at a plurality of consecutive sampling times separated by a predetermined sampling interval to thereby obtain a plurality of line voltage samples and line current samples;
converting the line voltage samples into corresponding digital line voltage samples and converting the line current samples into corresponding digital line current samples; and
computing a vector metering quantity for the power line for the determined interval of orthogonality from the digital line voltage samples and digital line current samples from the interval of orthogonality.

44. A method according to Claim 43 wherein said step of computing a vector metering quantity is preceded by the step of:

computing from the digital line voltage samples and digital line current samples, corresponding digital phase-to-neutral voltage samples and digital phase current samples to
5 thereby define a phase of the power line; and

wherein said step of computing a vector metering quantity comprises the step of computing a vector metering quantity for the power line for the interval of orthogonality from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality.

45. A method according to Claim 44 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:

computing vector apparent volt-ampere-hours for the power line for the interval of orthogonality from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality.

46. A method according to Claim 45 wherein said step of computing vector apparent volt-ampere-hours for the power line for the interval of orthogonality comprises the steps of:

computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, energy transferred by each defined phase of the power line for the interval of orthogonality;

computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, quadergy for each defined phase of the power line for the interval of orthogonality;

computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, apparent volt-ampere-hours for each defined phase of the power line for the interval of orthogonality; and

computing vector apparent volt-ampere-hours for the power line for the interval of orthogonality from the energy, quadergy and apparent volt-ampere-hours for the defined phases of the power line for the interval of orthogonality.

47. A method according to Claim 45 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality further comprises the step of dividing the computed apparent volt-ampere-hours for the power line for the interval of orthogonality by the number of sampling intervals for the interval of orthogonality to thereby compute vector apparent power for the power line for the interval of orthogonality.

48. A method according to Claim 44 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:
computing phasor volt-ampere-hours for the power line for the interval of orthogonality from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality.

49. A method according to Claim 48 wherein said step of computing phasor volt-ampere-hours for the power line for the interval of orthogonality comprises the steps of:
computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, energy for each defined phase of the power line for the interval of orthogonality;

computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, quadergy for each defined phase of the power line for the interval of orthogonality; and

computing phasor volt-ampere-hours for the power line for the interval of orthogonality from the energy and quadergy for the defined phases of the power line for the interval of orthogonality.

50. A method according to Claim 48 wherein the step of computing a vector metering quantity for the power line for an interval of orthogonality further comprises the step of dividing the computed phasor volt-ampere-hours for the power line for the interval of orthogonality by the number of sampling intervals for the interval of orthogonality to thereby compute phasor power for the power line for the interval of orthogonality.

51. A method according to Claim 44 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:
computing quadergy for the power line for the interval of orthogonality from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality.

52. A method according to Claim 51 wherein said step of computing quadergy for the power line for the interval of orthogonality comprises the steps of:

computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, quadergy for each defined phase of the power line for the interval of orthogonality; and

summing the quadergy for the defined phases of the power line for the interval of orthogonality to thereby compute quadergy for the power line for the interval of orthogonality.

53. A method according to Claim 51 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality further comprises the step of dividing the computed quadergy for the power line for the interval of orthogonality by the number of sampling intervals for the interval of orthogonality to thereby compute reactive power for the power line for the interval of orthogonality.

54. A method according to Claim 52 wherein said step of computing quadergy for each defined phase of the power line for the interval of orthogonality comprises the following steps, which are performed for the phase-to-neutral voltage samples and corresponding phase current samples:

applying a first phase-shifting filter having a first transfer function to the digital phase-to-neutral voltage sample to thereby compute a first phase-shifted digital phase-to-neutral voltage sample;

applying a second phase-shifting filter having a second transfer function to the digital phase-to-neutral voltage sample to thereby compute a second phase-shifted digital phase-to-neutral voltage sample;

applying a third phase-shifting filter having the first transfer function to the digital phase current sample to thereby compute a first phase-shifted digital current sample;

applying a fourth phase-shifting filter to the digital phase current sample to thereby compute a second phase-shifted digital current sample;

multiplying the first phase-shifted digital phase-to-neutral voltage sample by the second phase-shifted digital phase current sample to thereby compute a first intermediate power product sample;

multiplying the second phase-shifted digital phase-to-neutral voltage sample by the first phase-shifted digital phase current sample to thereby compute a second intermediate power product sample;

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computing the quadergy for the phase of the power line for the interval of orthogonality from the first intermediate power product samples and the second intermediate power product samples from the interval of orthogonality.

55. A method according to Claim 54:

wherein said step of applying a first phase-shifting filter to the digital phase-to-neutral voltage sample comprises the step of applying a first phase-shifting filter having a first phase-versus-frequency transfer function to the digital phase-to-neutral voltage sample to thereby compute a first phase-shifted digital phase-to-neutral voltage sample;

wherein said step of computing a second phase-shifted digital phase-to-voltage sample comprises the step of applying a second phase-shifting filter having a second phase-versus frequency transfer function to the digital phase-to-neutral voltage sample to thereby compute a second phase-shifted digital phase-to-neutral voltage sample, the second phase-versus-frequency transfer function differing from the first phase-versus-frequency transfer function by approximately 90 degrees over a frequency range from less than a lowest frequency component of a fundamental frequency reference signal to greater than a twenty-third harmonic of a highest frequency component of the-fundamental frequency reference signal;

wherein said step of applying a third phase-shifting filter to the digital phase current sample comprises the step of applying a third phase-shifting filter having the first phase-versus-frequency transfer function to the digital phase current sample to thereby compute a first phase-shifted digital phase current sample; and

wherein said step of applying a fourth phase-shifting filter to the digital phase current sample comprises the step of applying a fourth phase-shifting filter having the second phase-versus-frequency transfer function to the digital phase current sample to thereby compute a second phase-shifted phase current sample.

56. A method according to Claim 44 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:

computing energy for the power line for the interval of orthogonality from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality.

57. A method according to Claim 56 wherein said step of computing energy for the power line for the interval of orthogonality comprises the steps of:

computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, energy for each defined phase of the power line for the interval of orthogonality; and

summing the energy for the defined phases of the power line for the interval of orthogonality to thereby compute energy for the power line for the interval of orthogonality.

58. A method according to Claim 56 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality further comprises the step of dividing the computed energy for the power line for the interval of orthogonality by the number of sampling intervals for the interval of orthogonality to thereby compute active power for the power line for the interval of orthogonality.

59. A method according to Claim 44 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:

computing distortion volt-ampere-hours for the power line for the interval of orthogonality from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality.

60. A method according to Claim 59 wherein said step of computing distortion volt-ampere-hours for the power line for the interval of orthogonality comprises the steps of:

computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, energy transferred by each defined phase of the power line for the interval of orthogonality;

computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, quadergy for each defined phase of the power line for the interval of orthogonality;

computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, apparent volt-ampere-hours for each defined phase of the power line for the interval of orthogonality; and

computing distortion volt-ampere-hours for the power line for the interval of orthogonality from the energy, quadergy and apparent volt-ampere-hours for the defined phases of the power line for the interval of orthogonality.

61. A method according to Claim 59 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality further comprises the step of dividing the computed distortion volt-ampere-hours for the power line for the interval of orthogonality by the number of sampling intervals for the interval of orthogonality to thereby compute distortion power for the power line for the interval of orthogonality.

62. A method according to Claim 44 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:
computing a power factor for the power line for the interval of orthogonality from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality.

63. A method according to Claim 62 wherein said step of computing a power factor for the power line for the interval of orthogonality comprises the steps of:
computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, energy transferred by each defined phase of the power line for the interval of orthogonality;
computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, quadergy for each defined phase of the power line for the interval of orthogonality; and
computing a power factor for the power line for the interval of orthogonality from the energy and quadergy for the defined phases of the power line for the interval of orthogonality.

64. A method according to Claim 44 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the step of:
computing a distortion power factor for the power line for the interval of orthogonality from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality.

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65. A method according to Claim 64 wherein said step of computing a distortion power factor for the power line for the interval of orthogonality comprises the steps of:

- computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, energy for each defined phase of the power line for the interval of orthogonality;
- computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, quadergy for each defined phase of the power line for the interval of orthogonality;
- computing from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality, apparent volt-ampere-hours for each defined phase of the power line for the interval of orthogonality; and
- computing a distortion power factor for the power line for the interval of orthogonality from the computed energy, quadergy and apparent volt-ampere-hours for the defined phases of the power line for the interval of orthogonality.

66. A method according to Claim 44 wherein said step of computing a vector metering quantity for the power line for the interval of orthogonality comprises the steps of:

- a narrow-band filtering digital line voltage samples corresponding to a sensed line voltage signal to obtain a series of digital fundamental frequency line voltage samples representing a fundamental frequency component of the sensed line voltage signal; and
- computing a phase angle between the fundamental frequency component of the sensed line voltage signal and a fundamental frequency reference signal.

67. A method according to Claim 66 wherein said step of computing a phase angle between a fundamental frequency component of the sensed line voltage and the fundamental frequency component of a second sensed line voltage comprises the steps of:

- selecting a set of migratory decimated digital fundamental frequency line voltage samples from the series of digital fundamental frequency line voltage samples, each migratory decimated digital fundamental frequency line voltage sample corresponding to a sampling time delayed a predetermined migratory decimation interval from the sampling time of the preceding migratory decimated sample with respect to passage of a fundamental frequency reference signal through a predetermined value so that the set of

migratory decimated fundamental frequency digital line voltage samples approximately represent a period of the fundamental frequency component of the sensed line voltage; and

Fourier analyzing the set of migratory decimated digital fundamental frequency line voltage samples to thereby compute a phase angle between the fundamental frequency component of the sensed line voltage and the fundamental frequency reference signal.

68. A method according to Claim 44 wherein said step of computing a digital phase-to-neutral voltage sample and digital phase current sample is preceded by the step of:

identifying a circuit topology of the power line; and

wherein the step of computing a digital phase-to-neutral voltage sample and digital phase current sample comprises the step of computing a digital phase-to-neutral voltage sample and digital phase current sample based upon the identified circuit topology of the power line.

69. A method according to Claim 44 further comprising the step of:

computing a neutral current magnitude for the power line from the computed digital phase current samples.

70. A method according to Claim 69 wherein said step of computing a vector metering quantity further comprises the step of:

comparing the computed neutral current magnitude to a predetermined threshold to thereby compute a neutral current status.

71. A method according to Claim 44 wherein said step of computing a vector metering quantity comprises the step of:

computing an effective line voltage for a line of the power line for the interval of orthogonality from the digital line voltage samples from the interval of orthogonality.

72. A method according to Claim 71 wherein said step of computing a vector metering quantity further comprises the step of:

comparing the computed effective line voltage to a predetermined expected nominal operating voltage to thereby compute a line voltage status.

73. A method according to Claim 44 wherein said step of computing a vector metering quantity comprises the step of:

computing an expected nominal operating voltage from the digital line voltage samples from the interval of orthogonality.

74. A method according to Claim 73 wherein said step of computing an expected nominal operating voltage comprises the steps of:

computing an effective line voltage for a line of the power line for an interval of orthogonality from the digital line voltage samples from the interval of orthogonality; and

selecting from a plurality of predetermined nominal operating voltages, an expected nominal operating voltage closest to the computed effective line voltage.

75. A method according to Claim 74 wherein said step of computing an expected nominal operating voltage is preceded by the step of:

computing a phase angle for the line of the power line from the digital line voltage samples from the interval of orthogonality;

and wherein said step of selecting an expected nominal operating voltage comprises the step of selecting from a plurality of predetermined nominal operating voltages, each of the predetermined nominal operating voltages having an associated nominal phase angle relationship, an expected nominal operating voltage approximating the computed effective line voltage and having an associated nominal phase angle relationship closest to the computed phase angle.

76. A method according to Claim 44 wherein the step of computing a vector metering quantity is preceded by the step of:

identifying a vector metering quantity to be metered; and

wherein the step of computing a vector metering quantity comprises the step of computing the identified vector metering quantity.

77. A method according to Claim 44 wherein said step of computing a digital phase-to-neutral voltage sample and digital phase current sample is preceded by the step of:

detecting absence of a voltage on a conducting path of the power line; and

wherein said step of computing a digital phase-to-neutral voltage sample and digital phase current sample comprises the step of computing a digital phase-to-neutral voltage sample and digital phase current sample notwithstanding absence of a voltage on the conducting path of the power line.

78. A vector electricity meter for metering electricity on a power line having at least two conducting paths, the meter comprising:

a voltage sensor which senses a line voltage signal on the power line;

a current sensor which senses a line current signal on the power line;

means, responsive to the sensed line voltage and line current signals for determining an interval of orthogonality for the sensed line voltage and line current signals;

means for converting the sensed line voltage and line current signals into a digital signal; and

vector computing means, responsive to said interval determining means and to said converting means, for computing a vector metering quantity for the power line for the determined interval of orthogonality from the digital signal.

79. A vector electricity meter according to Claim 78 wherein said vector computing means comprises means for computing from the digital signal, a corresponding digital phase-to-neutral voltage signal and digital phase current signal to thereby define a phase of the power line.

80. A vector electricity meter according to Claim 79 wherein said vector computing means comprises means for computing vector apparent volt-ampere-hours for the power line for the interval of orthogonality from the digital phase-to-neutral signal and digital phase current signal.

81. A vector electricity meter according to Claim 80 wherein said vector computing means further comprises means for computing vector apparent power for the power line for the interval of orthogonality from the computed apparent volt-ampere-hours for the power line for the interval of orthogonality.

82. A vector electricity meter according to Claim 79 wherein said vector computing means comprises means for computing arithmetic apparent volt-ampere-hours for the power line for the interval of orthogonality from the digital phase-to-neutral signal and digital phase current signal.

83. A vector electricity meter according to Claim 82 wherein said vector computing means further comprises means for computing arithmetic apparent power for the power line for the interval of orthogonality from the computed arithmetic apparent volt-ampere-hours for the power line for the interval of orthogonality.

84. A vector electricity meter according to Claim 79 wherein said vector computing means comprises means for computing phasor volt-ampere-hours for the power line for the interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

85. A vector electricity meter according to Claim 84 wherein said vector computing means further comprises means for computing phasor power for the power line for the interval of orthogonality from the computed phasor volt-ampere-hours for the power line for the interval of orthogonality.

86. A vector electricity meter according to Claim 79 wherein said vector computing means comprises means for computing quadergy for the power line for the interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

87. A vector electricity meter according to Claim 86 wherein said vector computing means further comprises means for computing reactive power for the power line for the interval of orthogonality from the computed quadergy for the power line for the interval of orthogonality.

88. A vector electricity meter according to Claim 86 wherein said means for computing quadergy for the power line for the interval of orthogonality comprises:

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a first phase-shifting filter which produces a first phase-shifted digital phase-to-neutral voltage signal from the digital phase-to-neutral voltage signal, said first phase-shifting filter having a first transfer function;

a second phase-shifting filter which produces a second phase-shifted digital phase-to-voltage signal from the digital phase-to-neutral voltage signal, said second phase-shifting filter having a second transfer function;

a third phase-shifting filter which produces a first phase-shifted digital current signal from the digital phase current signal, said third phase-shifting filter having the first transfer function;

a fourth phase-shifting filter which produces a second phase-shifted digital current signal from the digital phase current signal, said fourth phase-shifting filter having the second transfer function;

a first multiplier, responsive to said first phase-shifting filter and to said fourth phase-shifting filter, which computes a first intermediate power product signal from the first phase-shifted digital phase-to-neutral voltage signal and the second phase-shifted digital phase current signal;

a second multiplier, responsive to said second phase-shifting filter and said third phase-shifting filter, which computes a second intermediate power product signal from the second phase-shifted digital phase-to-neutral voltage signal and the first phase-shifted digital phase current signal; and

means, responsive to said first multiplier and to said second multiplier, for computing the quadergy for a defined phase of the power line for the interval of orthogonality from the first intermediate power product signal and the second intermediate power product signal.

89. A vector electricity meter according to Claim 88:

wherein said first phase-shifting filter comprises a first phase-shifting filter having a first phase-versus-frequency transfer function;

wherein said second phase-shifting filter comprises a second phase-shifting filter having a second phase-versus frequency transfer function the second phase-versus-frequency transfer function differing from the first phase-versus-frequency transfer function by approximately 90 degrees over a frequency range from a first frequency less than a lowest frequency component of a fundamental frequency reference signal to a second frequency

greater than a twenty-third harmonic of a highest frequency component of the fundamental frequency reference signal;

wherein said third phase-shifting filter comprises a third phase-shifting filter having the first phase-versus-frequency transfer function; and

wherein said fourth phase-shifting filter comprises a fourth phase-shifting filter having the second phase-versus frequency transfer function.

90. A vector electricity meter according to Claim 79 wherein vector computing means comprises means for computing energy for the power line for the interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

91. A vector electricity meter according to Claim 90 wherein said vector computing means further comprises means for computing active power for the power line for the interval of orthogonality from the computed energy for the power line for the interval of orthogonality.

92. A vector electricity meter according to Claim 79 wherein said vector computing means comprises means for computing distortion volt-ampere-hours for the power line for the interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

93. A vector electricity meter according to Claim 92 wherein said vector computing means further comprises means for computing distortion power for the power line for the interval of orthogonality from the distortion volt-ampere-hours for the power line for the interval of orthogonality.

94. A vector electricity meter according to Claim 79 wherein said vector computing means comprises means for computing a power factor for the power line for the interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

95. A vector electricity meter according to Claim 79 wherein said vector computing means comprises means for computing a distortion power factor for the power

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line for the interval of orthogonality from the digital phase-to-neutral voltage signal and digital phase current signal.

96. A vector electricity meter according to Claim 79 wherein said means for determining an interval of orthogonality comprises:

5 means for producing a fundamental frequency reference signal from the phase-to-neutral voltage signal; and

means, responsive to said producing means, for detecting passage of a predetermined number of cycles of the fundamental frequency reference signal to thereby determine an interval of orthogonality.

10 97. A vector electricity meter according to Claim 96 wherein said producing means comprises:

a narrow-band filter which filters each phase-to-neutral voltage signal to produce a corresponding fundamental frequency phase-to-neutral voltage signal; and

15 linear combining means, responsive to said narrow-band filter, for linearly combining the fundamental frequency phase-to-neutral voltage signals to produce a fundamental frequency reference signal.

98. A vector electricity meter according to Claim 97 wherein said linear combining means comprises:

20 first scaling means for scaling a first fundamental frequency phase-to-neutral voltage signal by one-half to produce a first scaled fundamental frequency voltage signal;

second scaling means for scaling a second fundamental frequency phase-to-neutral voltage signal by one-fourth to produce a second scaled fundamental frequency phase-to-neutral voltage signal;

25 third scaling means for scaling a third fundamental frequency phase-to-neutral voltage signal by one-eighth to produce a third scaled fundamental frequency phase-to-neutral voltage signal; and

an adder, responsive to said first scaling means, said second scaling means and said third scaling means, which sums the first, second and third scaled fundamental frequency phase-to-neutral voltage signals to produce a fundamental frequency reference signal.

99. A vector electricity meter according to Claim 79 further comprising:
circuit identifying means for identifying a circuit topology of the power line; and
wherein said means for computing a digital phase-to-neutral voltage signal and
digital phase current signal comprises means for computing a digital phase-to-neutral voltage
signal and digital phase current signal based upon the identified topology of the power line.

100. A vector electricity meter according to Claim 79 wherein said vector
computing means comprises means for computing a neutral current magnitude for the power
line from the computed digital phase current signal.

101. A vector electricity meter according to Claim 100 wherein said vector
computing means further comprises means for comparing the computed neutral current
magnitude to a predetermined threshold to thereby compute a neutral current status.

102. A vector electricity meter according to Claim 79 wherein said vector
computing means comprises means for computing an effective line voltage for a line of the
power line for the interval of orthogonality from the digital signal.

103. A vector electricity meter according to Claim 102 wherein said vector
computing means further comprises means for comparing the computed effective line voltage
to a predetermined expected nominal operating voltage to thereby compute a line voltage
status.

104. A vector electricity meter according to Claim 79 wherein said vector
computing means comprises means for computing an expected nominal operating voltage
from the digital signal.

105. A vector electricity meter according to Claim 102 wherein said vector
computing means further comprises means, responsive to said means for computing an
effective line voltage, for selecting from a plurality of predetermined nominal operating
voltages, an expected nominal operating voltage closest to the computed effective line
voltage.

106. A vector electricity meter according to Claim 102 wherein said vector computing means further comprises:

means for computing a phase angle for the line of the power line from the digital signal;

means, responsive to said means for computing an effective line voltage and to said means for computing a phase angle, for selecting from a plurality of predetermined nominal operating voltages, each predetermined nominal operating voltage having an associated nominal phase angle, an expected nominal operating voltage approximating the computed effective line voltage and having an associated nominal phase angle closest to the computed phase angle.

107. A vector electricity meter according to Claim 79 wherein said vector computing means comprises:

means for identifying a vector metering quantity to be metered; and

means, responsive to said identifying means, for computing the identified vector metering quantity.

108. A vector electricity meter according to Claim 79 wherein said vector computing means comprises:

means for identifying a vector metering quantity to be metered and an associated metering detent; and

means, responsive to said identifying means, for computing the identified vector metering quantity based upon the associated metering detent.

109. A vector electricity meter according to Claim 79 further comprising:

means for detecting absence of a voltage on a conducting path of the power line; and

wherein said means for computing a digital phase-to-neutral voltage signal and digital phase current signal comprises means for computing a digital phase-to-neutral voltage signal and digital phase current signal notwithstanding absence of a voltage on the conducting path of the power line.

110. A vector electricity meter for metering electricity on a power line having at least two conducting paths, the meter comprising:

a voltage sensor which senses a line voltage signal on the power line;
a current sensor which senses a line current signal on the power line;
means for determining from the sensed line voltage and line current signals, an interval of orthogonality for the sensed line voltage and line current signals on the power line;

sampling means, responsive to said voltage sensor and to said current sensor, for sampling the sensed line voltage and line current signals at a plurality of consecutive sampling times separated by a predetermined sampling interval to thereby obtain a plurality of line voltage samples and line current samples;

sample converting means, responsive to said sampling means, for converting each line voltage sample into a corresponding digital line voltage sample and each line current sample into a corresponding digital line current sample; and

vector computing means, responsive to said interval determining means and to said sample converting means, for computing a vector metering quantity for the power line for the determined interval of orthogonality from the digital line voltage samples and digital line current samples from the interval of orthogonality.

111. A vector electricity meter according to Claim 110 wherein vector computing means comprises means for computing from the digital line voltage samples and digital line current samples, a corresponding digital phase-to-neutral voltage sample and digital phase current sample to thereby define a phase of the power line.

112. A vector electricity meter according to Claim 111 wherein said vector computing means comprises means for computing vector apparent volt-ampere-hours for the power line for the interval of orthogonality from the digital line voltage samples and digital line current samples from the interval of orthogonality.

113. A vector electricity meter according to Claim 112 wherein said vector computing means further comprises means for dividing the computed apparent volt-ampere-hours for the power line for the interval of orthogonality by the number of sampling intervals for the interval of orthogonality to thereby compute vector apparent power for the power line for the interval of orthogonality.

114. A vector electricity meter according to Claim 79 wherein said vector computing means comprises means for computing distortion volt-ampere-hours for the power line for the interval of orthogonality from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality.

115. A vector electricity meter according to Claim 114 wherein said vector computing means further comprises means for dividing the computed distortion volt-ampere-hours for the power line for the interval of orthogonality by the number of sampling intervals for the interval of orthogonality to thereby compute distortion power for the power line for the interval of orthogonality.

116. A vector electricity meter according to Claim 79 wherein said vector computing means comprises means for computing a distortion power factor for the power line for the interval of orthogonality from the digital phase-to-neutral voltage samples and digital phase current samples from the interval of orthogonality.

117. A vector electricity meter according to Claim 79 wherein said vector computing means comprises:

a narrow-band filter which computes from digital line voltage samples corresponding to a sensed line voltage signal, a series of digital fundamental frequency digital line voltage samples representing a fundamental frequency component of the sensed line voltage; and

means, responsive to said narrow-band filter, for computing a phase angle between the fundamental frequency component of the sensed line voltage and a fundamental frequency reference signal.

118. A vector electricity meter according to Claim 117 wherein said means for computing a phase angle between the fundamental frequency component of the sensed line voltage and a fundamental frequency reference signal comprises:

means for selecting a set of migratory decimated digital fundamental frequency line voltage samples from the series of digital fundamental frequency line voltage samples, each migratory decimated digital fundamental frequency line voltage sample corresponding to a sampling time delayed a predetermined migratory decimation interval from the sample time of the preceding migratory decimated fundamental frequency voltage sample with respect to

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the passage of a the fundamental frequency reference signal through a predetermined value so that the set of migratory decimated fundamental frequency digital line voltage samples approximately represent a period of the fundamental frequency component of the sensed line voltage;

- 5 Fourier analyzing means, responsive to said selecting means, for Fourier analyzing the set of migratory decimated digital fundamental frequency line voltage samples to thereby compute a phase angle between the fundamental frequency component of the sensed line voltage and the fundamental frequency reference signal.

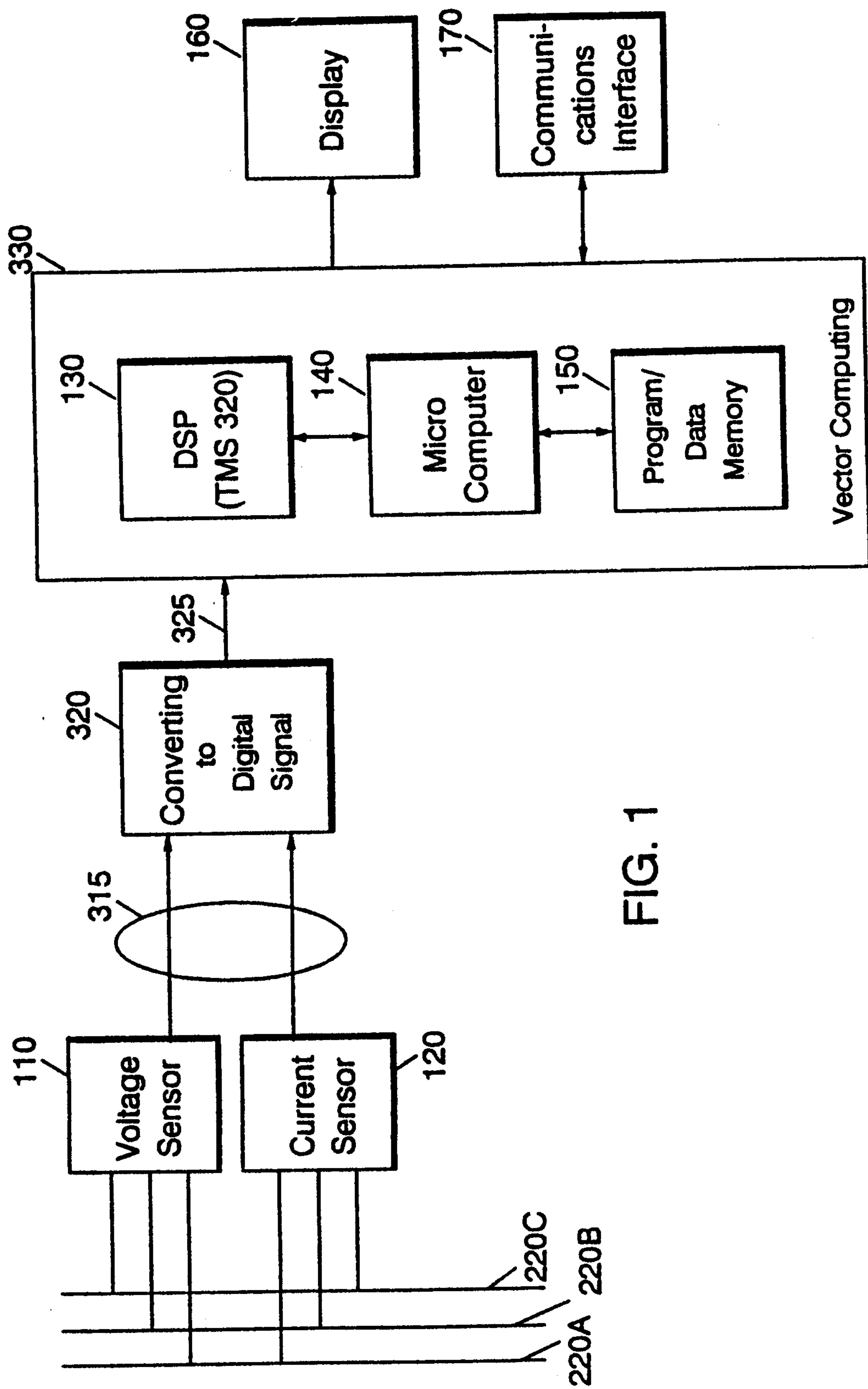


FIG. 1

FIG. 2

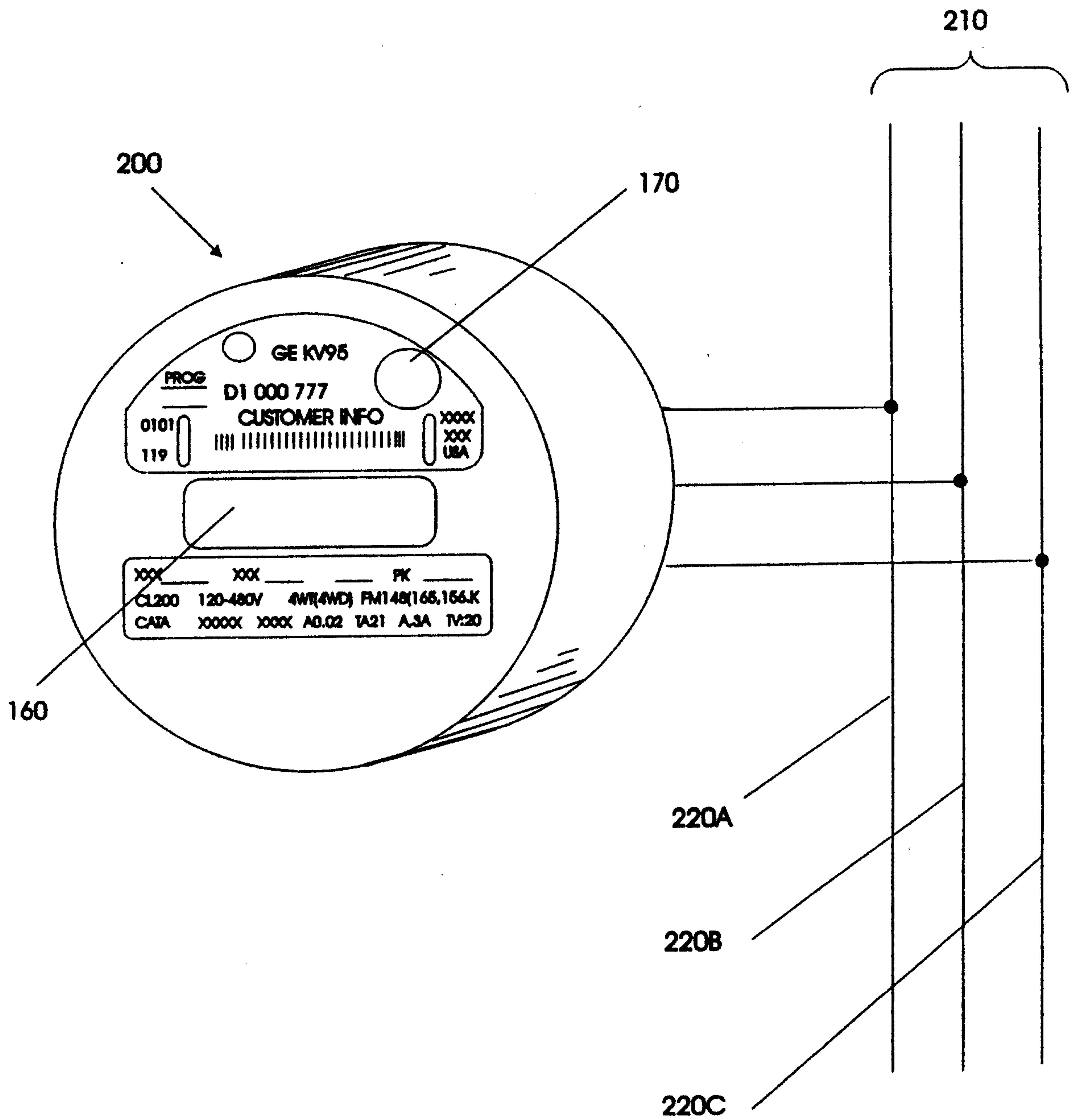


FIG. 3A

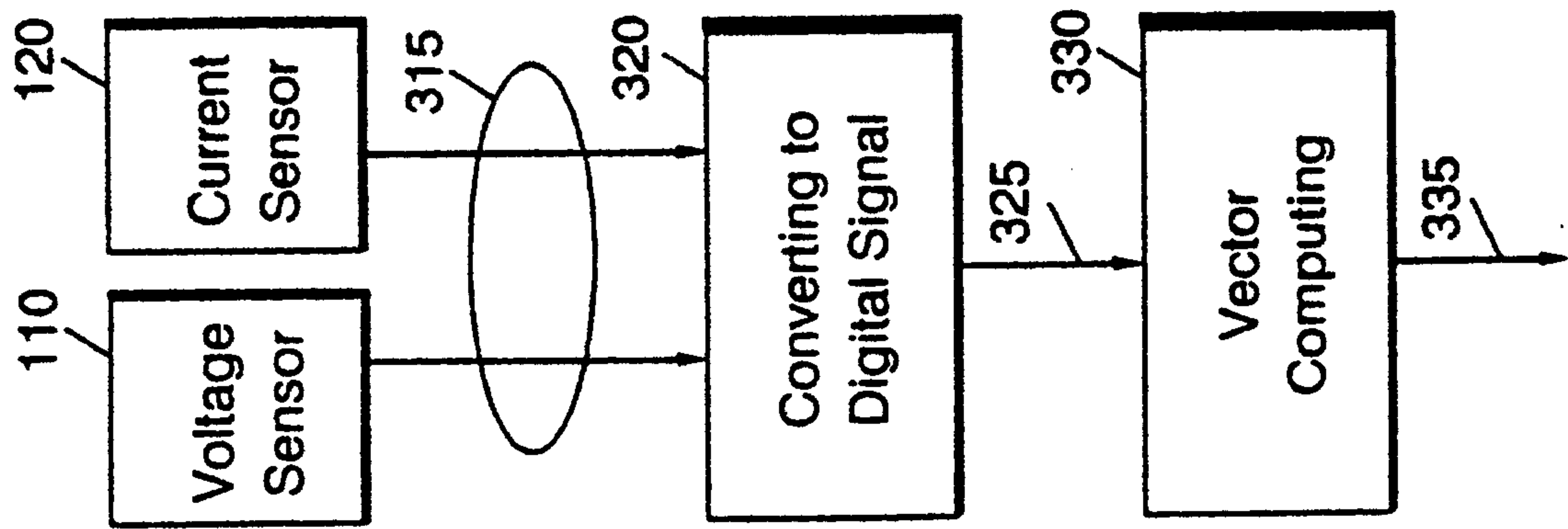


FIG. 3B

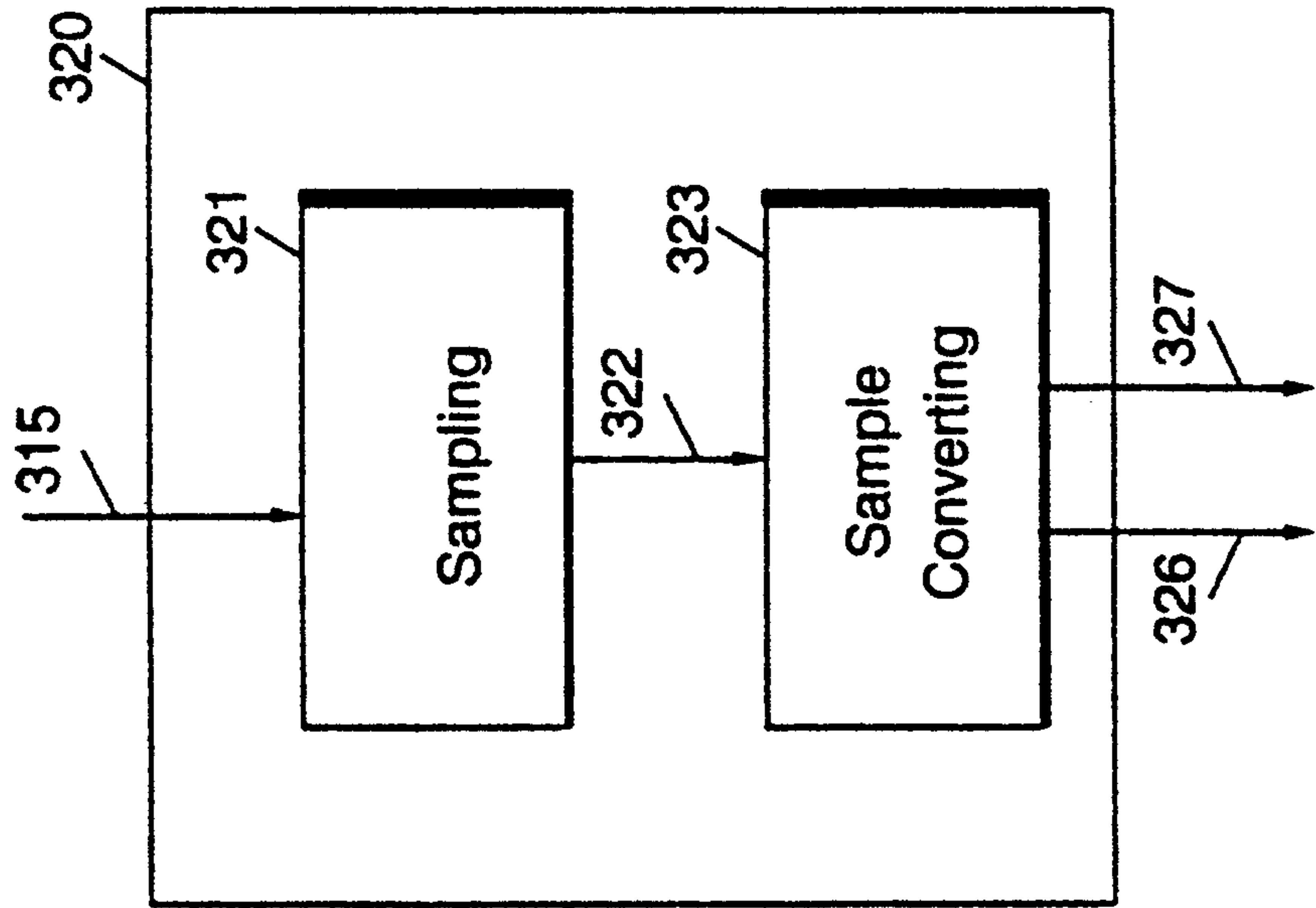


FIG. 4

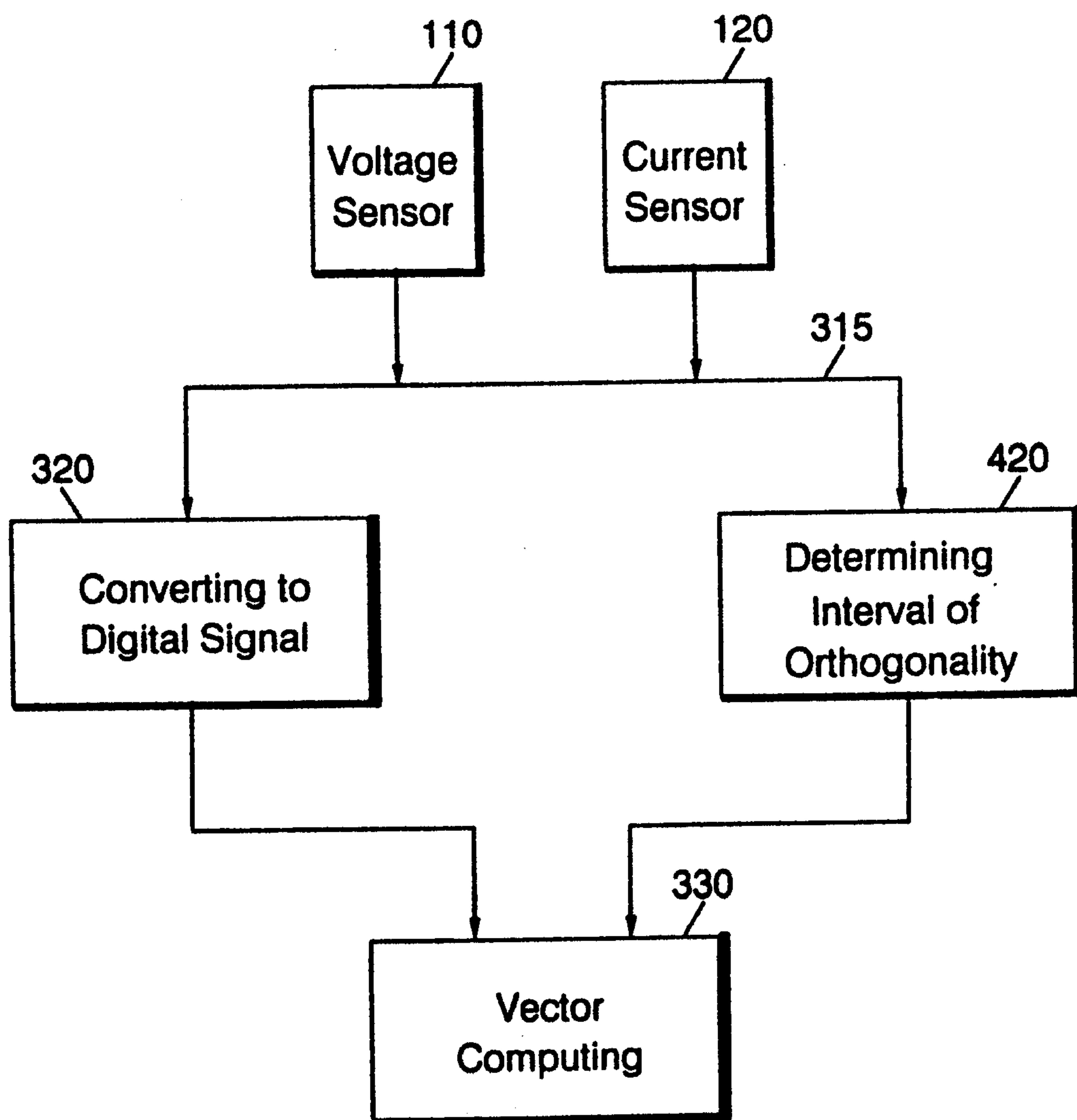


FIG. 5

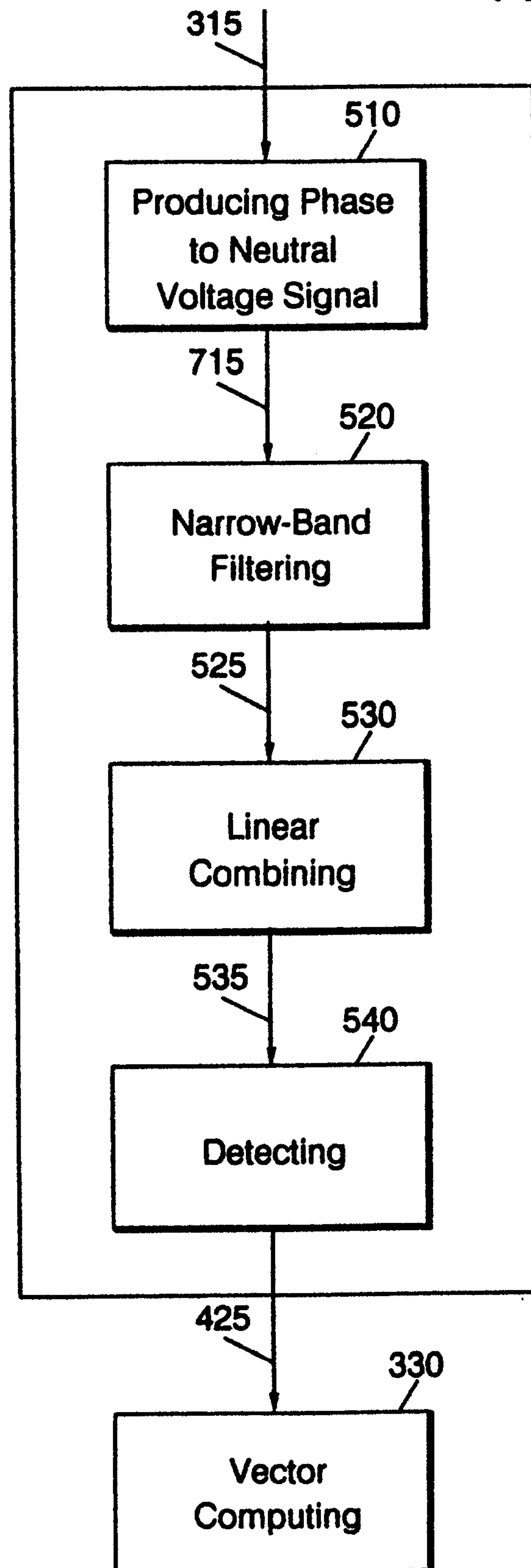


FIG. 6

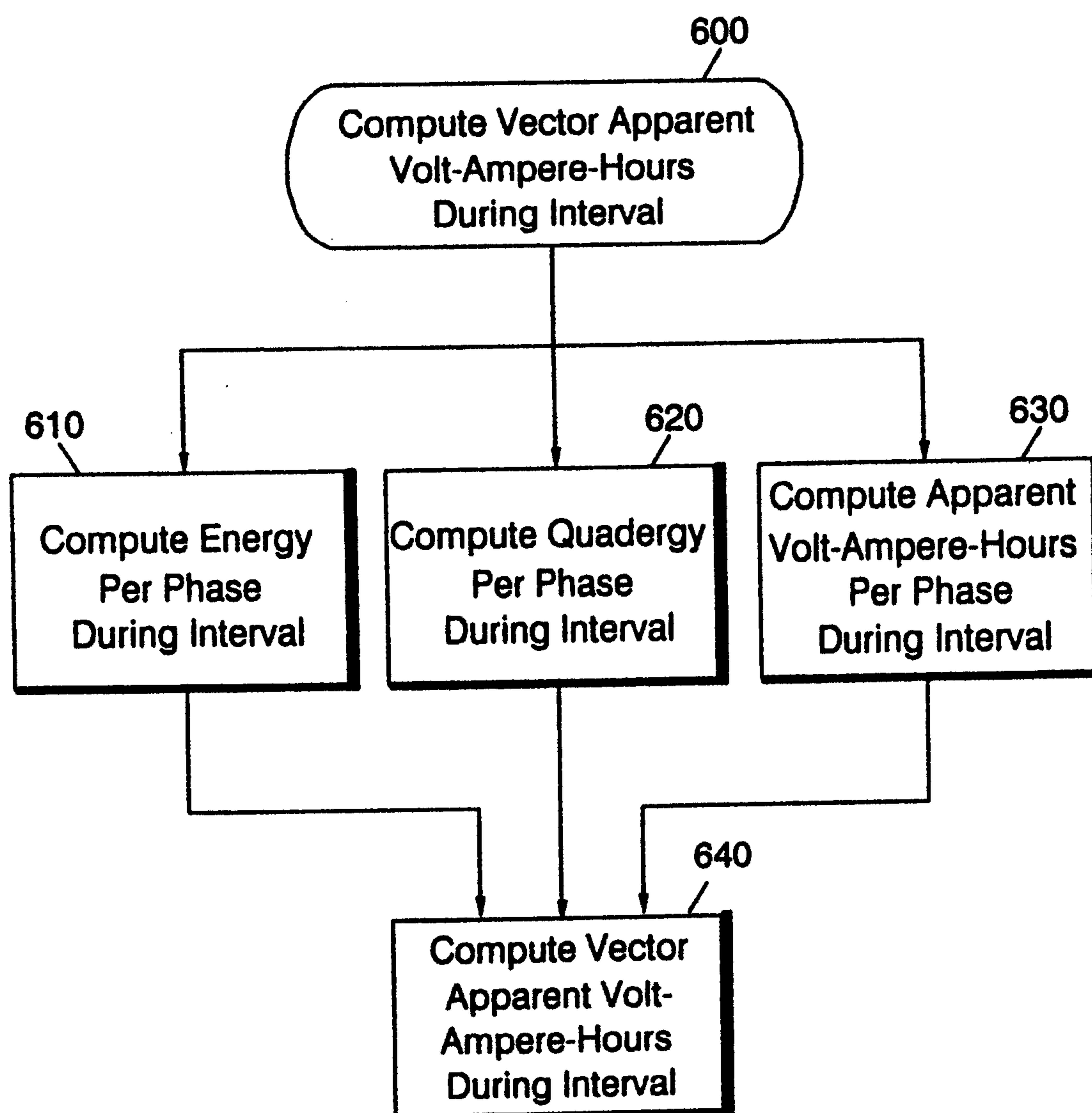


FIG. 8

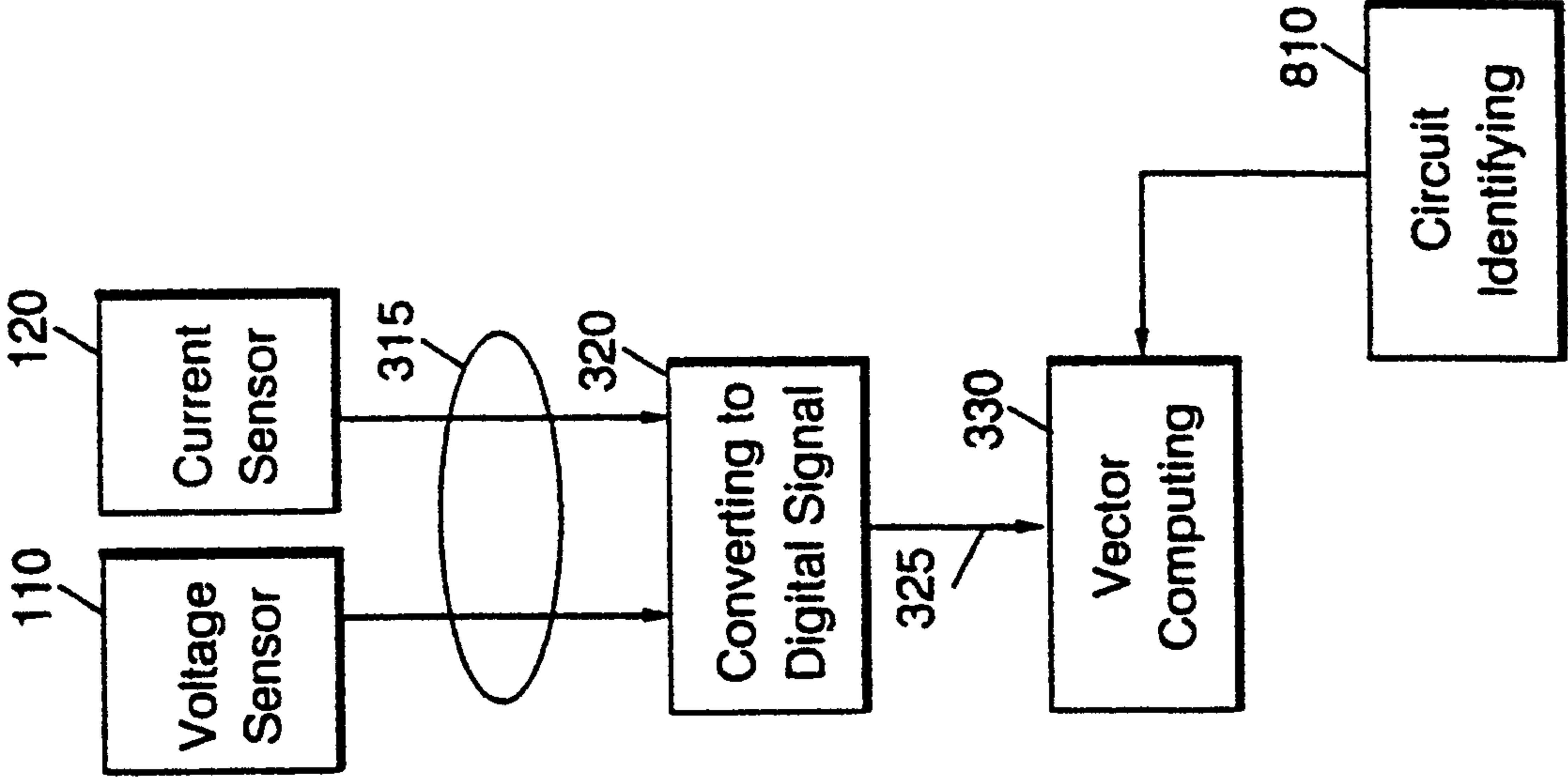
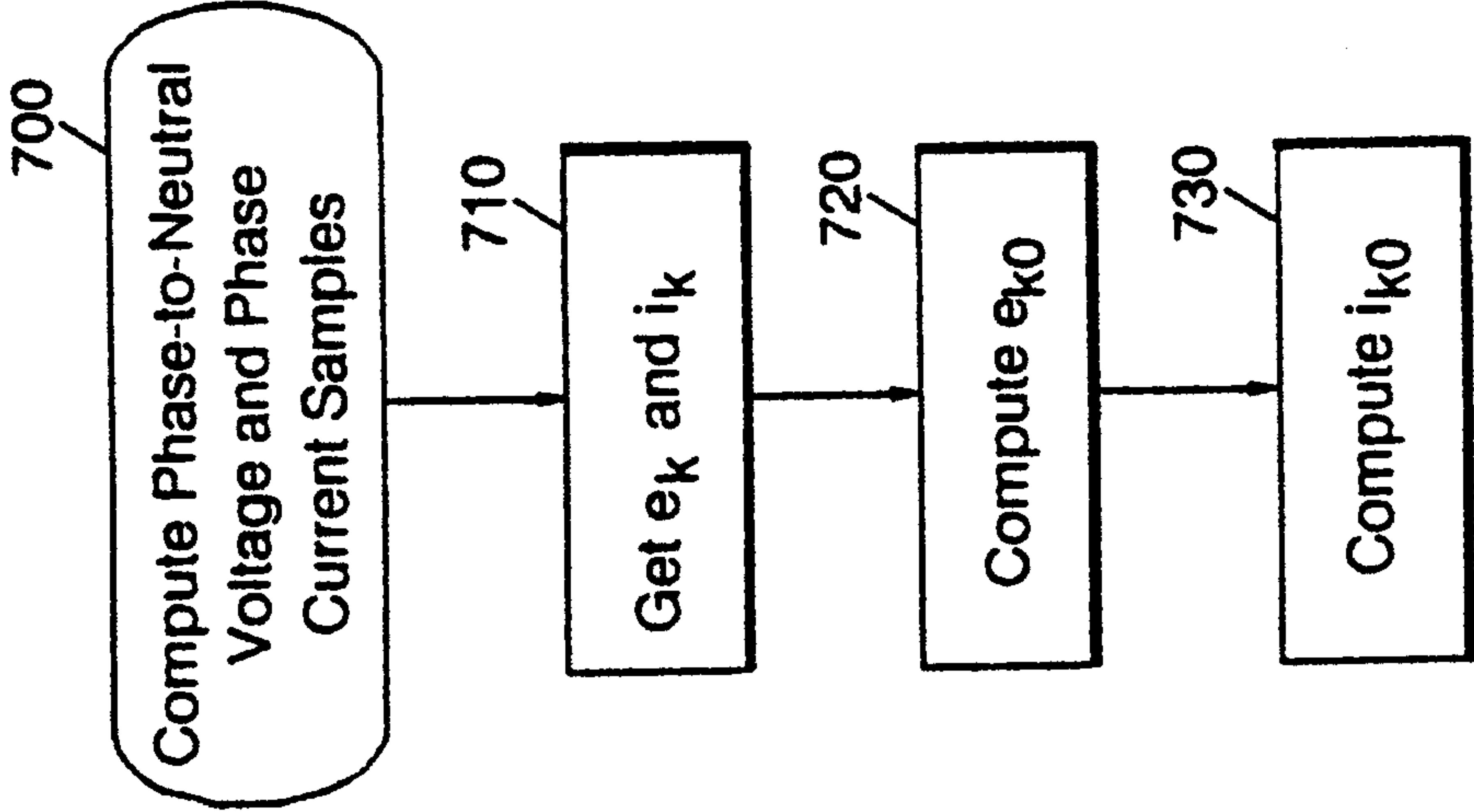


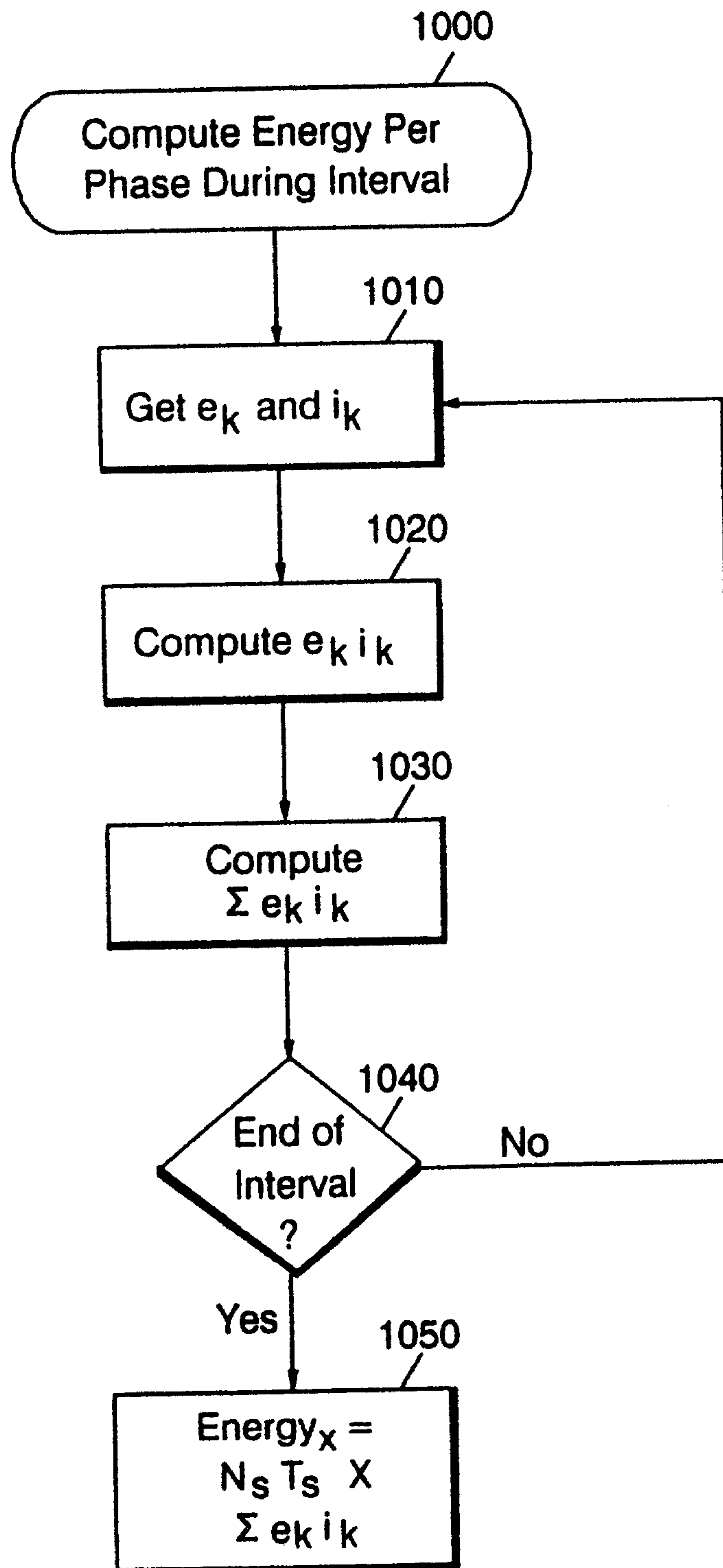
FIG. 7



Circuit Topology	Signals Sensed	Processing
2-element, 3-wire meter 3-phase, 3-wire delta service	$V_a' I_a' V_c' I_c$	$V_{b0} \leftarrow 1/3(V_a + V_c)$ $V_{a0} \leftarrow V_{b0} + V_a$ $V_{c0} \leftarrow V_{b0} + V_c$ $I_{a0} \leftarrow I_a$ $I_{b0} \leftarrow -(I_a + I_c)$ $I_{c0} \leftarrow I_c$
1-element, 3-wire meter 1-phase, 3-wire service	$V_a' I_a' I_c$	$V_{a0} \leftarrow V_a / 2$ $V_{b0} \leftarrow 0$ $V_{c0} \leftarrow V_{a0}$ $I_{a0} \leftarrow I_a$ $I_{b0} \leftarrow 0$ $I_{c0} \leftarrow I_c$
2-1/2 element, 4-wire meter 3-phase, 4-wire wye service	$V_a' I_a' I_b' V_c' I_c$ (V_b absent)	$V_{a0} \leftarrow V_a$ $V_{b0} \leftarrow -(V_a + V_c)$ $V_{c0} \leftarrow V_c$ $I_{a0} \leftarrow I_a$ $I_{b0} \leftarrow I_b$ $I_{c0} \leftarrow I_c$
2-element, 3-wire meter 3-phase, 4-wire delta service 1-phase, 3-wire service 3-phase, 4-wire wye service	$V_a' I_a' V_b' V_c' I_c$	$V_{a0} \leftarrow V_a - V_b$ $V_{b0} \leftarrow 0$ $V_{c0} \leftarrow V_c$ $I_{a0} \leftarrow I_a$ $I_{b0} \leftarrow 0$ $I_{c0} \leftarrow I_c$
2-element, 3-wire meter 1-phase, 3-wire service, network service	$V_a' I_a' V_c' I_c$	$V_{a0} \leftarrow V_a$ $V_{b0} \leftarrow 0$ $V_{c0} \leftarrow V_c$ $I_{a0} \leftarrow I_a$ $I_{b0} \leftarrow 0$ $I_{c0} \leftarrow I_c$
1-element, 2-wire meter 1-phase, 2-wire service	$V_a' I_a$	$V_{a0} \leftarrow V_a$ $V_{b0} \leftarrow 0$ $V_{c0} \leftarrow 0$ $I_{a0} \leftarrow I_a$ $I_{b0} \leftarrow 0$ $I_{c0} \leftarrow 0$

FIG.
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FIG. 10



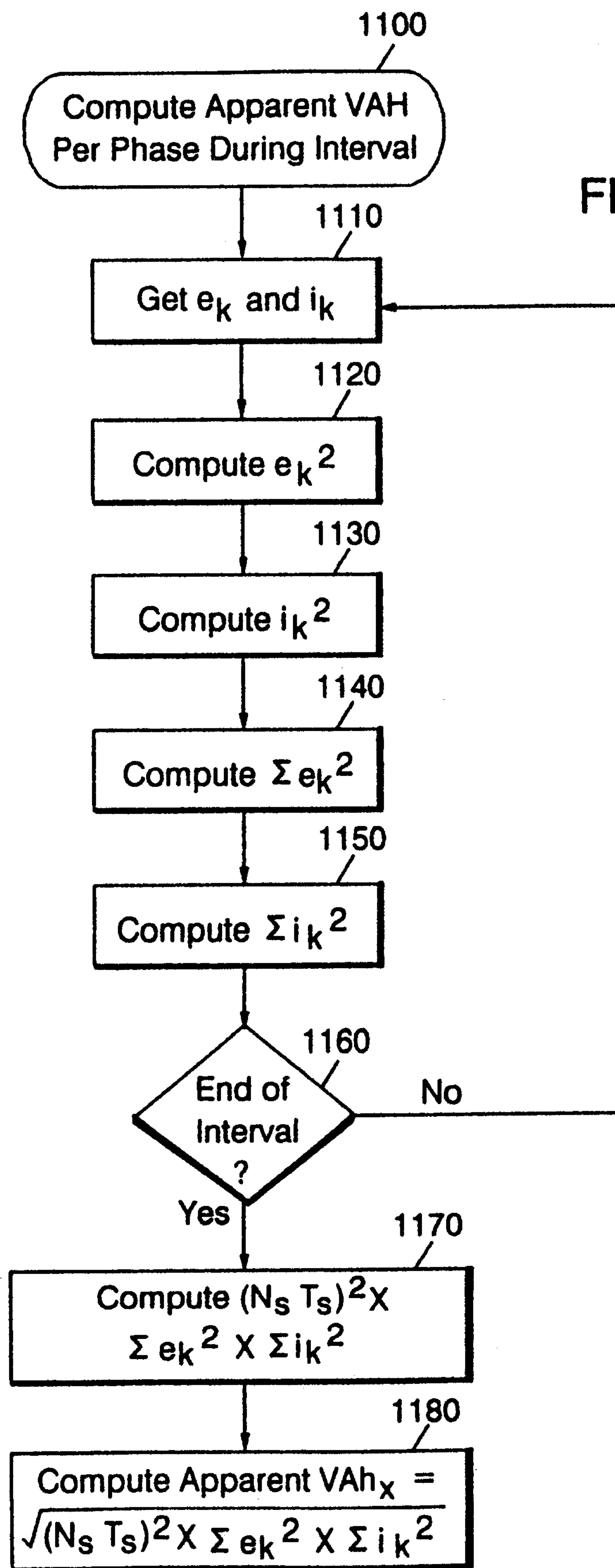


FIG. 11

FIG. 12A

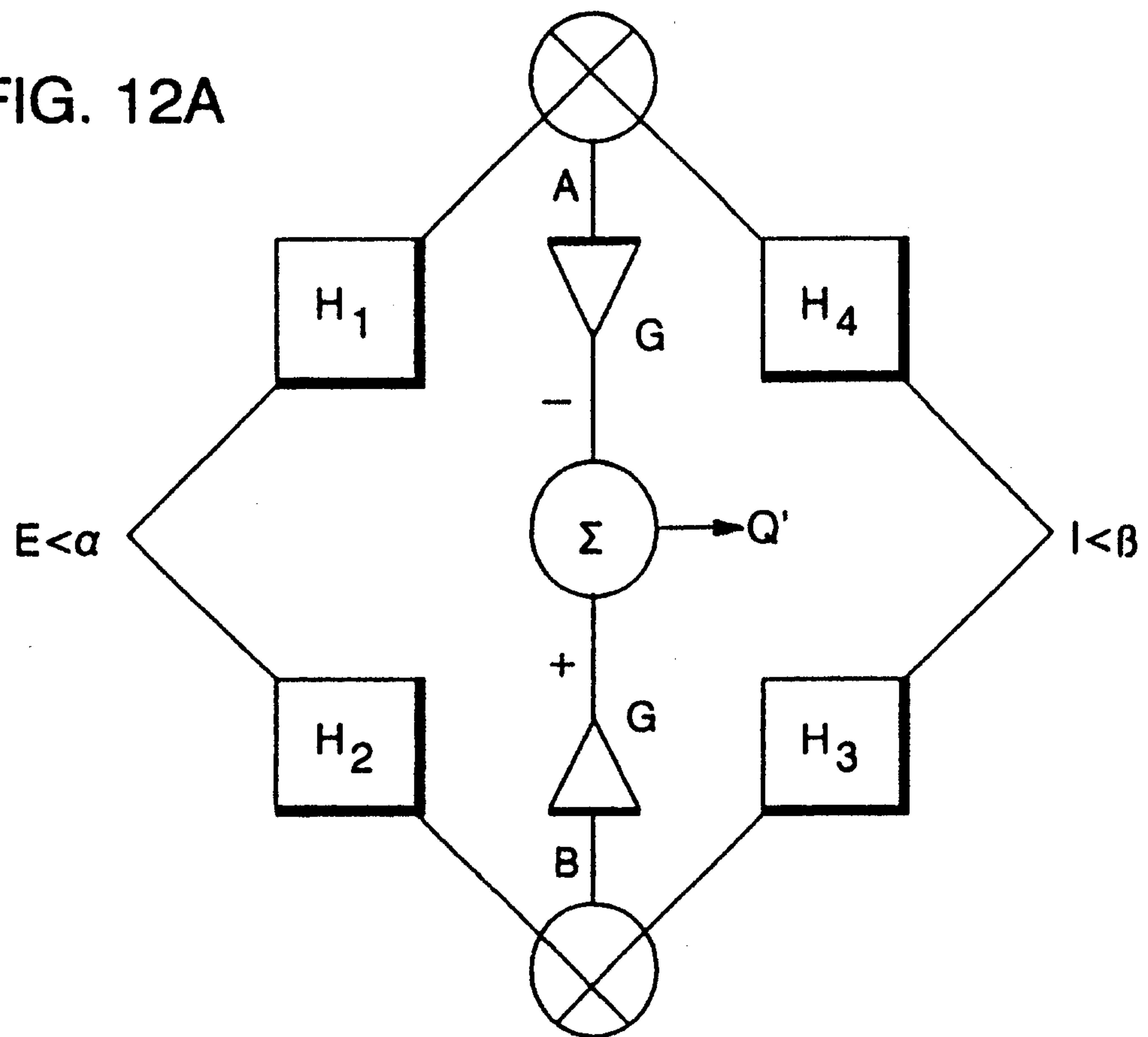
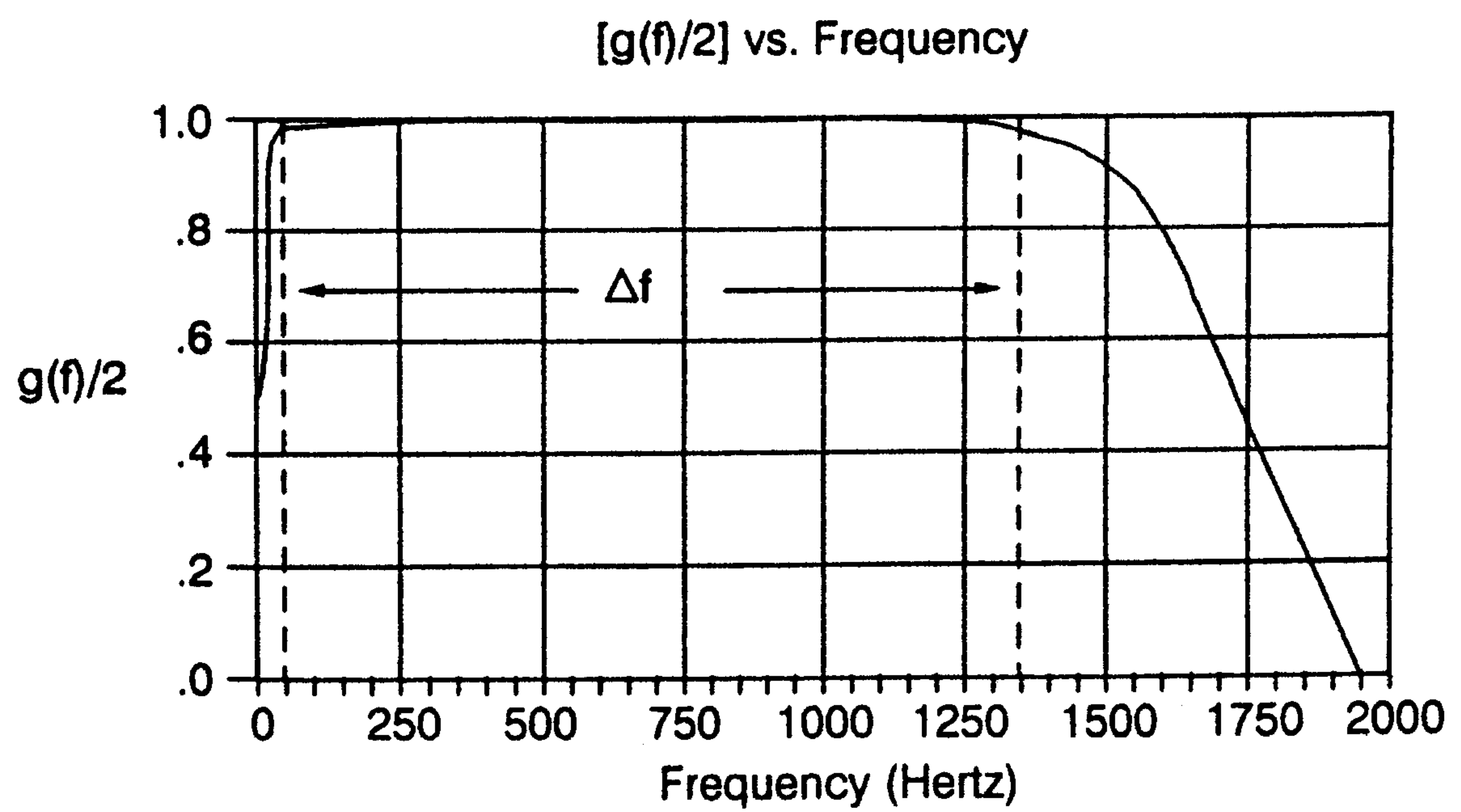
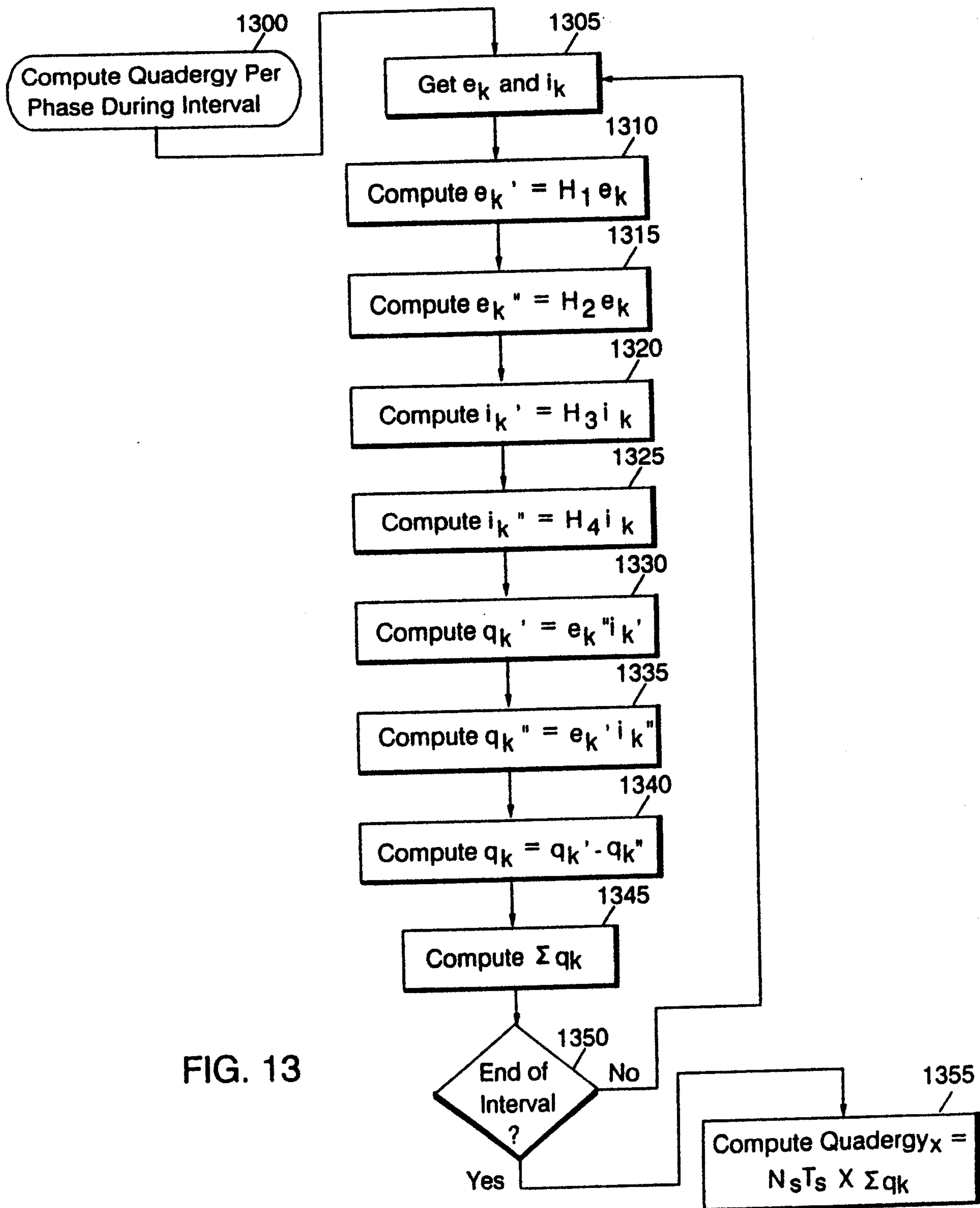


FIG. 12B





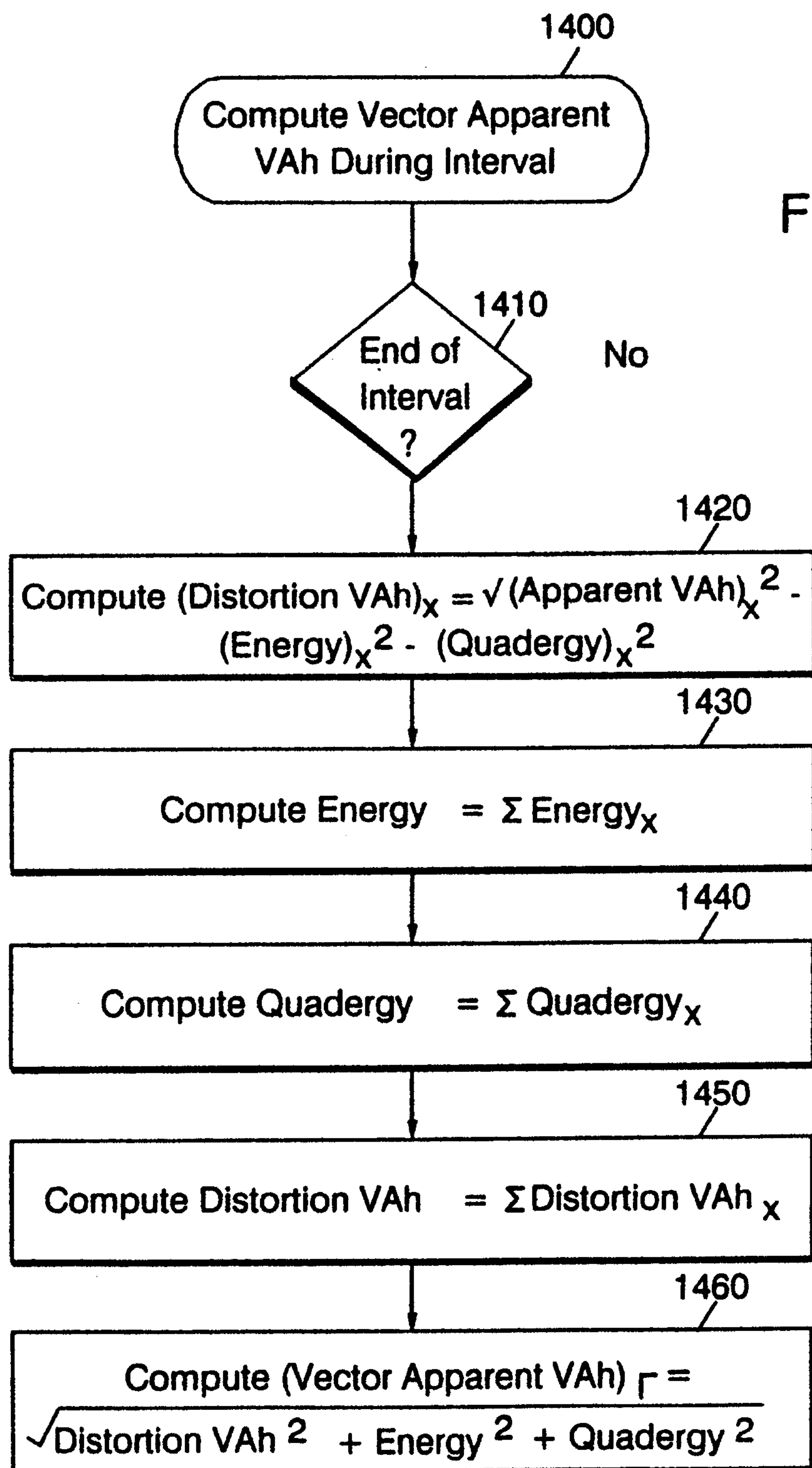


FIG. 15A

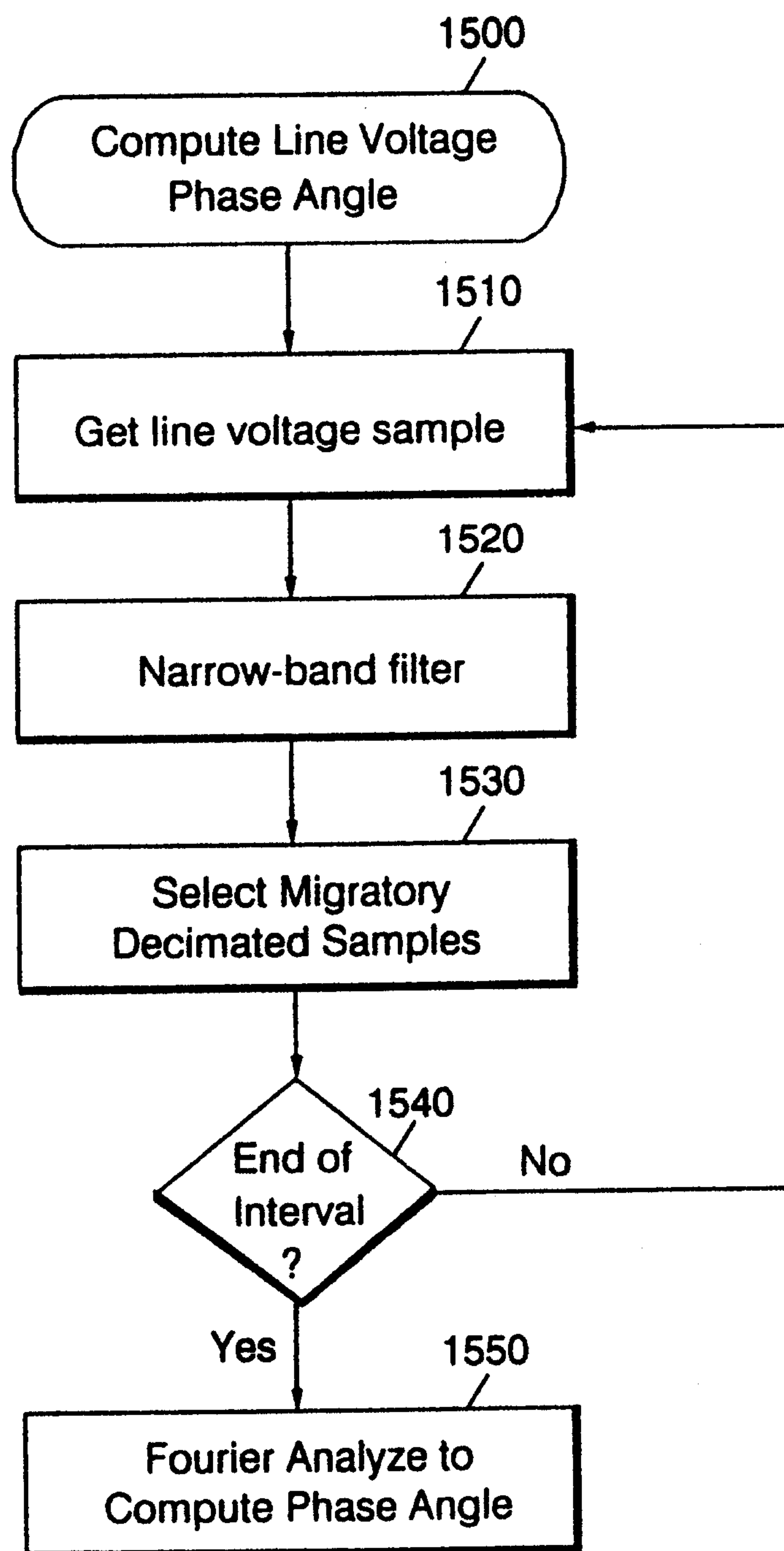


FIG. 15B

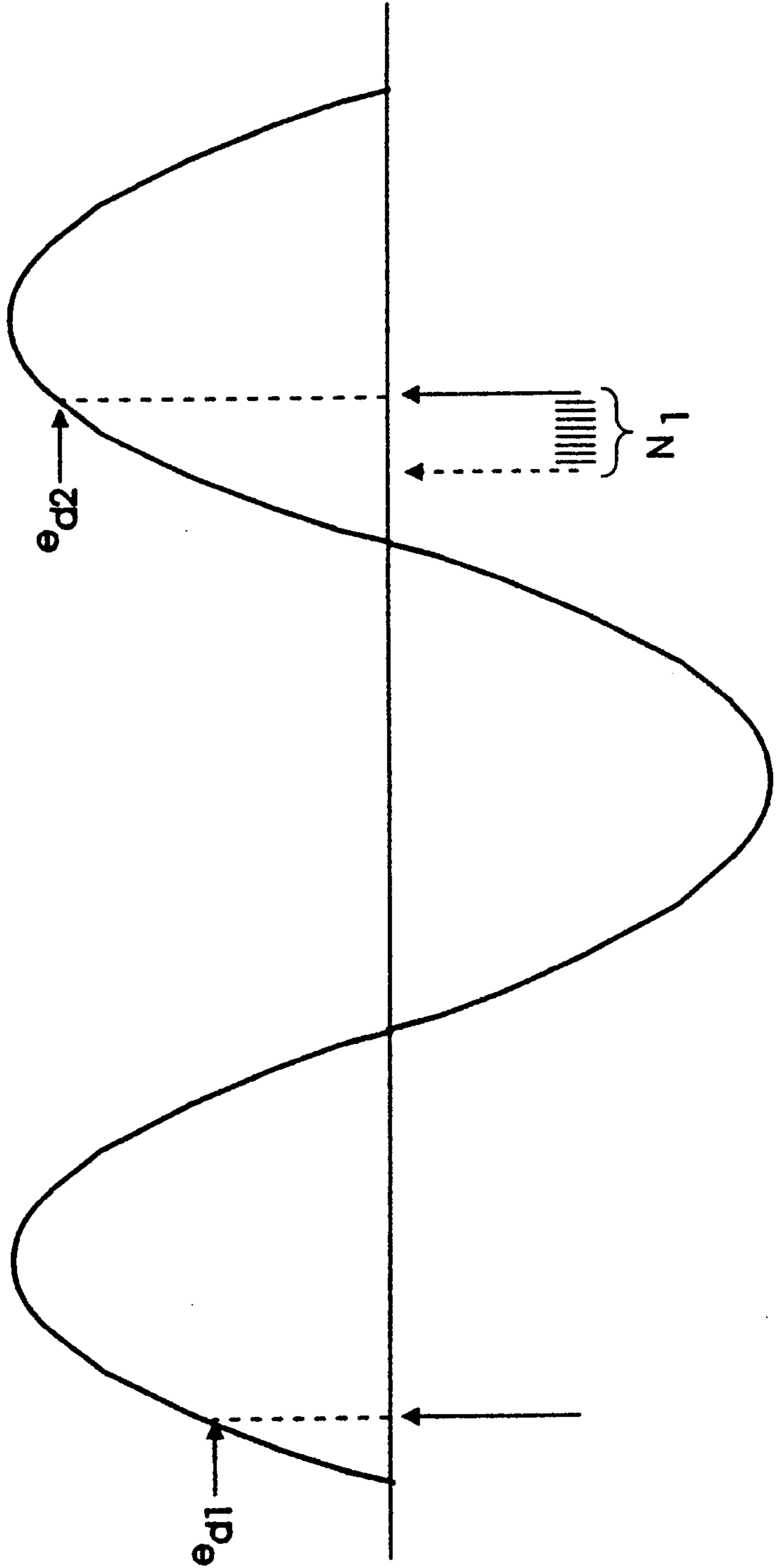


FIG. 16A

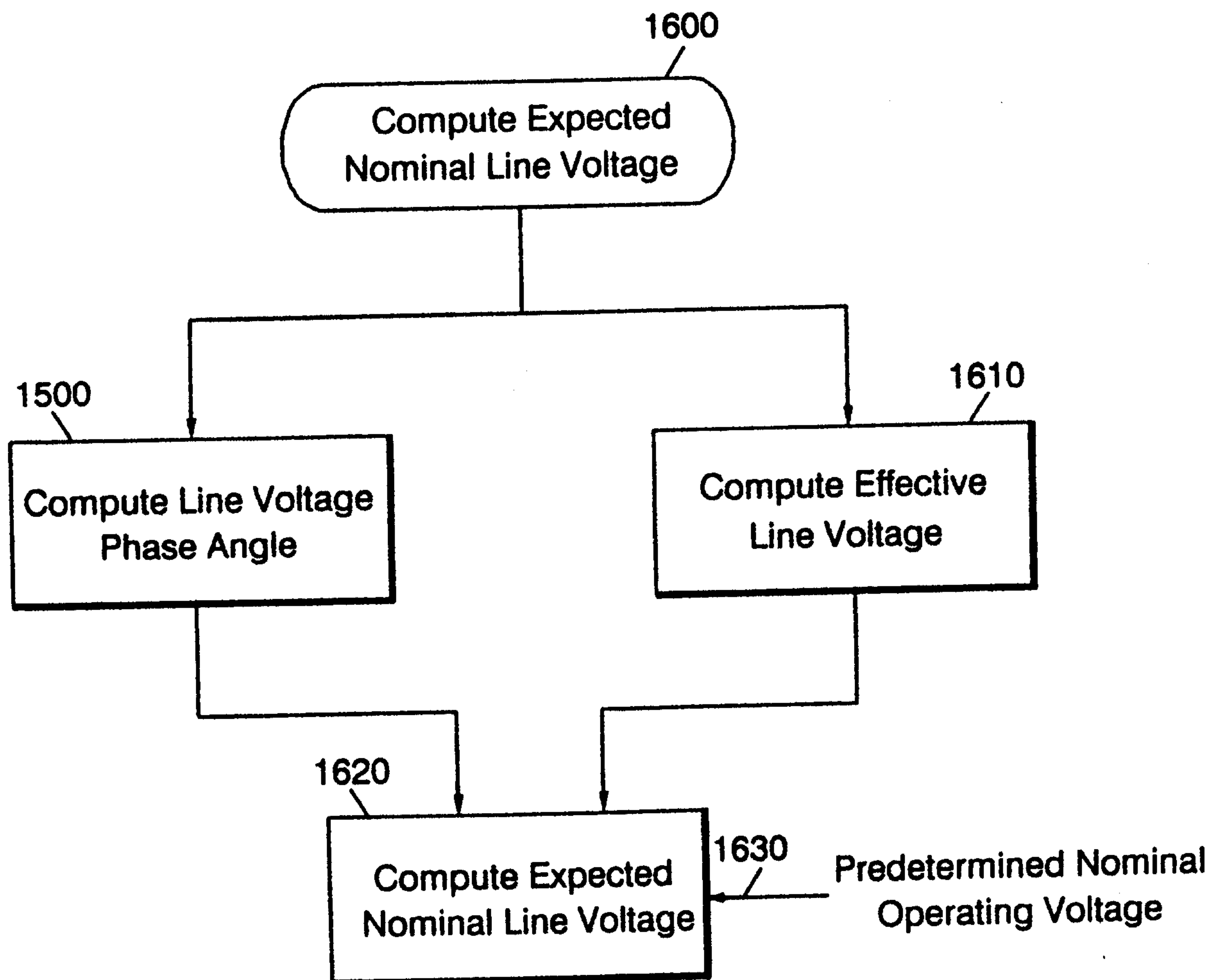


FIG. 16B

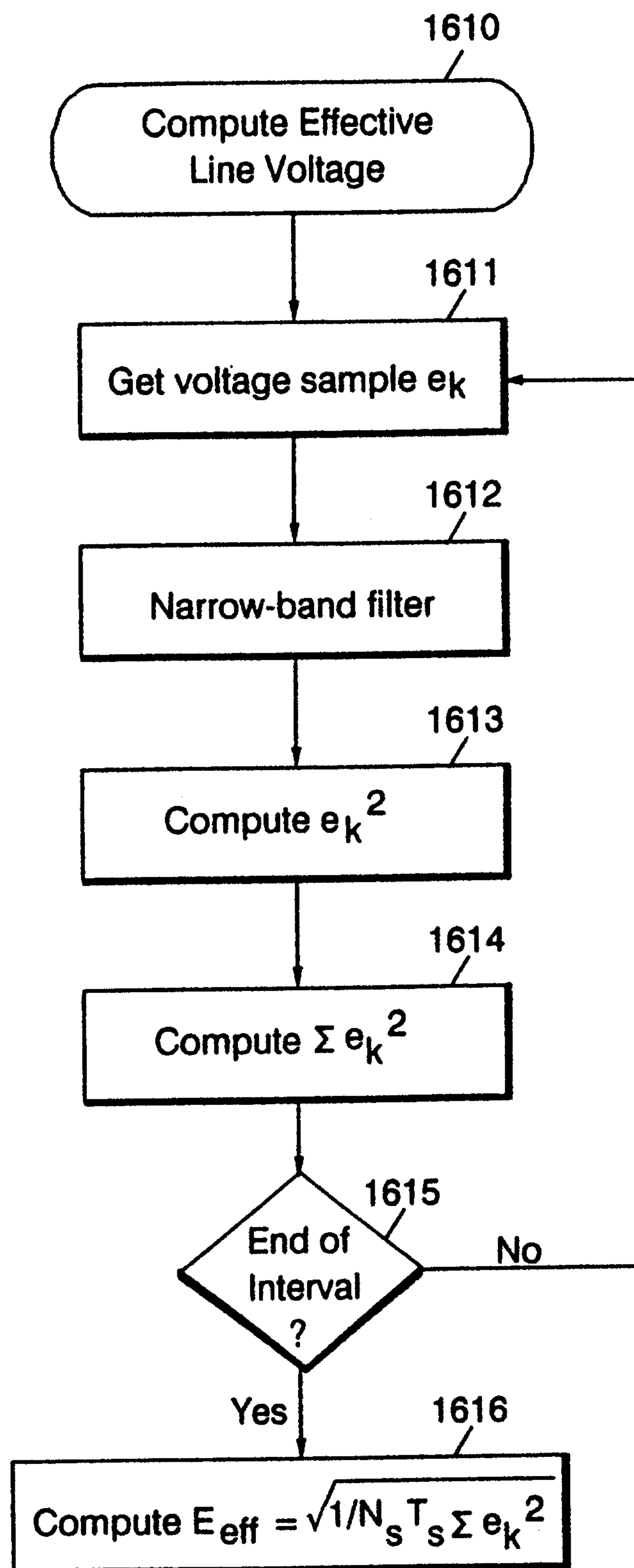


FIG. 17

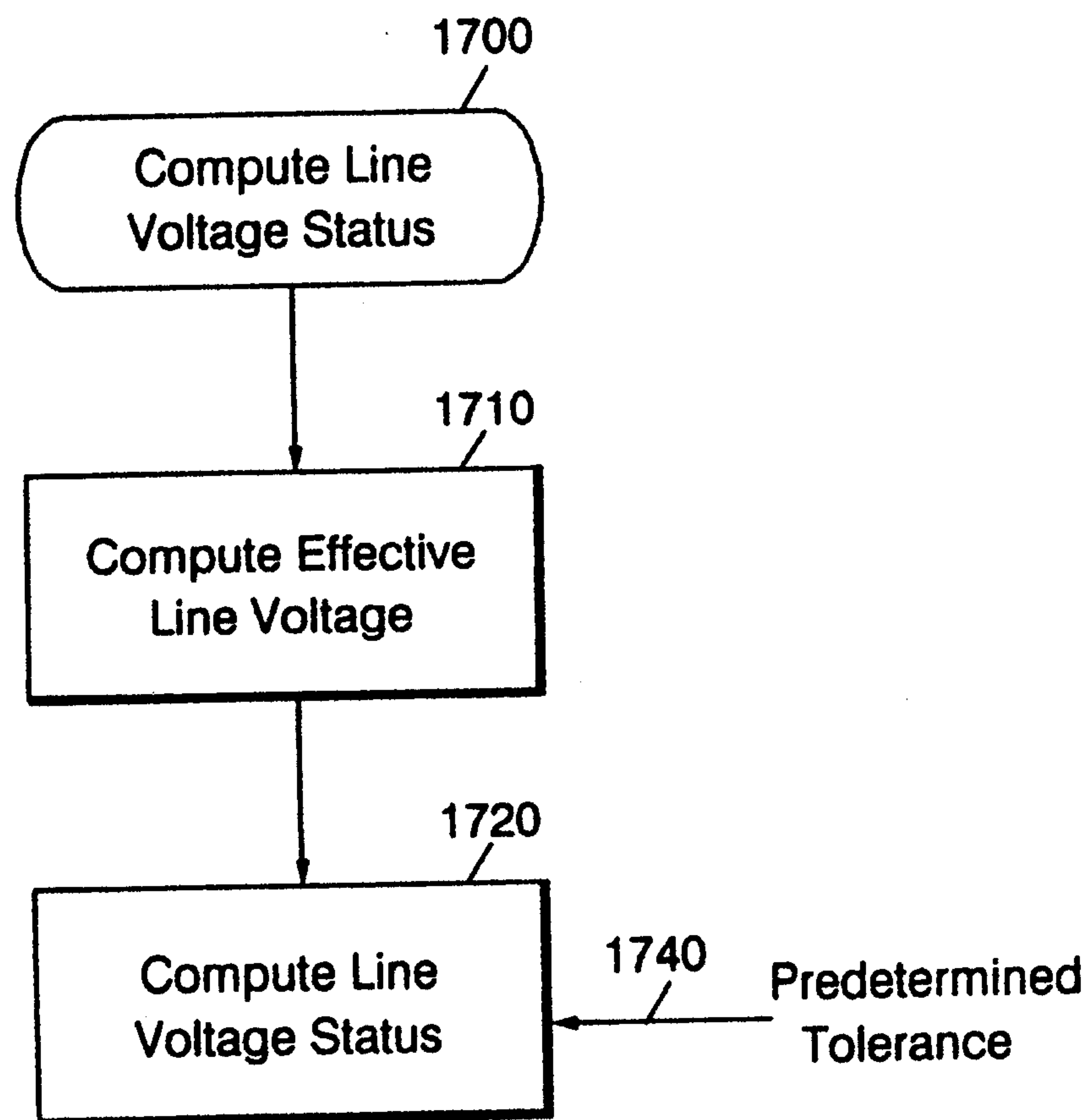


FIG. 18

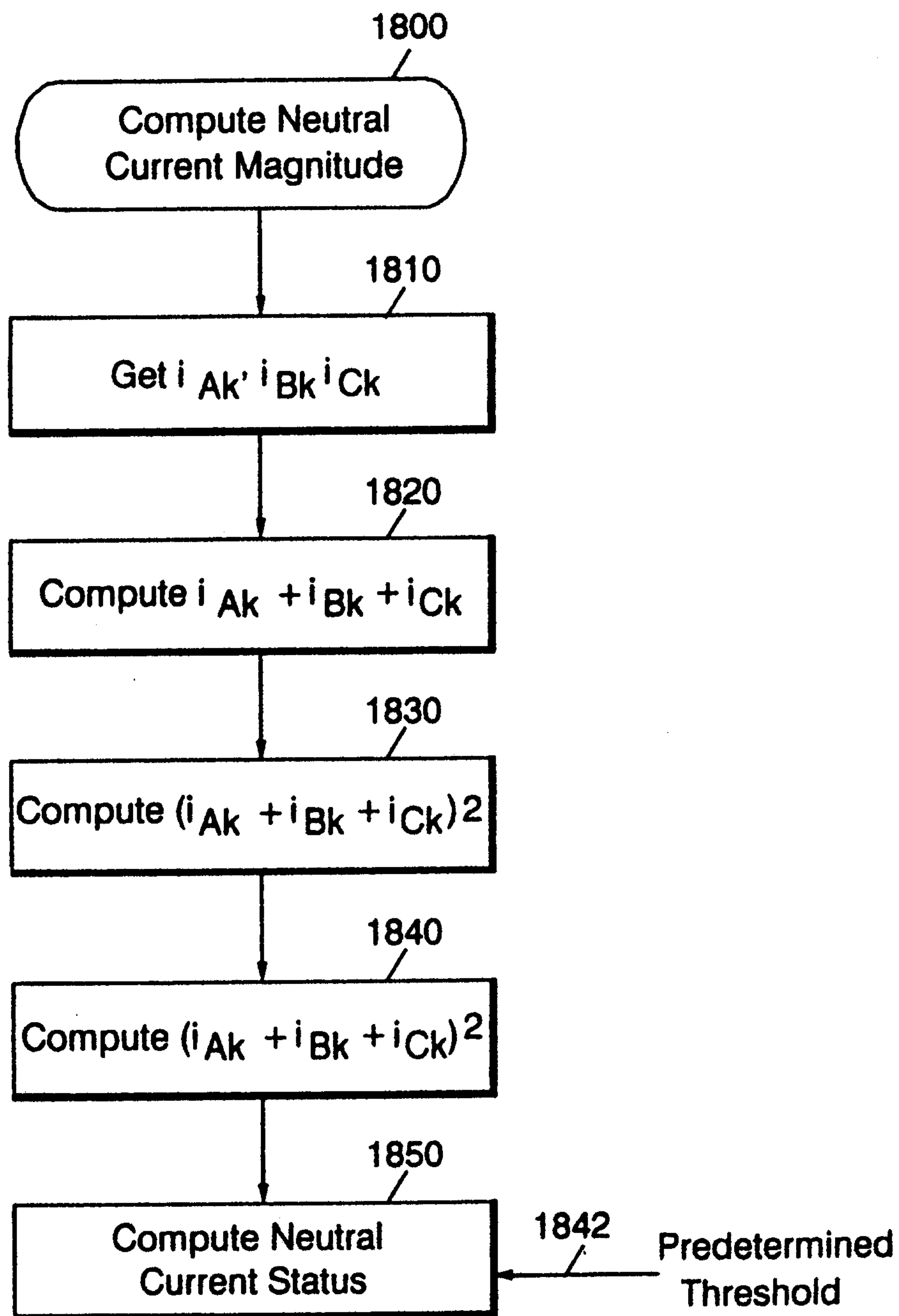


FIG. 19

