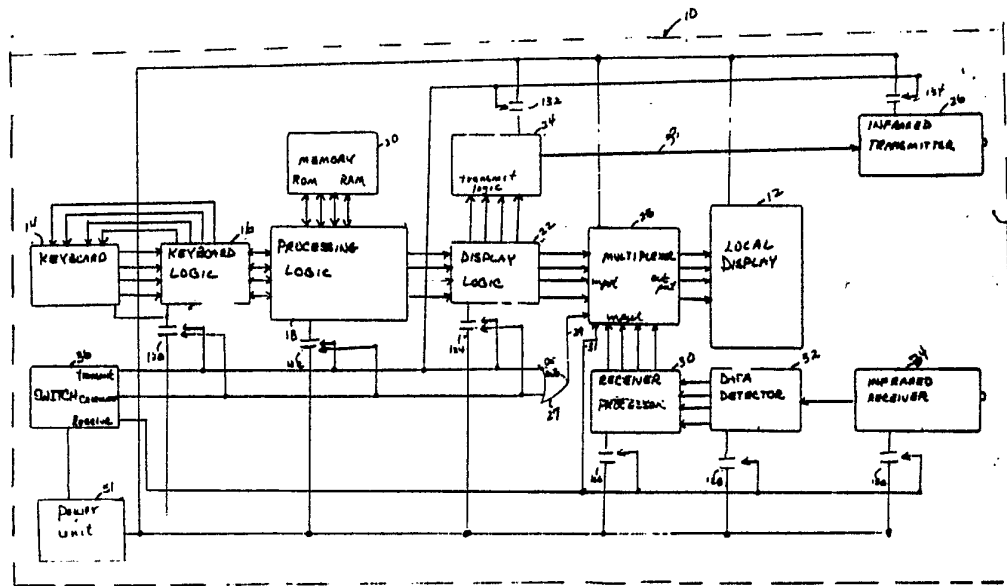




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

<p>(51) International Patent Classification ⁴ : H04L 11/02, G06F 13/00 G09B 5/00</p>	A1	<p>(11) International Publication Number: WO 87/ 05455</p> <p>(43) International Publication Date: 11 September 1987 (11.09.87)</p>
<p>(21) International Application Number: PCT/US87/00468</p> <p>(22) International Filing Date: 27 February 1987 (27.02.87)</p> <p>(31) Priority Application Number: 836,843</p> <p>(32) Priority Date: 6 March 1986 (06.03.86)</p> <p>(33) Priority Country: US</p> <p>(71) Applicant: PROJECT BENJAMIN, LTD. [US/US]; 180-2B Park Avenue, North, Winter Park, FL 32789 (US).</p> <p>(72) Inventor: SCHWABER, Jeffrey, S. ; 180-2B Park Avenue, North, Winter Park, FL 32789 (US).</p> <p>(74) Agent: BEUSSE, James, H.; Duckworth, Allen & Dyer, P.O. Box 3791, Orlando, FL 32802 (US).</p>		<p>(81) Designated States: AT (European patent), AU, BE (European patent), CH (European patent), DE (European patent), FR (European patent), GB (European patent), IT (European patent), JP, KR, LU (European patent), NL (European patent), SE (European patent), SU.</p> <p>Published With international search report.</p>

(54) Title: REMOTE DISPLAY DEVICE FOR A MICROCOMPUTER



(57) Abstract

An optically linked calculator system (Fig. 1) for displaying data (12) generated by a local calculator (10) at both the local calculator and at a detached display (60). The calculator system includes a plurality of transmitting and receiving calculator units (10, 40), and a remote display receiving unit (60). The calculators can transmit and receive entered data and calculated results. The remote receiving unit receives and displays transmitted data entries and calculated results. A calculator includes a keyboard (14), keyboard logic (16), processing logic (18), display (12), display logic (22), transmit logic (24), infrared transmitter (26), switching means (27, 36, 120-130), infrared receiver logic (34), and converting logic. The switching means selectively enables components of the calculator for effecting a transmit mode, receive mode, and calculate mode. The remote receiver display unit (60) includes an infrared receiver (64), data detection logic (66), processing logic (68), and a display (62).

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AT Austria	FR France	ML Mali
AU Australia	GA Gabon	MR Mauritania
BB Barbados	GB United Kingdom	MW Malawi
BE Belgium	HU Hungary	NL Netherlands
BG Bulgaria	IT Italy	NO Norway
BJ Benin	JP Japan	RO Romania
BR Brazil	KP Democratic People's Republic of Korea	SD Sudan
CF Central African Republic	KR Republic of Korea	SE Sweden
CG Congo	LI Liechtenstein	SN Senegal
CH Switzerland	LK Sri Lanka	SU Soviet Union
CM Cameroon	LU Luxembourg	TD Chad
DE Germany, Federal Republic of	MC Monaco	TG Togo
DK Denmark	MG Madagascar	US United States of America
FI Finland		

- 1 -

REMOTE DISPLAY DEVICE FOR A MICROCOMPUTERBACKGROUND OF THE INVENTION

5 1. Field of the Invention

10 This invention generally relates to an optical link system for transferring data from a central processing site to a remote display site, and more particular, to a method and device for remotely displaying of data from a calculator.

2. Background Discussion

15 Only a few years after its introduction the electronic calculator had become a standard item. The functions of the calculator are simple, it accepts data from a keyboard, performs the required arithmetic, and shows the results on a display. The
20 calculator's programs are stored in read only memories and the entered data are stored in random access memory.

- 2 -

In seminars or conferences where important calculations are made with a calculator, it is often desirable to remotely locate one or more data displays for observing data entries or calculated results for persons attending the meeting. By virtue of a person's physical location or proximity to the calculator, a person may not readily see the display data entries or calculator results on the calculator. For the benefit of those who may have an interest in the processing, it is desirable to display the data while the data are manipulated by a user of the calculator.

For instance, in a classroom setting it is desirable for a teacher to enter problems and solutions on a calculator keyboard and display the results on remote display units readily seen by pupils. This prevents the necessity of students from gathering around the calculator and allows each student to follow the calculator operation by the teacher. Another example, is in meetings where important calculations are done in which persons at the meeting have an interest in the results. It is also desirable for a person with a second calculator to transfer different data entries or calculated results back to the first calculator and remote display for comparisons.

It is also desirable to eliminate transmission lines between the calculator and remote displays. This permits faster installation of the calculator and displays, and allows a calculator user to move freely without the worry of becoming entangled. It would also benefit the persons in the meeting if the remote display could be easily moved from location to

- 3 -

location without the need of connecting and reconnecting transmission and power lines.

5 It is therefore an object of the present invention to provide a remote display which automatically displays the data entries or calculated results from a calculator.

10 It is therefore an object of the present invention to provide a remote display which automatically displays the data entries or calculated results from a calculator.

It is another object of the present invention to provide a remote display receiving unit to receive the data from the calculator and display it.

15 It is a further object of the present invention to eliminate the necessity of transmission lines between a calculator and a remote display.

It is an object of this invention to provide an optical link system for transmitting data from a calculator to a remote display unit.

20 It is another object of the present invention to provide a portable, self-powered remote display receiving unit.

25 It is another object of the present invention to preserve power by selectively removing power from components of the calculator which are not needed.

30 It is a further object of the present invention to provide a calculator with a transmit mode, a calculate mode, and a receive mode, whereby the transmit mode transmits data over an optical link system while the calculator performs calculations, the receive mode receives data via the optical link system, and the calculate mode allows normal

- 4 -

calculations to be displayed without transmitting and receiving.

5 SUMMARY OF THE INVENTION

 This invention relates to an optical link system and method for communicating among a plurality of transmitting processing units and receiving units.
10 The receiving units include remote display receiving units and processing units. The processing units transmit data indicative of entered data and calculated results and receive data from transmitting processing units. The remote display receiving units
15 receive and display transmitted data entries and calculated results.

 A processing unit includes a keyboard, means for entering data from the keyboard into the processing unit, a display means, a converting means, a
20 transmission means, and a receiving means. The display means connects to the processing unit for displaying locally calculated results or data entries. The means for converting transforms data entries or calculated results into electrical signals which are
25 transferred to the optical transmission means. The optical transmission means converts the electrical signals into pulses of radiation representative of the electrical signals. The receiving means accepts radiation from other transmitting processing units.

30 Each processing unit includes a switch means for selecting a transmission mode, a reception mode, and a calculation mode. In the transmission mode the processing means when performing calculations,

- 5 -

transmits entered data and calculated results to other receiving units. In the reception mode, the processing means receives data from the optical link for displaying on its display data calculated or
5 entered from a transmitting processing unit. In the calculation mode, the processing unit functions as a normal calculator without transmitting or receiving data.

10 A remote display device includes a receiving means for receiving the pulses of radiation and converts the electrical signals thereof to a displayed value which represents the data displayed on the transmitting processing unit.

15 DETAILED DESCRIPTION OF THE DRAWINGS

The foreground aspects and other features of the invention are explained in the following description, taken in connection with the accompanying drawings,
20 wherein:

FIG. 1 shows a general system overview for the method of remotely displaying data for a calculator.

FIG. 2 is an electrical block diagram for a processing unit.

25 FIG. 3 is an electrical block diagram for a remote display receiving unit.

FIG. 4 is an electrical schematic drawing of a calculator.

30 FIG. 5 is an electrical schematic drawing for a data detector/transmit logic.

- 6 -

DETAILED DESCRIPTION OF AN ILLUSTRATIVE EMBODIMENT

1. General Description

5 With reference to the drawings in general, there is illustrated a method in one form of the invention of remotely displaying data from a calculator. In this method, a calculator transfers data via an optical transmission link system to a receiver
10 calculator or a remote display. While the present invention is described hereinafter with particular reference to a calculator, it is to be understood at the outset of the description which follows that the devices and methods in accordance with the present
15 invention may be used to transfer display information from general purpose processing units. These include but are not limited to various microprocessors and microcomputers well known in the art.

 FIG. 1 shows a general system overview for the
20 method of remotely displaying data for a calculator. The calculator 10 performs calculations on data entered into its keyboard 14 in a manner well known in the art, and displays the data on a local display 12. The calculator 10 operates in either a transmit mode,
25 a receive mode, or a calculate mode depending upon the position of a three way switch 15. In the transmit mode, calculator 10 performs normal calculations and the data in the local display 12 are transmitted to a plurality of remote display units, such as remote
30 display unit 60 and calculator 40. The data on local display 12 are transferred via an optical link to calculator 40 and remote display 60. In the receive mode, calculator 10 receives data via the optical link

- 7 -

from a transmitting calculator. In the calculate mode, calculator 10 performs normal calculations in a manner well known in the art. In the calculate mode, no data are transmitted.

5 For purposes of illustration, calculator 10 transmits, and calculator 40 receives. It is important to note that calculator 10 also receives if switch 15 is in the receive mode. Calculator 40 transmits if switch 17 is in the transmit mode. The
10 data transmitted by calculator 10 are received by calculator 40 and displayed on its local display 42. Calculator 40 performs in the same matter as calculator 10 and is able to effect transmission to calculator 10 of data displayed on its local display
15 42. Thus, calculator 40 also transmits data to calculator 10 for display on local display 12.

Calculator 10 also transmits via the optical link to a remote display unit 62 which includes a remote display 60. The remote display 60 receives the
20 transmission from calculator 10 and converts the transmission data into a form suitable for displaying on local display 62. The optical link includes a plurality of optical transmitters and optical receivers.

25 FIG. 2 shows a system block diagram for calculator 10. Calculator 10 includes an optical transmitter such as infrared transmitter 26 for transmitting data and an optical receiver such as infrared receiver 34 for receiving data. The infrared
30 transmitter 26 transmits in the infrared spectrum and includes at least a single infrared emitting device. This device emits radiation just above the visible light spectrum between 770 nanometers and 10^6

- 8 -

nanometers. Gallium Arsenide and Gallium Aluminum Arsenide are two currently available compounds that are used for producing infrared emissions. These types of devices are commonly used in remote television controls. Depending upon the input power these devices can transmit up to distances of fifty feet. To increase the distance the input power can be increased accordingly. The beam from the infrared transmitter is in the form of a cone with the infrared emitting device located at the apex of the cone. To broaden the beam width more than one infrared emitting device is used. Thus, the infrared transmitter is able to transmit to a plurality of infrared receivers independent of its orientation, as long as an infrared receiver is located within the cone of reception. Increasing the cone of reception is accomplished by increasing the power input and increasing the number of infrared emitting devices. The infrared emitting devices of the infrared transmitter are synchronized in a manner well known in the art. The infrared receiver 34 is sensitive enough to detect the emitted radiation.

Calculator 10 includes a keyboard 14, keyboard logic 16, processing logic 18, memory 20, display logic 22, transmit logic 24, infrared transmitter 26, multiplexor 25, receiver processor 30, data detector 32, infrared receiver 34, and display 12. Entries are made into calculator 10 by virtue of keyboard 14 which represents a unique pattern of key locations and labels according to particular industry conventions. Special key functions are created according to their intended use which presents an advantage in time, or function to the user, thereby offering a uniqueness

- 9 -

not found in a generic mathematically orientated calculator.

5 The memory 20 contains electrical elements (random access memories) which retain the digital representation of the mathematical operations performed by the logic 18. It also contains specific software routines in read only memory (ROM) necessary to effect functional operations and instructions necessary for operating the processing logic 18
10 according to a format of the microprocessing requirements of the processing logic 18. Memory 20 provides for bidirectional information exchange and for storage devices for support of the processor logic 18.

15 The display logic 22 provides the necessary electrical and logical interfaces between processor logic 18 and the local display unit 12. The electrical representations of the resulted logic and mathematical operations performed in the processor
20 logic 18 are presented to the display logic 12 for formatting, which enables the direct viewing by the user's local display 12. The local display 12 can be a LED, LCD, vacuum fluorescent, video scan or other commonly used data displays.

25 The display logic 22 converts the electrical signals from processing logic 18 in such a manner as to cause appropriate characters to appear on local display unit 12 through multiplexor 25. The display logic 22 stores each individual character
30 representation of a larger number or word being displayed and sequentially presents them in a repeating fashion to the display unit 12, while simultaneously activating the respective individual

- 10 -

display character locations in which the specified character appears.

5 The display unit 12 performs necessary conversion between the electrically represented characters and a user by visual methods. Light emitting diodes (LED's) are among many of the devices and techniques utilized in commonly accepted and commercially available packages, which can be arranged in segmented lines or dots in such a manner as to represent characters by approximation of their shape and orientation. In the case of an LED format display, as the electrical signals are applied to the segments or dots, each in turn illuminates. By proper and prior determination of specific individual segments or dots being illuminated, the display logic 22 causes a character to be shown by a specific segment or dot grouping. Similar adaptations or display guidance arrangements are made for other display techniques and adaptations and display guidance arrangements are made for other display techniques.

10
15
20
25
30 The transmit logic 24 interfaces between the processing logic 18, display logic 22 and the infrared transmitter 26. The processing logic 18 electrically transmits data to transmit logic 24 in parallel form, with each individual data bit occupying a unique position and polarity. The character thus presented is thus defined by a parallel arrangement of these data bits. The transmit logic 24 converts from a parallel data format to a serial data format while maintaining a position of polarity integrity of a data bit representation. The parallel data word composed of all of the data bit elements is applied to the

- 11 -

parallel input of a shift register element contained within transmit logic 24.

5 The stored data word is then caused by the function of the internal system clock to be sequentially transmitted in serial fashion by the shift register. Each individual data bit is sent in sequence to the infrared transmitter 26 for transmission. The data word is altered prior to transmission according to a predetermined formula for insuring its transmission integrity. A typical method is asynchronous serial data transmission as used in the data communications and telecommunications industry, and is employed using commonly accepted methods of encoding resident and commercial logic unit circuits, such as a universal asynchronous receiver and transmitter (UART).

10 With asynchronous serial data transmission, each serial character transmitted consists of three parts: a start bit, data bits and a stop bit. A start bit is a line state that lasts for one-bit time and indicates the beginning of the character. After the data bits are transmitted, a stop bit is sent. Asynchronous serial data transmission technique is well known in the art of data transmission.

20 Data are loaded from processing logic 18 into a transmitter holding register of transmit logic 24 whenever the register is empty, which is signified by register empty signal. Circuitry internal to the transmit logic 24 automatically loads new data into the transmitter whenever it is ready. The start and stop bits are then added to the data, and a serial transmission is initiated to the infrared transmitter

- 12 -

26. Control lines permit control of the loading and transmission process by the processing logic 18.

The infrared transmitter 26 accepts as its input the serial data stream from the transmit logic 24 and generates coded radiation pulses representatives thereof. The radiation output lies in the infrared spectrum and is enabled in response to the electrical polarity of the data being transmitted on a bit by bit basis. The infrared transmitter 26 includes a single element or series of elements which comprises infrared emitting devices.

A switch 36 selectively controls the power to elements of the calculator 10. If the switch is in the transmit position, power is enabled to the keyboard 14 and keyboard logic 16, processing logic 18, memory 20, display logic 22, transmit logic 24 and infrared transmitter 26. In the calculate mode the switch enables power to the keyboard logic 16 and the keyboard 14, process logic 18, memory 20, and display logic 22. In the receive mode the switch enables power to the receiver processor 30, the data detector 32, and infrared receiver 34. Power is constantly applied to multiplexor 25 and local display 12, since both these elements are used in the transmit, calculate or receive mode.

Power unit 51 energizes each element of calculator 10 through power contactors 120-134. In the normal state, all power contactors are open de-energizing the elements of calculator 10. Positioning switch 36 to the receive mode, enables power contactors 126-130 for energizing the receiving circuit. Placing switch 36 to the calculate mode enables power contactors 120-124 providing power to

- 13 -

keyboard 14, keyboard logic 16, processing logic 18, memory 20, and display logic 22. Positioning switch 36 to the transmit mode enables power contactors 120-124 and 132-134 for providing power to the calculating circuitry, transmit logic 24, and infrared transmitter 26. Thus, the power contactors disable elements of the system not needed during operation for conserving energy of the power unit 51. The power contactors can be of numerous types such as relays, reed switches, etc., well known to those of ordinary skill in the art.

Switch 36 also selects which inputs of multiplexor 25 are applied to the outputs of multiplexor 25. In the calculate mode multiplexor 25 is configured so that processing logic 18 works as a normal calculator. In the transmit mode, the data transmitted is also displayed on display 12 by multiplexor 25. In the receive mode, multiplexor 25 is selected so that data received by infrared receiver 34 is shown on display 12.

When the calculator 10 is switched to the receive mode, the infrared receiver 30 collects infrared signals emanating from an infrared transmitter from a transmitting processing unit and converts them to electrical signals representing the originally transmitted data bits, by altering its receiver output and response to incoming presence or absence of coded infrared pulses. The basic element of the infrared receiver 34 is a photo diode or phototransistor device, either as a single unit or an array of units. The stream of serial coded infrared pulses, alternating polarity, is applied to the data detector 32 where it is examined for position and format

- 14 -

integrity in a corresponding manner to its encryption by a transmit logic unit 24.

5 The serial coded data bits are presented in parallel form by the data detector 32 to the receiver processor 30. The receiver processor 30 arranges the data in a format similar to that of display logic 22 and sends the data in parallel fashion to multiplexor 25. In the receive mode, multiplexor 25 connects the outputs from receiver processor 30 to the local
10 display 12 for displaying received data. In the transmit mode and calculate mode multiplexor 25 is connected to display logic 22 for displaying transmitted data and calculated data.

15 In order for a remote display receiver to properly recover data being sent by an infrared transmitter the data bits are transmitted in data character groups of 8 bits in length and synchronizing start and stop bits are added to each character. The start bit is a single logical zero data bit that is
20 added to the front of each character. The stop bit is composed of two logic 1 data bits added to the end of each character. The logic 1 level is maintained until the next data character is ready to be transmitted. Asynchronous data transmission is different from
25 synchronous methods which require that data being sent be contiguous and be utilized to reconstruct a system clock signal used in its own data recoding process. It is functionally advantageous to utilize asynchronous data transmission since it does not
30 require that a synchronous clock be transmitted with the data transmission and thereby permits the reception circuitry to be greatly simplified. It is additionally advantageous that the data need not be

- 15 -

contiguous in time, but may be transmitted as it becomes available. This is particularly relevant due to the manual data entry via the keyboard 14.

5 Power is supplied to the operating circuits by means of an internal battery supply or by an AC power supply package.

10 FIG. 3 shows a system block diagram for the remote display. An infrared receiver 64 collects infrared signals emitting from infrared transmitter 26 and converts them to electrical signals representing the originally transmitted data bits, by altering its receiver output in response to the incoming presence or absence of coded infrared pulses. The basic element of the infrared receiver 64 is a photo diode or photo transistor device either as a single unit or
15 an array of units.

The stream of coded infrared pulses, alternating in polarity, is applied to the data detector logic 66 where it is examined for position and format integrity in a corresponding manner to its encryption by the
20 transmitting device. Internal to the data detector logic 66 a start bit detect circuit continually searches for logic 1 to 0 transmission while in the idle state. When detected a counter is reset and allowed to count until the center of the start bit is
25 located. The level of incoming data bit is again tested to ascertain its logical polarity. If the logical level is still low, representing a 0, the signal is assumed to be a valid start bit, and the counter continues to count to find the center of all
30 subsequent data stop bits. Verification of the start bit presents an advantage which prevents the receiver from assembling an erroneous data character when a

- 16 -

logical 0 noise burst or spike is presented to the receiver input.

As each receive character is transferred to a holding register of the data detector logic 66, control logic signals the display processor logic circuits that a character is ready for reading. An internal clock circuit is crystal frequency controlled for accuracy and stability and is used for the timing of all functions within the data detector logic 66. Additionally the internal clock frequency is sixteen times that of the comparable speed of transmission and is divided by sixteen within the data detector logic 66. This results in greater accuracy of determination with respect to locating the exact center of any data bit. Errors, if they occur, are detected and processed along with the intended data and displayed as question marks on the final display to the viewer. Circuitry used for the decoding process in the data detector logic 66 is the same as that utilized in the data detector logic circuits contained in the transmitter processing unit.

The serial recovered data bits are presented in parallel form by data detector logic 66 to the display processor 68 of the remote infrared receiver unit. The data is arranged and set in a format similar to the case of the transmitter unit. The display processor 68 may take the form of a microcomputer which is coupled by an instruction/data bus to each of a read only memory (ROM) and a random access memory (RAM). The ROM is adapted to store instructions which the display processor 68 executes to detect and recognize the received data from the infrared receiver 64. The display processor 68 processes these signals

- 17 -

for storage in memory or display on the remote display 62. The data is decoded and fed in parallel to remote display 62. The remote display 62 presents a display of the transmitted data. Thus, the remote display 62 will have the same data displayed as that of the transmitting processing unit. Power provided to the remote display unit is by internal battery or AC power supply.

2. Processing Unit

A. Calculator

FIG. 4 shows an electrical schematic drawing of a typical calculator used in the practice of the present invention. Processing logic unit 18 interfaces with keyboard 14. A key closure is sent by logic 18 when one of the key input lines, K1, K2 or K3 is forced more positive than a logical high level. At the instant of closure, an internal key bounce out stability time counter is started. Any significant voltage perturbation occurring on the switch key input during timeout resets the timer. Hence, a key is not accepted as a valid entry until noise or ringing is stopped and the stability time counter has timed out. Processing logic 18 provides for at least a 6 digit entry and display capacity for positive and negative numbers, at least four functions of addition, subtraction, multiplication and division, floating point input and output, algebraic key entry notation, chain operations, direct interface to a LED display, overflow and divide by zero error indication, and right justified entry and results with leading and trailing zero suppression. Processing logic 18

- 18 -

includes memory 20 used for performing required algebraic and mathematic manipulations. The processing logic 18 as used by applicants in the practice of this invention is a model MM5777 calculator manufactured by National Semiconductor Corporation.

B. Keyboard and Keyboard Logic

Keyboard 14 operates by causing switch closures to occur beneath individual keys depressed by a user. A switch closure connects two close proximity trachelines together, during a time interval in which the key is held depressed by the user. The two trace lines are physically and electrically located so as to form a matrix of rows in column. Thus when a connection is made a particular row is connected to a particular column which uniquely corresponds to a particular calculator function or a character on the keyboard.

The keyboard logic 16 causes an electrical signal to be systematically, and sequentially applied to only one column trace at a time, while internally recognizing which column trace is being currently activated. In a similar manner it electrically monitors each row trace and internally recognizes which row trace is being currently monitored. The resulting key depression and its effected switch closure and resulting electrical connection, between its unique column and row trace, is electrically sensed by the keyboard logic circuitry 16 since this switch closure results in the application of an electrical signal from its unique column trace to its

- 19 -

unique row trace thus being sensed and recognized during the scanning process described above.

5 The uniquely representative function or character represented by its respective row/column electrical determination is applied to the processing logic 18 for further involvement of the overall calculator operation. Resident within the processing logic circuitry are hardware and logic circuits which perform mathematical operations on the data entered by
10 the user. These operations and the results are stored and manipulated as needed to effect functions of specific natures applicable to the particular calculator style being used.

15 C. The Display Driver

The display driver 22 is a driver for a 7 segment display 12. The display driver 22 converts a BCD decade to a corresponding 7 segment display digit for display 12. Display driver 22 accepts a BCD decade as
20 input from processing logic 18 and converts it to an output for illuminating the proper segments of a 7 segment display. The digital display driver as used by the applicant is a model DS8977 manufactured by National Semiconductor Corporation.

25 The display logic 22 controls the overall brightness of the display by effecting the time duration during which each display character is "on." By causing the duration to increase, a character is perceived to be brighter as the human eye will average
30 its perception between the "on" time "off" time, with the result and appearance of average brightness. This methodology reduces the power consumption of the calculator unit as a whole, since a continuous display

- 20 -

necessitates the application and consumption of greater amounts of power as compared to an average power consumption divided between "on" and "off" times.

5

D. Multiplexor

The multiplexor 25 funnels signals from either display logic 22 or receiver processor 30 to local display 12. The inputs to multiplexor 25 are from either display logic 22 or receiver processor 30 which are selected by signals on select lines 29 and 31. The outputs from multiplexor 25 are connected to the inputs of local display 12. The signals on lines 29 and 31 are generated from switch 36. If switch 36 is in the receive position the signal on line 31 selects inputs from receiver processor 30 for connection to the outputs of multiplexor 25. In the receive mode, therefore, display data generated by processor logic 30 is applied to local display 12.

In the transit mode or calculate mode, the output from display logic 22 is applied to local display 12. The transmit logic 22 is applied to local display 12. The transmit signal from switch 36 is applied to one input of OR gate 27. The calculate signal is applied to a second input of OR gate 27. The output of OR gate 27 is applied to a select input of multiplexor 25. If a transmit or a calculate signal is present the output signal of OR gate 25 selects the display logic inputs of multiplexor 25 to be applied to the inputs of local display 12. Thus, in the transmit mode or calculate mode the output from display logic 22 is applied to the inputs of local display 12. Essentially, the multiplexor 25 switches to inputs

- 21 -

from receiver processor 30 in the receive mode to inputs from display logic 22 in the transmit or calculate mode.

5 E. Local Display

10 The display 12 as illustrated is a 7 segment digital display driven by display logic 22. The 7 segment display displays numbers between 0 and 9 and decimal points along with a 7 digit used for the negative sign of a 6 digit number and as an error indicator. Negative results less than 6 digits have the negative sign displayed one digit to the left of the most significant digit. The display as used by the applicants in the practice of this invention is a model NSAll61 display manufactured by National Semiconductor Corporation.

15 The 7 digit display uses monolithic digits and has up to 9 digits. These devices are common cathode GsAsP LED, with nominal 0.100 inch character height. Each digit comprises 7 segments with a righthand decimal point. Eight inputs provide for selection of the appropriate segments and decimal (anodes) and separate inputs for digit (cathodes) selection. The anodes are internally interconnected for multiplexing.

20 A clear lens on the display provides light transmission and ease of visibility over a wide angle. The digital display as utilized by applicants in this invention is a model NSAll00 series 0.100 inch nine digit LED display, manufactured by National Semiconductor Corporation.

25

30

- 22 -

F. Transmit Logic

The transmit logic includes a universal asynchronous receiver transmitter (UART). The transmit logic 24 is a general purpose programmable device. Display Transmit 24 converts parallel data from display logic 22 to a serial data format on transmission line 33 to drive the infrared transmitter 26.

The serial format in order of transmission and reception is a start bit, followed by five to eight data bits, a parity bit and two stop bits. The transmitter operates on the clock signals from processing logic 18. The transmitter is double buffered allowing one character to be loaded while transmitting another. The inputs and outputs are TTL compatible. The transmit logic used in the practice of the present invention is a model TR1402/TR1602 universal asynchronous receiver/transmitter as manufactured by the Western Digital Corporation. For a better understanding of the configuration and functional cooperation of the components described above, attention is directed to the discussion of the data detector, wherein the respective components are illustrated in detail.

G. Infrared Transmitter

The infrared transmitter 26 includes a GaAs infrared emitting diode designed for emitting radiation at wave length in the near infrared range. The radiation emitted is excited by current flowing in a forward direction which can be modulated. The infrared transmitter has a beam width of 25 degrees, with a power output of 16 mW and an intensity of at

- 23 -

least 16 mW/sr. The infrared transmitter accepts the serial bits from the transmit logic and produces a plurality of pulses representative of the serial bits in a manner well known in the art.

5

H. Infrared Receiver

The receiver portion of calculator 10 includes an infrared receiver 34, a data detector 32 and a receiver processor 30. The infrared receiver 34 may take the form of a planar photo silicon photodiode. The photodiode is capable of receiving the wavelength of the infrared transmitter 26 and is sensitive enough to detect the transmission from the infrared transmitter in a large meeting room. The photodiode used in the practice of this invention is a model 15 DPX93 photodiode manufactured by Litronix Company.

I. Data Detector

The data detector is a universal asynchronous receiver which converts the asynchronous serial data from the infrared receiver 34 to parallel data for transferring to the receiver processor 30. The data detector 32 is double buffered allowing one character to be read while the second character is being transformed to parallel output to the receiver processor 30. The data detector 32 monitors three types of errors caused by transmission: a parity error which indicates that the receive parity differs from that which is expected from infrared transmitter 26; a framing error indicating that the received character has no valid stop bit; an overrun error indicates a data received flag was not reset before the next 30

- 24 -

character was transferred to the receiver processor 30.

FIG. 5 is an electrical schematic drawing for a data detector/transmit logic unit. As a data
5 detector, serial data is received by receive register 100 from the infrared detector. A control register 106 activates the receiving and timing control 102 for transferring the serial data in receiver register 100 to the receiver holding register 104. The receiver
10 holding register 104 includes 8 parallel lines to transmit the bit configuration in receiver holding register 104 to the receiver processor.

In the transmit configuration, control register 106 activates the transmitter timing and control logic
15 108. Parallel data from a display logic unit enters into a transmitter holding register 112 via 8 parallel lines, TR 1 through TR 8. Under command from the transmitter timing and control unit 108, the transmitter holding register transfers the parallel
20 bit configuration it contains to a transmitter register 110. Under commands from the transmitter timing and control unit 108 the transmitter register shifts the bit configuration contained within in a serial manner on line TR 0 to the infrared transmitter
25 for transmitting to remote receiving units. Data detector 32 and receiver processor 30 are interfaced in a manner well known in the art for transmitting data from a parallel interface device to a
30 microcomputer.

J. Receiver Processor

The receiver Processor 30 includes a micro-computer which converts the parallel data from data

- 25 -

detector 32 and transfers the data to multiplexor 25 for driving local display 12. The microcomputer 30 contains the necessary hardware and logic for converting the parallel data from data collector 32 to a compatible level for local display 12. It is understood that receiver processor 30 has its own internal clock, as is well known in the art for controlling its internal operations, as well as interfacing with data detector 32 and multiplexor 25. The receiver processor 30 includes a read only memory and a random access memory. The random access memory is adapted to store instructions which the receiver processor 30 executes for detecting or recognizing input signals from data detector 32, for processing these signals, converting the data to an appropriate level for local display 12, and storing the data in designated areas of the random access memory. The programs or routines as stored in the read only memory convert the data from the data detector 32 to an appropriate level for local display 12 in a manner well known in the art. The microcomputer used by applicants in the practice of this invention is a model Z8601 manufactured by the Zilog Corporation.

3. REMOTE DISPLAY RECEIVING UNIT

Referring back to FIG. 3, FIG. 3 is an electrical block diagram for a remote display receiving unit. The remote display receiving unit 60 includes an infrared receiver 64, a data detector logic 66, a display logic 68 and a display unit 62.

- 26 -

A. Infrared Receiver

The infrared receiver 64 may take the form of a planar silicon photo diode. The photo diode is capable of receiving the wavelength of an infrared transmitter from a processing unit and is sensitive enough to detect the transmission from the infrared transmitter in a large meeting room. The infrared receiver 64 is identical and operates infrared receiver 34 of the calculator.

10

B. Data Detector Logic

The data detector logic 66 is a universal asynchronous receiver transmitter (UART) which converts the asynchronous serial data from the infrared receiver to parallel data for transferring to the receiver display logic 68. The data detector logic 66 is identical and operates as that described with respect to data detector 32 of the calculator.

15

C. Display Processor

The receiver display processor is a microcomputer which converts the parallel data from the data detector logic to signal to drive the 7 segment display unit 62. The microcomputer contains the necessary hardware and logic for converting the parallel data lines to a display on the display unit 62. The microcomputer used by applicants in the practice of this invention is a model Z8601 manufactured by the Zilog Corporation. The microcomputer is identical and operates as receiver 30 of the calculator.

20

25

30

Thus it has been shown an optical link for remotely displaying data from a calculator. The link

- 27 -

includes a plurality of transmitting processing units and remote receiving units. The remote receiving units include receiving processing units and remote display receiving units. Entered data and calculated results of a transmitting processing unit are transmitted to remote receiving units for displaying the data.

The above described embodiment of the invention is illustrative only, and modifications thereof may occur to those skilled in the art. Accordingly, this invention is not to be regarded as limited to the embodiment disclosed therein, but is to be limited as defined by the appended claims.

- 28 -

CLAIMS

1. An optically linked calculator system for displaying data generated by a local calculator at both the local calculator and at a detached display comprising:

5 calculating means for receiving data indicative of input data and commands and for calculating results responsive to the input data and commands;

a keyboard;

10 means for entering data from said keyboard into said calculating means;

display means for exhibiting said results;

a memory unit;

15 first means for transferring said results from said calculating means to said display means;

first means for converting said result to electrical signals representative thereof; and

20 an optical transmission means for converting said electrical signals into pulses of optical radiation representative thereof and for transmitting said radiation to the detached display.

- 29 -

2. The system of claim 1, wherein said optical transmission means comprises at least one infrared transmission element.

3. The system of claim 1 further comprising:
an optical receiving means for receiving said radiation pulses and for converting said radiation pulses into serial electrical signals representative thereof;

5
second means for converting said serial electrical signals into received signal;

10
second means for transferring said received signal from said second converting means to said display means.

4. The system of claim 3 wherein said optical receiving means comprises at least one infrared receiving element.

5
5. The system of claim 4 wherein said second converting means converts said serial electrical signals to parallel electrical signals and said second transferring means receives said parallel electrical signal for converting said parallel electrical signals to display signals for said display means.

6. The system of claim 5 wherein said second transferring means includes:

a microprocessing unit for converting said electrical signals into a data value;

- 30 -

5 a read only memory means for controlling the
sequence of operations of said microprocessing unit;
and

random access memory for storing said data
values.

7. The system of claim 3, further including:
means for selecting between first
transferring means and second transferring means for
sending data to said display means; and

5 switching means mounted on said calculating
means for controlling said selecting means for
effecting operation of said calculating means for
transmitting entered data and calculated results,
receiving data from a transmitting calculating means,
10 and performing calculating functions.

8. The system of claim 7, further comprising:

a plurality of power contactors wherein a
power contactor controls power to an element of the
optical link system, said power contactor being
5 responsive to said switching means.

9. The system of claim 8, wherein said power
contactors enable power to said optical receiving
means, said second means for converting and said
second transferring means when said switching means
5 selects receiving data.

10. The system of claim 8, wherein said power
contactors enable power to said keyboard, keyboard
logic, memory unit, first transferring means, and

- 31 -

5 calculating means when said switching means selects performing calculating functions.

11. The system of claim 10, wherein said power contactors enable additional power to said first converting means and said optical transmission means when said switching means selects transmitting data.

12. The system of claim 1, wherein said calculating means includes:

a microprocessing means for converting said into a data value;

5 a read only memory means for controlling the sequence of operations of said microprocessing unit; and

random access memory for storing said data values.

13. The system of claim 3 wherein said first and second converting means comprises a universal asynchronous receiver/transmitter.

14. The system of claim 1, further comprising:
a remote display for exhibiting data;
a remote optical receiving means for receiving radiation pulses for converting said radiation pulses into serial electrical signals representative thereof;

5 remote means for converting said serial electrical signals into a received signal; and

10 remote means for transferring said signal from said remote converting means to said remote display means.

- 32 -

15. The system of claim 14, wherein said remote
converting means converts said serial electrical
signals to parallel electrical signals for converting
said parallel electrical signals to display signals
5 for said remote display.

16. The system of claim 14, wherein said remote
transferring means includes:

a microprocessing means for converting said
into a data value;

5 a read only memory means for controlling the
sequence of operations of said microprocessing unit;
and

random access memory for storing said data
values.

17. The system of claim 14, wherein said remote
converting means is a universal asynchronous receiver
transmitter.

18. In an optical link system having an optical
transmitter for providing a beam of optical radiation,
an optical detector for converting received radiation
into electrical signals representative thereof, a
5 method for transmitting data comprising the steps of:

(a) generating electrical signals repre-
sentative of data entered into a calculating means;

(b) converting the electrical signals into
a form for effecting the transmission of the signals
10 over the optical link;

(c) transmitting the data through the
optical transmitter;

- 33 -

15 (d) receiving the radiation at a remote site with the optical receiver for generating electrical signals representative thereof;

(e) converting the electrical signals into a form for effecting the display of the data received; and

(f) displaying the data at the remote site.

19. The method of claim 18 wherein the step (b) of converting further includes the substeps of:

(g) converting the data into parallel electrical pulses;

5 (h) converting the parallel electrical pulses into serial electrical pulses; and

(i) transmitting the serial electrical pulses through the optical link transmitter.

20. The method of claim 19 wherein the step (e) of converting further includes the substeps of:

(j) converting the electrical signals into parallel electrical pulses; and

5 (k) converting the parallel electrical pulses into signals for displaying the received value on the remote display.

21. In an infrared link network for receiving and transmitting data between a plurality of devices, each device having at least an infrared receiver for receiving pulses of infrared transmission, an infrared transmitter for transmitting pulses of infrared transmissions, a calculator for performing calculations, and a display for exhibiting data, a
5

- 34 -

method for displaying data at each device from a transmitting device comprising the steps of:

10 (a) generating electrical signals from the calculator representative of entered data and calculated results;

(b) transforming the electrical signals to a series of electrical pulses;

15 (c) applying the electrical pulses to the infrared transmitter for generating a series of infrared pulses;

(d) receiving the infrared pulses with the infrared receiver and generating a series of electrical pulses representative thereof; and

20 (e) converting the series of electrical pulses to display signals for the display to exhibit the received data.

22. The method of claim 21, wherein the step of (c) of applying further comprises the steps of:

(f) coding the electrical pulses for producing a series of coded infrared pulses; and

5 (g) transmitting a validity condition along with the coded infrared pulses.

23. The method of claim 22, wherein the step (d) of receiving further comprises the steps of:

(h) decoding the series of coded infrared pulses into a series of coded electrical pulses;

5 (i) detecting the validity condition transmitted by the transmitting device; and

(j) determining an error condition from the validity condition of the data received.

24. The method of claim 23, further including the step of:

(k) rejecting the data upon determining an error condition.

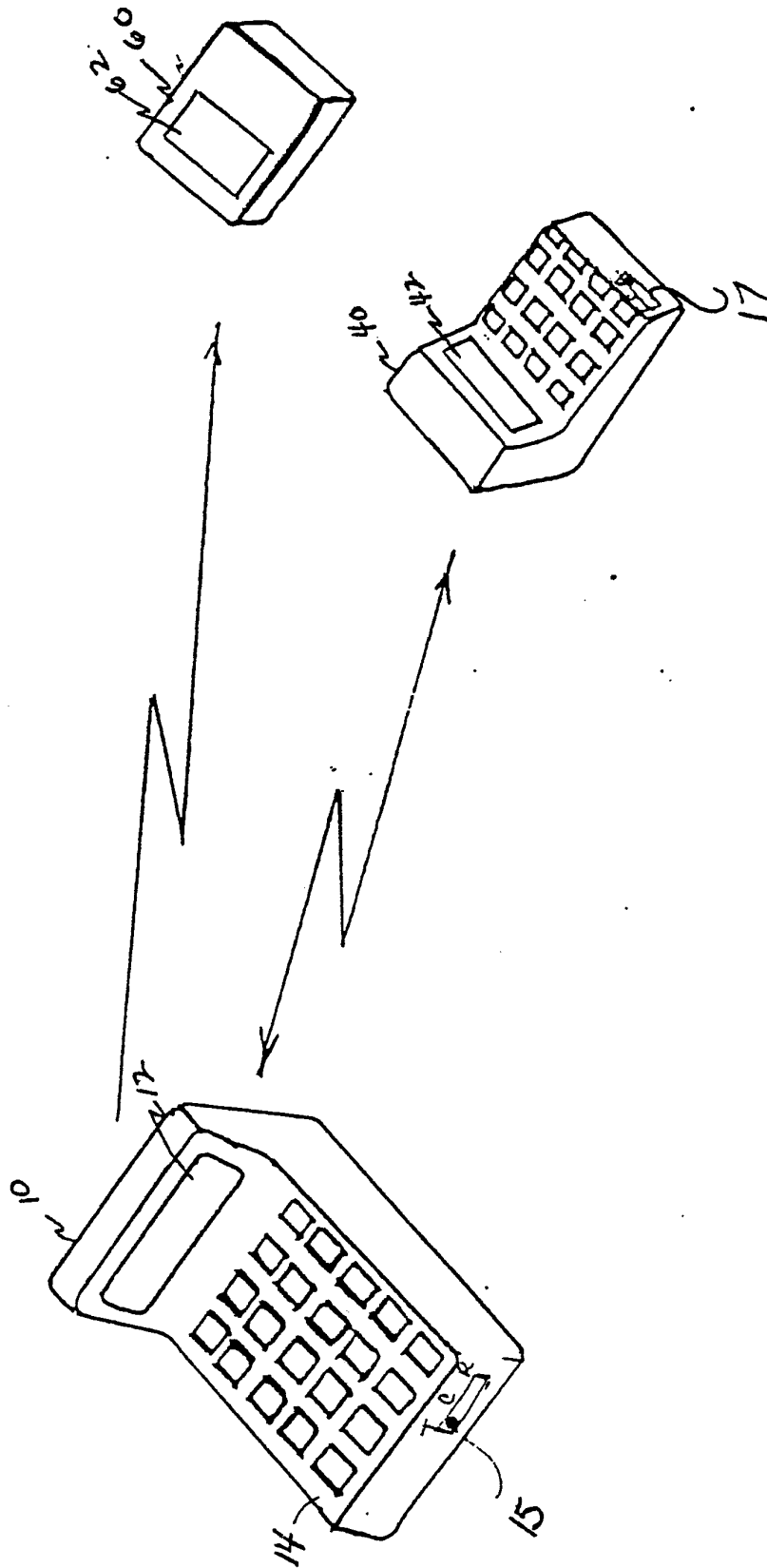


FIGURE 1
GENERAL SYSTEM
OVERVIEW

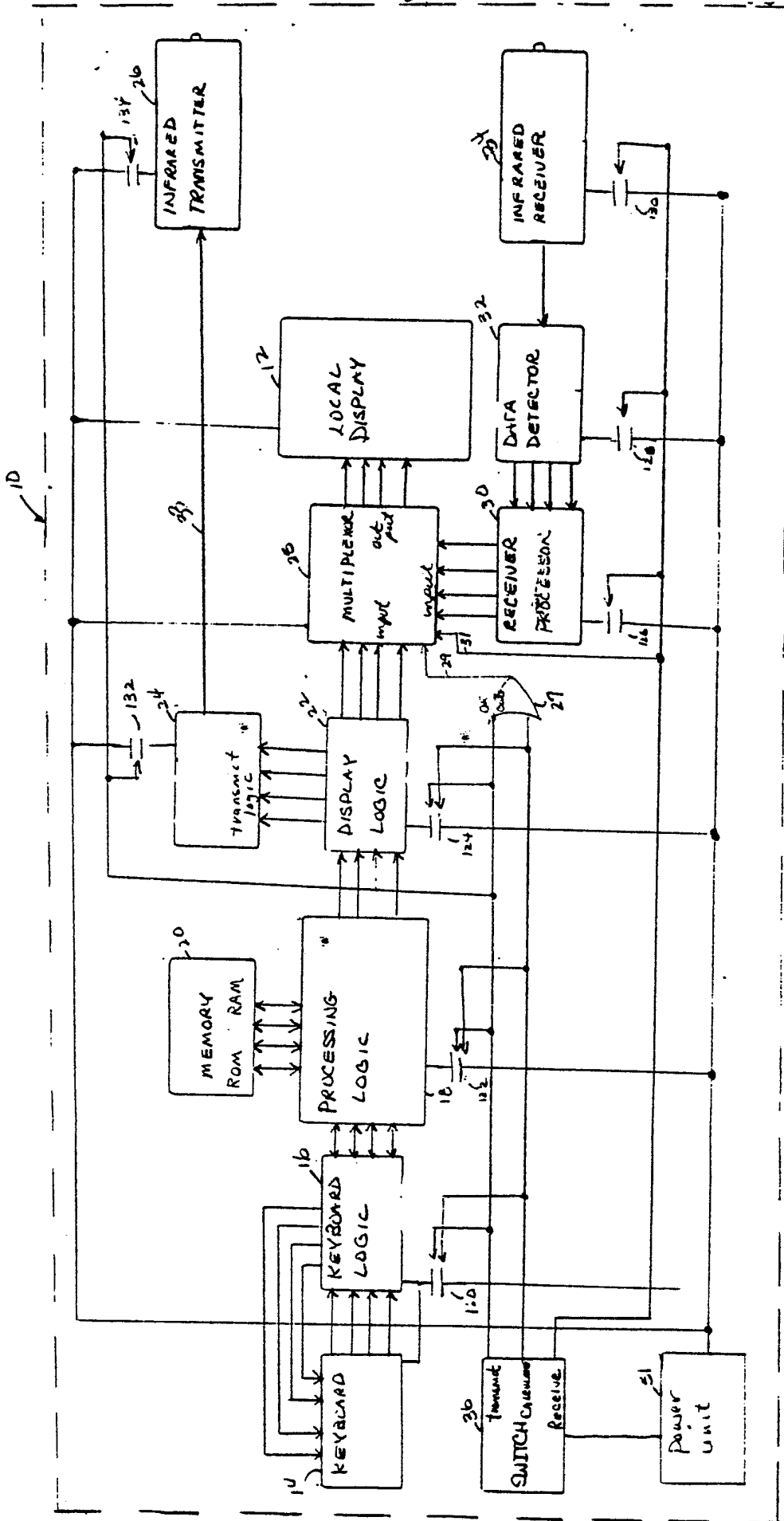


FIG 2

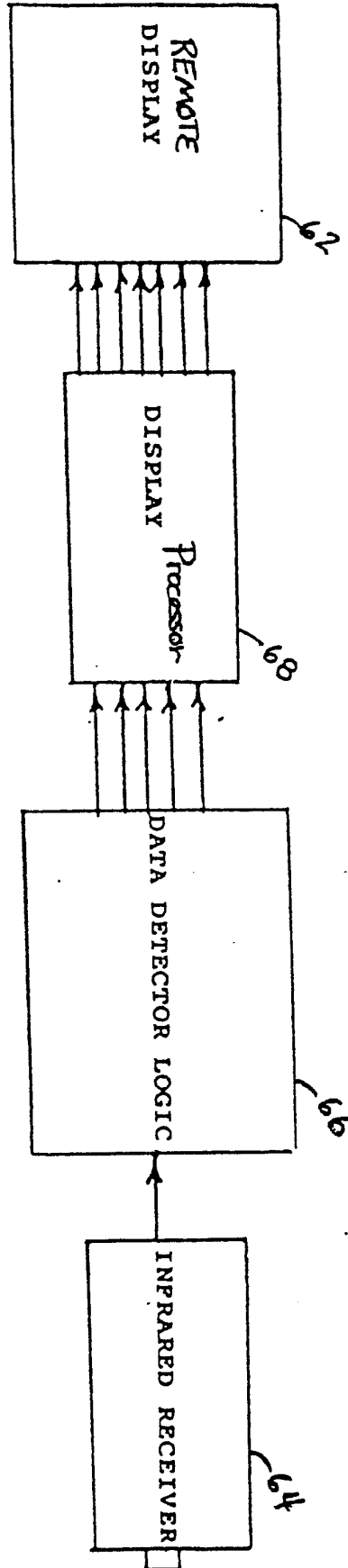


FIGURE 3
SYSTEM BLOCK DIAGRAM
ONE-WAY RECEIVER

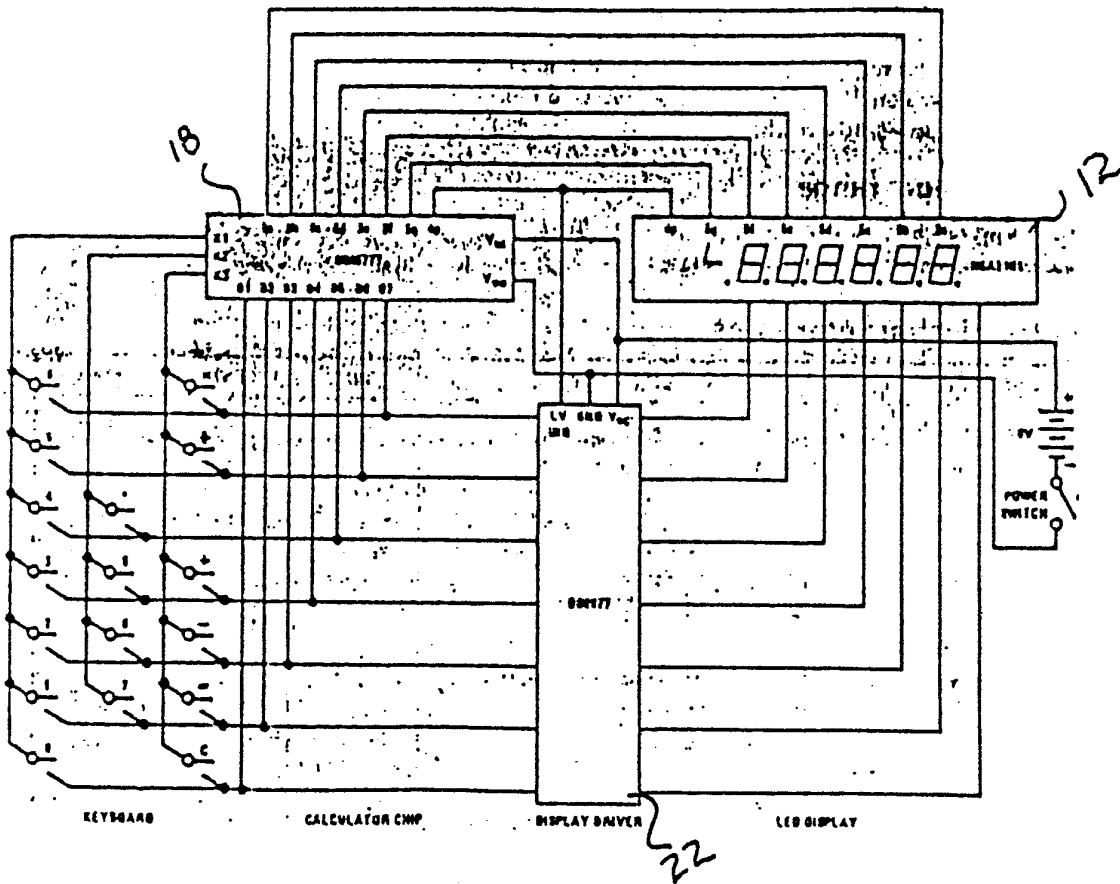


FIG. 4

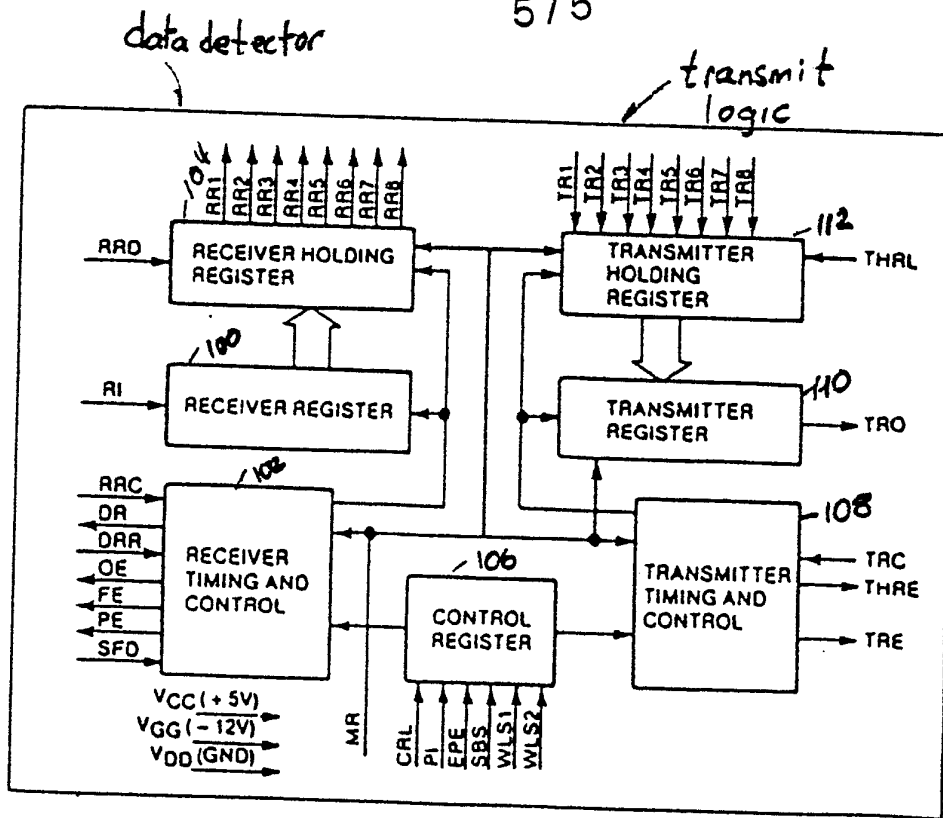
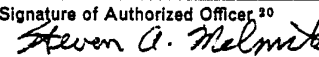


FIG. 5

INTERNATIONAL SEARCH REPORT

International Application No PCT/US87/00468

I. CLASSIFICATION OF SUBJECT MATTER (if several classification symbols apply, indicate all) ³		
According to International Patent Classification (IPC) or to both National Classification and IPC IPC(4): H04L 11/02; G06F 13/00; G09B 3/00 U.S. CL.: 364/707, 709, 710; 434/118, 365; 340/706, 711		
II. FIELDS SEARCHED		
Minimum Documentation Searched ⁴		
Classification System	Classification Symbols	
U.S.	340/706, 711; 364/704, 705, 709, 710 434/118, 191, 192, 365, DIG28; 455/603	
Documentation Searched other than Minimum Documentation to the Extent that such Documents are Included in the Fields Searched ⁵		
III. DOCUMENTS CONSIDERED TO BE RELEVANT ¹⁴		
Category *	Citation of Document, ¹⁵ with indication, where appropriate, of the relevant passages ¹⁷	Relevant to Claim No. ¹⁸
Y	US, A, 4,468,203 (GIMMESTAD et al.) 28 AUGUST 1984 See abstract, Figs. 1 and 2, summary, Col. 3 lines 63-68.	1
Y	US, A, 4,538,993 (KRUMHOLZ) 3 SEPTEMBER 1985 See the entire document.	1-7 12-17
Y,E	US, A, 4,652,240 (WACKYM) 24 MARCH 1987 See entire document.	1-7
<u>X</u> Y	JP, B, 58-179216 (MIHASHI) 29 SEPTEMBER 1983 See purpose and constitution.	1, 18, 21 1-24
<u>X</u> Y,P	US, A, 4,628,541 (BEAVERS) 9 DECEMBER 1986 See abstract and drawing figures.	18-21 1-24
L		
<div style="display: flex; justify-content: space-between;"> <div style="width: 45%;"> <p>* Special categories of cited documents: ¹⁵</p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p> </div> <div style="width: 45%;"> <p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.</p> <p>"&" document member of the same patent family</p> </div> </div>		
IV. CERTIFICATION		
Date of the Actual Completion of the International Search ¹	Date of Mailing of this International Search Report ²	
14 APRIL 1987	15 MAY 1987	
International Searching Authority ¹	Signature of Authorized Officer ²⁰	
ISA/US	 STEVEN A. MELNICK	

III. DOCUMENTS CONSIDERED TO BE RELEVANT (CONTINUED FROM THE SECOND SHEET)

Category *	Citation of Document, ¹⁶ with indication, where appropriate, of the relevant passages ¹⁷	Relevant to Claim No ¹⁸
X Y/E	US, A, 4,649,373 (BLAND et al.) 10 MARCH 1987 See drawing figures, abstract and column 2.	18-21 1-24
X Y	US, A, 3,401,469 (SHAVER et al.) 17 SEPTEMBER 1968 See Figs. 1, 8 and 9, col. 1 (especially lines 42-54) and col. 8 line 58 through col. 11.	1-6, 12-16, 18, 1-6, 12, 14-16, 18
X Y	V.M. ROONEY and A.R. ISMAIL "Microprocessors and Microcomputers", Published 1984 by Macmillan Publishing Company. See pages 226, 234-241.	13, 17 5, 13, 15, 17, 19-24
A	US, A, 4,313,227 (EDER) 26 JANUARY 1982 Abstract. Figs. 1, 2, 5.	18, 21
A	UK, A, 2084381A (REYNOLDS) 7 APRIL 1982 Figs. 1, 3. Abstract. Column 1 lines 70-105.	1-24