



- (51) **International Patent Classification:**
H02J 3/18 (2006.01) *H02M 5/22* (2006.01)
- (21) **International Application Number:**
PCT/CN2016/070254
- (22) **International Filing Date:**
6 January 2016 (06.01.2016)
- (25) **Filing Language:** English
- (26) **Publication Language:** English
- (30) **Priority Data:**
62/100,074 6 January 2015 (06.01.2015) US
62/100,076 6 January 2015 (06.01.2015) US
- (71) **Applicant:** **EDGE ELECTRONICS LIMITED** [CN/CN];
Suite 2, 1st Floor, Shun Kwong Commercial Building, 8
Des Voeux Road West, Hong Kong (CN).
- (72) **Inventors:** **STEWART, Neal George**; 5C Costa Court,
Discovery Bay, Hong Kong (CN). **CHENG, Wing Ling**;
Suite 2, 1st Floor, Shun Kwong Commercial Building, 8
Des Voeux Road West, Hong Kong (CN).
- (74) **Agent:** **CHINA PAT INTELLECTUAL PROPERTY
OFFICE**; 2nd Floor, Zhongguancun Intellectual Property
Building, Block B, No.21 Haidian South Road, Haidian,
Beijing 100080 (CN).

(81) **Designated States** (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) **Designated States** (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LI, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Published:

— with international search report (Art. 21(3))

(54) **Title:** FAST RESPONSE ACTIVE REACTIVE POWER (KVAR) COMPENSATOR

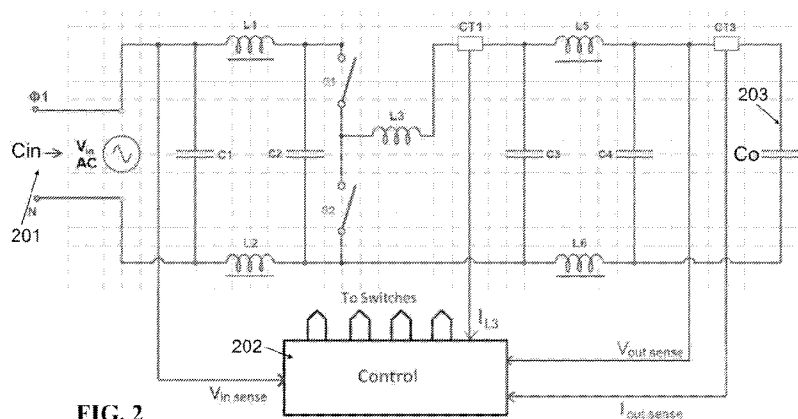


FIG. 2

(57) **Abstract:** Legacy automatic variable capacitor KVAR compensation systems typically use either electromechanical devices such as relays or contactors of various forms and types to switch the selected capacitors in and out of the electrical system under some form of electronic control. These systems are slow and discontinuous in their ability to closely regulate the exact value of compensatory capacitance needed to compensate the variable and rapidly changing reactive power KVAR in the electrical power transmission and distribution networks. The present invention provides a fast response active KVAR compensator based on a variable transimpedance topology, the KVAR compensator comprises a first and a second independently controllable AC bidirectional switches (S1, S2); a first power inductor (L1); a first current transformer (CT1) for generating a first power inductor current direction data signal indicating the first power inductor current direction; an output capacitor (Co) with a fixed capacitance or an output inductor with a fixed inductance; and a control circuitry for receiving an AC input voltage, an AC output voltage and the first power inductor current direction data signal, measuring a KVAR value and setting a variable gain to generate an apparent impedance across the AC input for compensating the KVAR.



FAST RESPONSE ACTIVE REACTIVE POWER (KVAR) COMPENSATOR

5 **Claim for Priority:**

[0001] This application claims priority to the United States Provisional Patent Application No. 62/100,074, filed January 6, 2015, and United States Provisional Patent Application No. 62/100,076, filed January 6, 2015, the disclosures of which are incorporated herein by reference in their entirety.

10

Field of the Invention:

[0002] The present invention generally relates to electrical power generation and distribution. Particularly, the present invention relates to methods and devices for alternating current (AC) reactive power (KVAR) compensation in
15 AC electrical power generation and distribution.

Background:

[0003] In AC electrical power distribution networks, the optimized operating conditions for maximum energy efficiency in the usage, transmission,
20 and delivery of AC electrical power is when the voltage and the current are closely in phase, and the reactive power (KVAR) is close to zero. However, with the addition of reactive loads and inherent reactive components such as capacitance and inductance in the transmission and distribution system, the AC current in the system can be phase-shifted with respect to the voltage waveform. This creates
25 power quality issues that affect the efficient transmission and usage of the delivered electrical power. The degradation in power quality due to the increasing reactive power value is measured by the amount of phase angle shift of the current behind (lagging) or ahead (leading) with respect to the voltage waveform. Reactive power value is derived by the following equation.

30

$$KVAR = \sqrt{KVA^2 - KW^2} \quad (1)$$

where KVAR is the total reactive power, KVA is the total apparent power, and KW is real power.

[0004] Noted that only the real power KW is useful for productive consumption, whereas the reactive power KVAR is wasted power. But since the
5 total apparent power KVA is made up of KW and KVAR as per the equation above, both power components are be generated, transmitted, and delivered. As such, an increasing phase shift of the AC current with respect to the voltage waveform, which means increasing value of reactive power KVAR will translate into a significant efficiency decrease in the electrical power system.

10 [0005] This problem is well known and there are various legacy methods of compensating and removing reactive power KVAR by introducing reactive components at various points within the electrical system. Typically these are capacitors introduced in shunt across the electricity supply lines to cancel the generally lagging current due to magnetic elements such as electric motors,
15 fluorescent ballasts, transformers, etc.

[0006] This has been traditionally achieved by adding fixed and variable capacitor banks in shunt across the electrical power transmission and distribution system. If a known and reproducible KVAR problem is stable, then a fixed capacitor KVAR compensation bank can be permanently installed at the point to
20 be compensated. If the reactive power KVAR is changing, then automatic variable capacitor banks that can respond, under electronic controls and KVAR sensing, and switch in the amount of capacitance needed to compensate for the level of KVAR at any given time.

[0007] These legacy automatic variable capacitor KVAR compensation
25 systems typically use either electromechanical devices such as relays or contactors of various forms and types to switch the selected capacitors in and out of the electrical system under some form of electronic control. In more recent versions of KVAR compensation systems, semiconductor switching devices, such as triodes for alternating current (TRIACs) and silicon-controlled rectifiers (SCRs), and
30 electromechanical switching devices have been seen in use.

[0008] Because of the need to switch discrete component capacitors in and out of circuit by various switching means, these legacy reactive power KVAR compensators are slow and discontinuous in their ability to closely regulate the exact value of compensatory capacitance needed to compensate the variable and rapidly changing reactive power KVAR in the electrical power transmission and distribution networks.

Summary:

[0009] In addressing the abovementioned shortcoming of the legacy automatic variable capacitor KVAR compensation systems, the present invention provides a fast response active KVAR compensator. In various embodiments in accordance to the present invention, the fast response active KVAR compensator is based on a variable transimpedance topology.

[0010] In one preferred embodiment, the fast response active KVAR system employs the specific AC variable voltage topology as described in the United States Patent No. 9,148,058 issued September 29, 2015 and the same in the PCT International Patent Application No. PCT/CN2014/089721 filed October 28, 2014; the disclosures of which are incorporated herein by reference in their entirety. In another preferred embodiment, the fast response active KVAR system employs the specific AC variable voltage topology as described in the United States Patent Application No. 14/565,444 filed December 10, 2014 and the same in the PCT International Patent Application No. PCT/CN2014/093475 filed December 10, 2014; the disclosures of which are incorporated herein by reference in their entirety.

Brief Description of the Drawings:

[0011] Embodiments of the invention are described in more detail hereinafter with reference to the drawings, in which

[0012] FIG. 1 depicts a logical diagram illustrating a general variable transimpedance topology;

[0013] FIG. 2 depicts a circuit diagram of an embodiment of the fast response active KVAR compensator in accordance to the present invention; and

[0014] FIG. 3 depicts a circuit diagram of another embodiment of the fast response active KVAR compensator in accordance to the present invention.

5

Detailed Description:

[0015] In the following description, methods and systems for compensating reactive power (KVAR) in electrical power generation and distribution networks and the like are set forth as preferred examples. It will be apparent to those skilled in the art that modifications, including additions and/or substitutions may be made without departing from the scope and spirit of the invention. Specific details may be omitted so as not to obscure the invention; however, the disclosure is written to enable one skilled in the art to practice the teachings herein without undue experimentation.

10 [0016] FIG. 1 depicts a logical diagram illustrating a general variable transimpedance topology and it is used herein to show the general operating principle of a transimpedance system in which the value of an output impedance (Z_o) across the output **103** of the alternating current (AC) amplifier **101** is transferred and reflected onto the input **102** of the AC amplifier. The value of this transferred input impedance (Z_{in}) is related the output impedance (Z_o) by the gain of the controlled gain AC amplifier (A_v). Thus, if the gain of the AC amplifier (A_v) is controllable and variable, not only a controlled transimpedance can be generated across the input **102** from the fixed output impedance (Z_o) across the output **103**, the apparent impedance (Z_{in}) across the input **102** can be smoothly and accurately controlled with a very fast response. The quickness of change of the apparent impedance (Z_{in}) across the input **102** is due to the fact that it only depends upon the response speed of the control electronics.

25 [0017] Still referring to FIG. 1. The relationship between the apparent input impedance (Z_{in}) and the output impedance (Z_o) is governed by the following equation.

30

$$Z_{in} = \frac{1}{Av^2} \times Z_o \quad (2)$$

In specifying a particular value of the output impedance (Z_o), either a capacitor across the output **103** with a fixed capacitance value (C_o) or an inductor across the output **103** with a fixed inductance value (L_o) can be used. Then, the relationship between the reflected apparent input capacitance (C_{in}) and the output capacitance (C_o) is governed by the following equation.

$$C_{in} = Av^2 \times C_o \quad (3)$$

And the relationship between the reflected apparent input inductance (L_{in}) and the output inductance (L_o) is governed by the following equation.

$$L_{in} = \frac{L_o}{Av^2} \quad (4)$$

It can be seen that by varying the gain of the AC amplifier (A_v), the apparent value of reflected capacitance (C_{in}) or reflected inductance (L_{in}) at the input of the ac amplifier can be varied over a range not only continuously without discrete steps, but also with a very rapid response limited only by the response speed of the control electronics.

[0018] FIG. 2 depicts a circuit diagram of an embodiment of the fast response active KVAR compensator in accordance to the present invention. In this first preferred embodiment, the fast response active KVAR compensator utilizes the buck section with an output capacitor **203** of fixed capacitance (C_o) across the output of the AC series buck-boost voltage regulator electronic circuitry described in the United States Patent No. 9,148,058 and the same in the PCT International Patent Application No. PCT/CN2014/089721 to achieve the KVAR compensation function.

[0019] Still referring to FIG. 2. As the gain of the AC variable voltage topology is changed by the electronic control module **202** in response to the level of KVAR at the input **201**, the value of the apparent input capacitance (C_{in}) across the input **201** is accurately adjusted to compensate and cancel out the reactive power KVAR. This is done without discrete capacitor steps, thus the apparent input capacitance (C_{in}) adjustment is achieved in smooth and continuous manner,

and the response time depends only upon the response speed of the control electronics of the electronic control module **202**.

[0020] Although it is described here with the preferred embodiment utilizing the buck section of the AC series buck-boost voltage regulator electronic circuitry described in the United States Patent No. 9,148,058 and the same in the PCT International Patent Application No. PCT/CN2014/089721 to achieve the KVAR compensation function, it should be obvious to an ordinarily skilled person in the art to use the boost section of the aforesaid AC series buck-boost voltage regulator electronic circuitry instead of the buck section.

[0021] Also, although it is described here with the preferred embodiment utilizing a capacitor of fixed capacitance across the output of the buck section of the AC series buck-boost voltage regulator electronic circuitry described in the United States Patent No. 9,148,058 and the same in the PCT International Patent Application No. PCT/CN2014/089721 to compensate for a lagging KVAR, it should be obvious to an ordinarily skilled person in the art to substitute the aforesaid capacitor with an inductor of fixed inductance to compensate for a leading KVAR.

[0022] FIG. 3 depicts a circuit diagram of another embodiment of the fast response active KVAR compensator in accordance to the present invention. In this second preferred embodiment, the fast response active KVAR compensator utilizes the buck section with an output capacitor **303** of fixed capacitance (C_o) across the output of the AC series buck-boost voltage regulator electronic circuitry described in the United States Patent Application No. 14/565,444 and the same in the PCT International Patent Application No. PCT/CN2014/093475 to achieve the KVAR compensation function.

[0023] Still referring to FIG. 3. As the gain of the AC variable voltage topology is changed by the electronic control module **302** in response to the level of KVAR at the input **301**, the value of the apparent input capacitance (C_{in}) across the input **301** is accurately adjusted to compensate and cancel out the reactive power KVAR. This is done without discrete capacitor steps, thus the apparent

input capacitance (C_{in}) adjustment is achieved in smooth and continuous manner, and the response time depends only upon the response speed of the control electronics of the electronic control module **302**.

5 [0024] Although it is described here with the preferred embodiment utilizing the buck section of the AC series buck-boost voltage regulator electronic circuitry described in the United States Patent Application No. 14/565,444 and the same in the PCT International Patent Application No. PCT/CN2014/093475 to achieve the KVAR compensation function, it should be obvious to an ordinarily skilled person in the art to use the boost section of the aforesaid AC series buck-boost voltage regulator electronic circuitry instead of the buck section.

10 [0025] Also, although it is described here with the preferred embodiment utilizing a capacitor of fixed capacitance across the output of the buck section of the AC series buck-boost voltage regulator electronic circuitry described in the United States Patent Application No. 14/565,444 and the same in the PCT International Patent Application No. PCT/CN2014/093475 to compensate for a lagging KVAR, it should be obvious to an ordinarily skilled person in the art to substitute the aforesaid capacitor with an inductor of fixed inductance to compensate for a leading KVAR.

20 [0026] Any ordinarily skilled person in the art can apply the inventive principles described herein to any poly-phase AC systems, such as three-phase electrical systems, without departing from the scope and spirit of the invention.

25 [0027] The embodiments disclosed herein may be implemented using general purpose or specialized computing devices, computer processors, microcontrollers, or electronic circuitries including but not limited to digital signal processors (DSP), application specific integrated circuits (ASIC), field programmable gate arrays (FPGA), and other programmable logic devices configured or programmed according to the teachings of the present disclosure. Computer instructions or software codes running in the general purpose or specialized computing devices, computer processors, or programmable logic

devices can readily be prepared by practitioners skilled in the software or electronic art based on the teachings of the present disclosure.

[0028] The foregoing description of the present invention has been provided for the purposes of illustration and description. It is not intended to be
5 exhaustive or to limit the invention to the precise forms disclosed. Many modifications and variations will be apparent to the practitioner skilled in the art.

[0029] The embodiments were chosen and described in order to best explain the principles of the invention and its practical application, thereby enabling others skilled in the art to understand the invention for various
10 embodiments and with various modifications that are suited to the particular use contemplated. It is intended that the scope of the invention be defined by the following claims and their equivalence.

CLAIMS

5 1. A reactive power (KVAR) compensator for compensating leading or lagging KVAR in alternating current (AC) distribution systems, the KVAR compensator comprising:

 a first and a second independently controllable AC bidirectional switches;

10 a first power inductor;

 a first current transformer for generating a first power inductor current direction data signal indicating the first power inductor current direction;

15 an output capacitor with a fixed capacitance or an output inductor with a fixed inductance; and

 a control circuitry for receiving an AC input voltage, an AC output voltage, and the first power inductor current direction data signal, measuring a KVAR value, and setting a variable gain to generate an apparent impedance across the AC input for compensating the KVAR.

20

2. A reactive power (KVAR) compensator for compensating leading or lagging KVAR in alternating current (AC) distribution systems, the KVAR compensator comprising:

 a first unipolar paths, comprising:

25 a first half-bridge, comprising a first and a second rectifiers connected in series with a first and a second independently controllable unipolar switches respectively,

 a first power inductor, and

- a first current transformer for generating a first power inductor current direction data signal indicating the first power inductor current direction;
- a second unipolar paths, comprising:
- 5 a second half-bridge, comprising a third and a forth rectifiers connected in series with a third and a forth independently controllable unipolar switches respectively,
- a second power inductor, and
- a second current transformer for generating a second power inductor current direction data signal indicating the second power inductor current direction;
- 10 an output capacitor with a fixed capacitance or an output inductor with a fixed inductance; and
- a control circuitry for receiving an AC input voltage signal, an AC output voltage signal, the first power inductor current direction data signal, and the second power inductor current direction data signal, measuring a KVAR value, and setting a variable gain to generate an apparent impedance across the AC input for compensating the KVAR.
- 15
- 20 3. A reactive power (KVAR) compensator for compensating leading or lagging KVAR in alternating current (AC) distribution systems, the KVAR compensator comprising:
- a first unipolar paths, comprising:
- a first half-bridge, comprising a first and a second unipolar switching devices,
- 25 a first power inductor, and
- a first current transformer for generating a first power inductor current direction data signal indicating the first power inductor current direction;
- 30 a second unipolar paths, comprising:

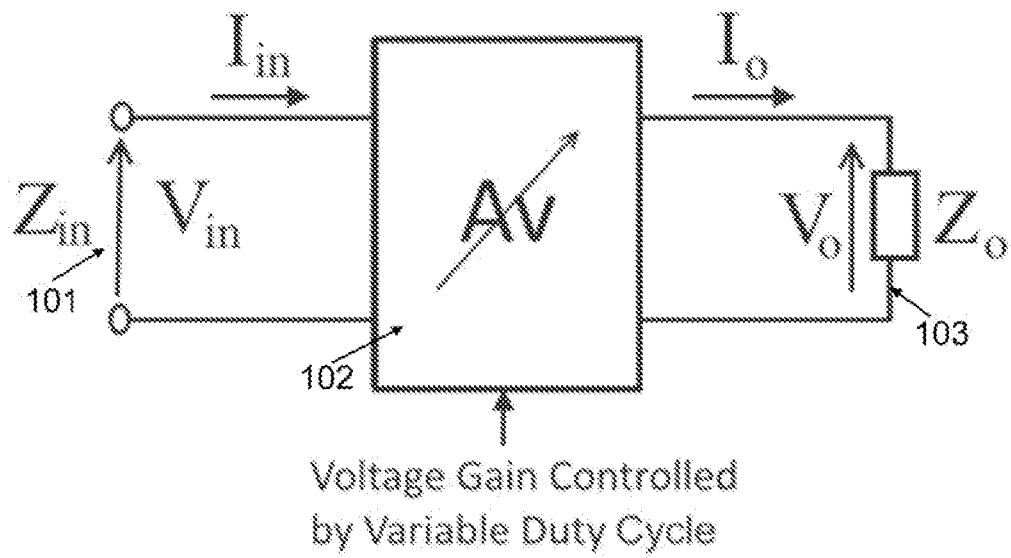
a second half-bridge, comprising a third and a forth unipolar switching devices,

a second power inductor, and

5 a second current transformer for generating a second power inductor current direction data signal indicating the second power inductor current direction;

an output capacitor with a fixed capacitance or an output inductor with a fixed inductance; and

10 a control circuitry for receiving an AC input voltage signal, an AC output voltage signal, the first power inductor current direction data signal, and the second power inductor current direction data signal, measuring a KVAR value, and setting a variable gain to generate an apparent impedance across the AC input for compensating the KVAR.

**FIG. 1**

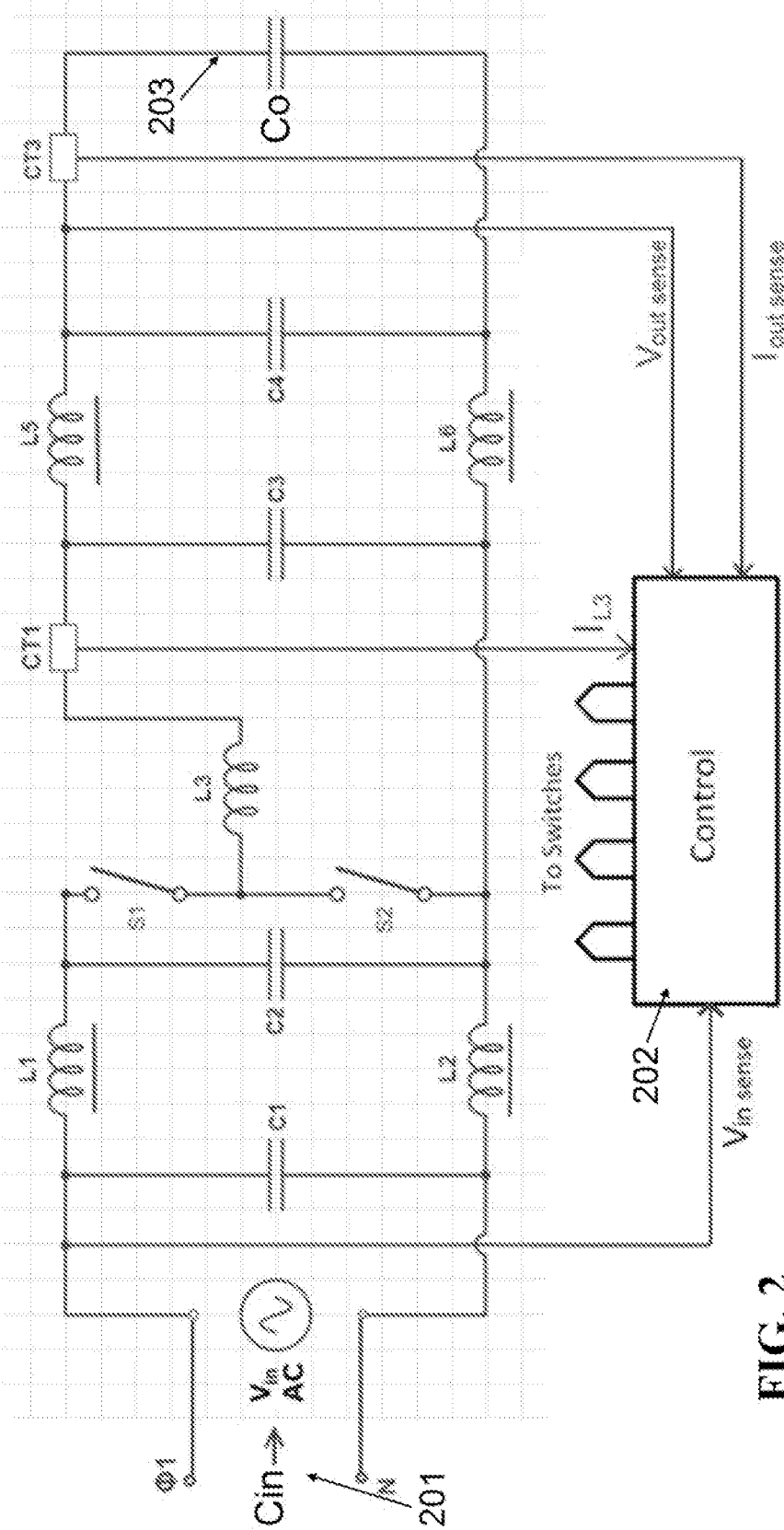


FIG. 2

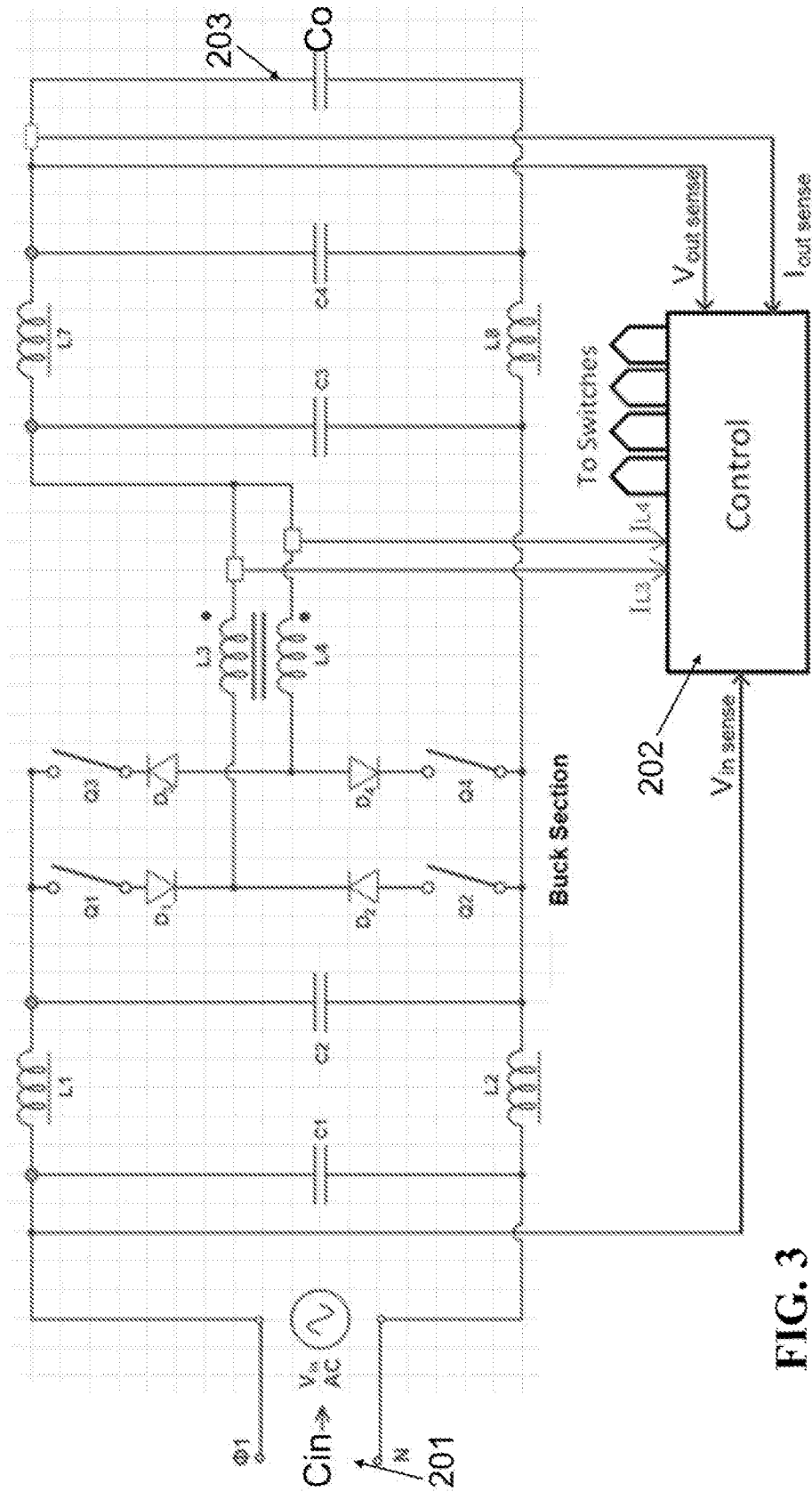


FIG. 3

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2016/070254

A. CLASSIFICATION OF SUBJECT MATTER

H02J 3/18(2006.01)i; H02M 5/22(2006.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H02J; H02M

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPODOC, WPI, CNPAT, USTXT, CNKI, IEEE: vari+, bridg+, alter+, bidirect+, AC, compensa+, chop+, switch, current, reactiv+, gain

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	CN 202034781 U (ANHUI LVNENG ELECTRIC TECHNOLOGY CO., LTD.) 09 November 2011 (2011-11-09) description, paragraphs [0016]-[0017], figures 1-2	1
X	CN 103457276 A (BEIJING YINGBO NEW ENERGY CO., LTD. ET AL.) 18 December 2013 (2013-12-18) description, paragraphs [0024]-[0026], figure 2	1
X	CN 103812352 A (UNIV. ZHENGZHOU LIGHT IND.) 21 May 2014 (2014-05-21) description, paragraphs [0020]-[0026], figure 2	2, 3
A	US 2012126734 A1 (NATIONAL FORMOSA UNIVERSITY) 24 May 2012 (2012-05-24) the whole document	1-3
A	US 2014125307 A1 (VERD, FRED) 08 May 2014 (2014-05-08) the whole document	1-3

☐ Further documents are listed in the continuation of Box C.
 ☒ See patent family annex.

* Special categories of cited documents:

“A” document defining the general state of the art which is not considered to be of particular relevance

“E” earlier application or patent but published on or after the international filing date

“L” document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

“O” document referring to an oral disclosure, use, exhibition or other means

“P” document published prior to the international filing date but later than the priority date claimed

“T” later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

“X” document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

“Y” document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

“&” document member of the same patent family

Date of the actual completion of the international search

25 February 2016

Date of mailing of the international search report

28 March 2016

Name and mailing address of the ISA/CN

STATE INTELLECTUAL PROPERTY OFFICE OF THE
P.R.CHINA
6, Xitucheng Rd., Jimen Bridge, Haidian District, Beijing
100088, China

Facsimile No. (86-10)62019451

Authorized officer

WANG, Xun

Telephone No. (86-10)62413643

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.

PCT/CN2016/070254

Patent document cited in search report			Publication date (day/month/year)	Patent family member(s)			Publication date (day/month/year)
CN	202034781	U	09 November 2011	None			
CN	103457276	A	18 December 2013	CN	103457276	B	25 November 2015
CN	103812352	A	21 May 2014	CN	103812352	B	23 September 2015
US	2012126734	A1	24 May 2012	US	8547053	B2	01 October 2013
				TW	I435536	B	21 April 2014
				TW	201223129	A	01 June 2012
US	2014125307	A1	08 May 2014	None			