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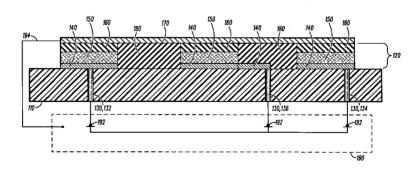
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(57) Abstract: An electroluminescent display contains an array of dynamically addressable pixels. The pixels are arranged on one side of a carrier substrate. Conductive vias in the substrate are electrically connected to each of the pixels. Each pixel consists of a bottom electrode that is coupled to a via, an electroluminescent material, and a dielectric material. A common top electrode is disposed on the dielectric material. A driver circuit conductor or connector is situated on the other side of the substrate and is electrically coupled to each of the conductive vias and to the common top electrode, so that each pixel can be individually addressed to illuminate the electroluminescent material on individual pixels.

INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER IPC: G09G 3/30(2006.01),3/06(2006.01)					
USPC: 345/44,45,76-81 According to International Patent Classification (IPC) or to both national classification and IPC					
B. FIELDS SEARCHED					
Minimum documentation searched (classification system followed by classification symbols) U.S.: 345/44, 45, 76-81					
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched					
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) EAST					
C. DOC	JMENTS CONSIDERED TO BE RELEVANT				
Category *	Citation of document, with indication, where appropriate, of the relevant passages			Relevant to claim No.	
X	US 6,738,031 B2 (YOUNG et al) 18 MAY 2004, col. 4, lines 13-62, col. 5, lines 7-57.			1-27	
Х	US 5,585,695 (KITAI) 17 DEC 1996, col. 4, lines 2-6, col. 4, lines 67-col. 5, line 44, col. 5, line 52-col. 6, line 34.			1-27	
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Further	documents are listed in the continuation of Box C.		See patent family annex.		
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