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(71) **Applicant** *(for all designated States except US)*: **QUALCOMM INCORPORATED** [US/US]; Attn: International Ip Administration, 5775 Morehouse Drive, San Diego, California 92121 (US).

(72) **Inventors; and**

(75) **Inventors/Applicants** *(for US only)*: **GRBO, Zeljko** [RS/US]; 5775 Morehouse Drive, San Diego, California 92121 (US). **PRODIC, Aleksandar** [CA/CA]; 5775 Morehouse Drive, San Diego, California 92121 (US). **CAROBOLANTE, Francesco** [US/US]; 5775 Morehouse Drive, San Diego, California 92121 (US).

(74) **Agent**: **HOOKS, William M.**; Attn: International Ip Administration, 5775 Morehouse Drive, San Diego, Califonia 92121 (US).

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[Continued on next page]

(54) **Title**: POWER SUPPLY CONTROLLER

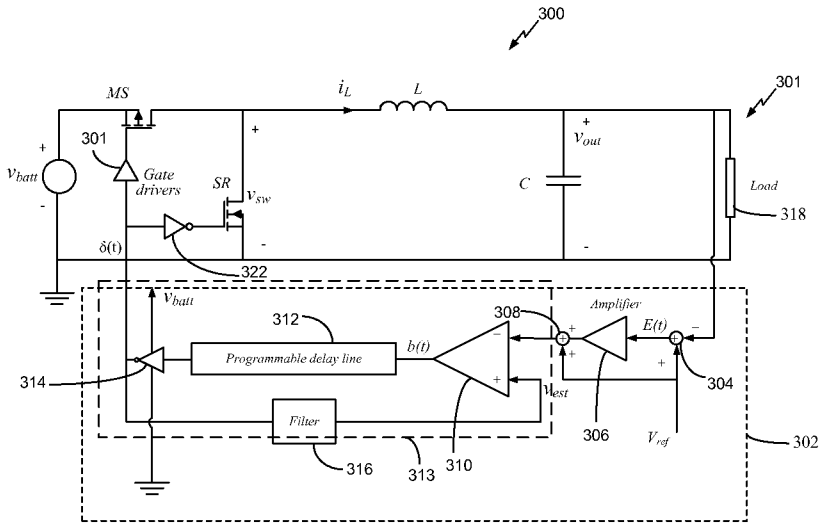


FIG. 3

(57) **Abstract**: Exemplary embodiments are directed to a power controller. A method may include comparing a summation voltage comprising a sum of an amplified error voltage and a reference voltage with an estimated voltage to generate a comparator output signal. The method may also include generating a gate drive signal from the comparator output signal and filtering a signal coupled to a power stage to generate the estimated voltage.

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Declarations under Rule 4.17:

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— *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))*

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A. CLASSIFICATION OF SUBJECT MATTER
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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H02M

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	ES 2 335 963 A1 (UNIV MADRID POLITECNICA [ES]) 6 April 2010 (2010-04-06)	1,2, 4-13, 16-28
Y	abstract figures 13, 16 page 6, line 30 - page 6, line 38 page 7, line 19 - page 7, line 67	3,15

X	US 2005/127881 A1 (SASE TAKASHI [JP] ET AL) 16 June 2005 (2005-06-16) figure 1	1,2, 4-13, 16-28

Y	US 6 064 187 A (REDL RICHARD [CH] ET AL) 16 May 2000 (2000-05-16) figure 9 column 10, line 19 - line 64	3,15

☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

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Name and mailing address of the ISA/	Authorized officer
European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016	Lorenzo Barreiro, M

INTERNATIONAL SEARCH REPORT

Information on patent family members

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