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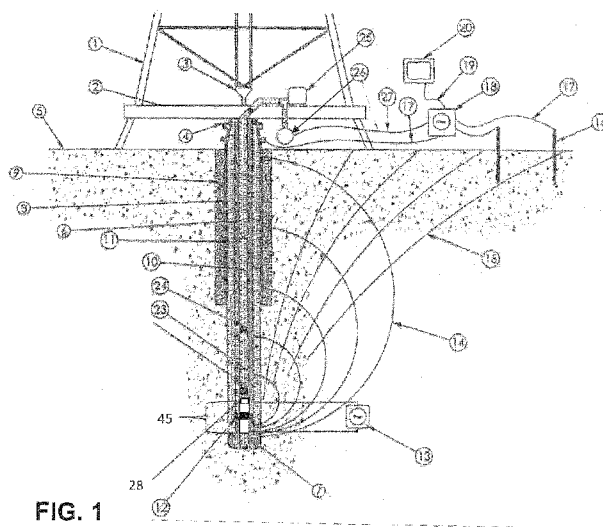
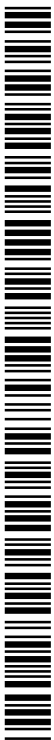


FIG. 1

(57) Abstract: A system for charging a capacitor used to power measurement-while-drilling equipment includes a power bus, which is electrically connected to the capacitor; a first pair of battery terminals; switching circuitry for electrically connecting the power bus to and disconnecting the power bus from the first pair of battery terminals; and a controller, for controlling the switching circuitry, which is configured to charge the capacitor by applying a first pulse width modulated control signal to control the switching circuitry. The first pulse width modulated control signal has a duty cycle selected such that the voltage of the first battery remains above a first minimum operating voltage while the capacitor is being charged.



## SYSTEM AND METHOD FOR CHARGING A CAPACITOR USED TO POWER MEASUREMENT-WHILE-DRILLING EQUIPMENT

### TECHNICAL FIELD

[0001] The present disclosure is directed at systems, methods, and techniques for charging a capacitor used to power measurement-while-drilling equipment.

### BACKGROUND

[0002] The recovery of hydrocarbons from subterranean zones relies on the process of drilling wellbores. The process includes using drilling equipment situated at surface and a drill string extending from equipment on the surface to a subterranean zone of interest such as a formation. The drill string can extend thousands of meters below the surface. The downhole terminal end of the drill string includes a drill bit for drilling the wellbore. Drilling wellbores also typically involves using some sort of drilling fluid system to pump a drilling fluid (“mud”) through the inside of the drill string, which cools and lubricates the drill bit and then exits out of the drill bit and carries rock cuttings back to the surface. The mud also helps control bottom hole pressure and prevents hydrocarbon influx from the formation into the wellbore and potential blow out at the surface.

[0003] Directional drilling is the process of steering a well from vertical to intersect a target endpoint or to follow a prescribed path. At the downhole terminal end of the drill string is a bottom-hole-assembly (“BHA”) that includes 1) the drill bit; 2) a steerable downhole mud motor; 3) sensors including survey equipment (e.g. one or both of logging-while-drilling (“LWD”) and measurement-while-drilling (“MWD”) tools (both “LWD” and “MWD” are hereinafter collectively referred to as “MWD” for simplicity)) to evaluate downhole conditions as drilling progresses; 4) telemetry equipment to transmit data to surface; and 5) other control equipment such as stabilizers or heavy weight drill collars. The BHA is conveyed into the wellbore by a string of metallic tubulars known as drill pipe. The MWD equipment is used to provide in a near real-time mode downhole sensor and status information to the surface while drilling. This information is used by

the rig operator to make decisions about controlling and steering the drill string to optimize the drilling speed and trajectory based on numerous factors, including lease boundaries, existing wells, formation properties, hydrocarbon size and location, *etc.* This can include making intentional deviations from the planned wellbore path as necessary based on the information gathered from the downhole sensors during the drilling process. The ability to obtain real-time data allows for a relatively more economical and more efficient drilling operation.

[0004] MWD is performed using MWD tools, each of which contains a sensor package to survey the wellbore and to send data back to the surface by various telemetry methods. Such telemetry methods include, but are not limited to telemetry via a hardwired drill pipe, acoustic telemetry, telemetry via a fiber optic cable, mud pulse (“MP”) telemetry and electromagnetic (“EM”) telemetry.

[0005] MP telemetry involves using a fluid pressure pulse generator to create pressure waves in the circulating mud in the drill string. Mud is circulated between the surface and downhole using positive displacement pumps. The resulting flow rate of mud is typically constant. The pulse generator creates pressure pulses by changing one or both of the flow area and path of the mud as it passes through the MWD tool in a timed, coded sequence, thereby creating pressure differentials in the drilling fluid. The pressure differentials or pulses may either be negative pulse or positive pulses in nature. Valves that use a controlled restriction within the circulating mud stream create a positive pressure pulse. Some valves are hydraulically powered to reduce the required actuation power typically by using a main valve controlled by a pilot valve. The pilot valve closes a flow restriction, which actuates the main valve and creates a change in pressure.

[0006] The pressure pulses generated by the pulse generator are used to transmit information acquired by the downhole sensors. Signals from the sensors are received and processed in a data encoder in the BHA where the data is digitally encoded. A controller then actuates the pulse generator to generate the mud pulses, which are modulated to represent the data. For example, the directional or inclination

data is conveyed or modulated using the physical mud pulse by generating the mud pulse at a particular amplitude and frequency. Typically a high-frequency sinusoid waveform is used as a carrier signal, but a square wave pulse train may also be used.

[0007] A typical arrangement for EM telemetry uses parts of the drill string as an antenna. The drill string is divided into two conductive sections by including an electrically insulating joint or connector (a "gap sub") in the drill string. The gap sub is typically placed within the BHA such that metallic drill pipe in the drill string above the gap sub serves as one antenna element and metallic sections below the gap sub serve as another antenna element. EM telemetry signals can then be transmitted by applying electrical signals across the two antenna elements. The signals typically include very low frequency AC signals applied in a manner that codes information for transmission to the surface. The electromagnetic signals may be detected at the surface, for example by measuring electrical potential differences between the drill string and one or more grounding rods spaced from the drill string.

[0008] Both EM and MP telemetry systems use a downhole source of power. One common power source is downhole batteries.

[0009] MWD systems contain power systems that are generally of two types. The first type uses a turbine or other generator to produce power downhole, and the second type uses specialized batteries developed for downhole applications. Turbines are powered via circulation of drilling fluid, whereas batteries operate independently of drilling fluid flow. In some cases, both types of power systems are used to help ensure adequate power is delivered to service all downhole load requirements. The batteries are typically lithium-thionyl chloride batteries, which provide high energy density and can withstand temperatures of up to approximately 180 - 200 °C. Many downhole batteries are rated to be able to store approximately 26 - 28 A·h @ 3.6 V per cell. The load is generally determined by electrical components within the BHA, drill collar geometry, gap sub or mud pulser specifications, and the properties of the surrounding formation. As an example, the current drawdown on the gap sub will vary depending on signal attenuation to the surface; or in a mud pulser, the current drawdown will vary with

the torque required to actuate the valve that generates mud pulses. An example of a typical industry battery is Exium™ Technologies Inc. MWD 3.6 DD size Li-SOCl<sub>2</sub>.

[0010] Notwithstanding these existing battery management systems, there exists a continued need for methods, systems, and techniques to manage batteries used in downhole MWD applications.

## **SUMMARY**

[0011] According to a first aspect, there is provided a system for charging a capacitor used to power measurement-while-drilling equipment, which comprises a power bus, wherein the capacitor is electrically connected to the power bus; a first pair of battery terminals for connecting to a first battery; switching circuitry operable to electrically connect the power bus to and to disconnect the power bus from the first pair of battery terminals; and a controller, operable to control the switching circuitry, and configured to charge the capacitor by applying a first pulse width modulated control signal to control the switching circuitry, wherein the first pulse width modulated control signal has a duty cycle selected such that the voltage of the first battery remains above a first minimum operating voltage while the capacitor is being charged.

[0012] The minimum operating voltage may be a reset voltage, and the controller may be further configured to reset the system if the voltage of the first battery drops below the reset voltage. The controller may be further configured to disconnect the first pair of battery terminals from the power bus when the voltage of the first battery drops below a voltage floor, and the voltage floor is above the reset voltage. The voltage floor may be approximately 50% of the maximum voltage of the battery.

[0013] The first pulse width modulated control signal may be configured to cause the first battery to be connected to the power bus for approximately 1.5 ms and disconnected from the power bus for approximately 2 ms for each charging cycle.

[0014] The controller may be configured to charge the capacitor for a charging period. The controller may be further configured to electrically connect the first pair of

battery terminals to the capacitor following the charging period and to subsequently indefinitely keep the first pair of battery terminals and the capacitor electrically connected.

[0015] The system may further comprise a first voltmeter, communicative with the controller, and electrically connected in parallel to the first pair of battery terminals such that the voltage of the first battery can be determined.

[0016] The system may further comprise a first ammeter electrically connected in series to the first pair of battery terminals such that current flowing out of the first battery can be determined.

[0017] The system may further comprise a second pair of battery terminals for connecting to a second battery and which are electrically connected in parallel to the first pair of battery terminals. Switching circuitry is operable to electrically connect the power bus to and to disconnect the power bus from the second pair of battery terminals independently from the first pair of battery terminals. The controller is further configured to apply a second pulse width modulated control signal to the switching circuitry such that the system alternates between only charging the capacitor using the first battery, by electrically connecting the first pair of battery terminals to the power bus, and electrically disconnecting the second pair of battery terminals from the power bus. The controller is further configured to only charge the capacitor using the second battery by electrically connecting the second pair of battery terminals to the power bus and electrically disconnecting the first pair of battery terminals from the power bus, wherein the second pulse width modulated control signal has a duty cycle selected such that the voltage of the second battery remains above a second minimum operating voltage while the capacitor is being charged.

[0018] The first and second minimum operating voltages may be identical. The minimum operating voltages may be a reset voltage, and the controller may be further configured to reset the system if the voltage of the first or second batteries drops below the reset voltage.

[0019] The controller may be further configured to disconnect the first pair of battery terminals from the power bus when the voltage of the first battery drops below a first voltage floor and to disconnect the second pair of battery terminals from the power bus when the voltage of the second battery drops below a second voltage floor, wherein the voltage floors may be above the reset voltage.

[0020] The first and second voltage floors may be identical. The voltage floors may be approximately 50% of the maximum voltage of the first battery.

[0021] The pulse width modulated control signals may be configured to cause the capacitor to always be charged by at least one of the batteries if the capacitor voltage drops below the battery voltage. The pulse width modulated control signals may be further configured to cause each of the batteries to be connected to the power bus for approximately 1.5 ms for each charging cycle.

[0022] The controller may be configured to charge the capacitor for a charging period. The controller may be further configured to electrically connect at least one of the pairs of battery terminals to the capacitor following the charging period and to subsequently indefinitely keep the at least one of the pairs of battery terminals and the capacitor electrically connected.

[0023] The system may further comprise first and second voltmeters which are communicative with the controller, and which are electrically connected in parallel to the first and second pairs of battery terminals, respectively, such that the voltages of the first and second batteries can be determined.

[0024] The system may further comprise first and second ammeters which are electrically connected in series to the first and second pairs of battery terminals, respectively, such that current flowing out of the first and second batteries can be determined.

[0025] According to another aspect, there is provided a method for charging a capacitor used to power measurement-while-drilling equipment. The method comprises

applying a first pulse width modulated control signal to intermittently electrically connect the capacitor to and disconnect the capacitor from a first battery, wherein the first pulse width modulated control signal has a duty cycle selected such that the voltage of the first battery remains above a first minimum operating voltage while the capacitor is being charged. The minimum operating voltage may be a reset voltage, and the method may further comprise monitoring the voltage of the first battery; and resetting circuitry used to charge the capacitor if the voltage of the first battery drops below the reset voltage.

[0026] The method may further comprise disconnecting the first pair of battery terminals from the capacitor when the voltage of the first battery drops below a voltage floor, wherein the voltage floor is above the reset voltage. The voltage floor may be approximately 50% of the maximum voltage of the first battery.

[0027] The first pulse width modulated control signal may be configured to cause the first battery to be connected to the power bus for approximately 1.5 ms and disconnected from the power bus for approximately 2 ms for each charging cycle.

[0028] The first battery may charge the capacitor for a charging period. The method may further comprise, following the charging period, electrically connecting the first pair of battery terminals to the capacitor and subsequently indefinitely keeping the first pair of battery terminals and the capacitor electrically connected.

[0029] The method may further comprise applying a second pulse width modulated control signal to intermittently electrically connect the capacitor to and disconnect the capacitor from a second battery. The pulse width modulated control signals are configured to alternate between only charging the capacitor using the first battery by electrically connecting the first pair of battery terminals to the power bus and electrically disconnecting the second pair of battery terminals from the power bus; and only charging the capacitor using the second battery by electrically connecting the second pair of battery terminals to the power bus and electrically disconnecting the first pair of battery terminals from the power bus. The second pulse width modulated control

signal has a duty cycle selected such that the voltage of the second battery remains above a second minimum operating voltage while the capacitor is being charged.

[0030] The first and second minimum operating voltages may be identical. The minimum operating voltages may be a reset voltage and the method may further comprise monitoring the voltages of the first and second batteries; and resetting circuitry used to charge the capacitor if the voltage of the first battery drops below the reset voltage.

[0031] The method may further comprise disconnect the first pair of battery terminals from the power bus when the voltage of the first battery drops below a first voltage floor and disconnecting the second pair of battery terminals from the power bus when the voltage of the second battery drops below a second voltage floor, wherein the voltage floors are above the reset voltage. The first and second voltage floors may be identical. The voltages of the first and second batteries may be identical and the voltage floors are approximately 50% of the maximum voltage of the battery.

[0032] The pulse width modulated control signals may be configured to cause the capacitor to always be charged by at least one of the batteries. The pulse width modulated control signals may be further configured to cause each of the batteries to be connected to the power bus for approximately 1.5 ms for each charging cycle.

[0033] The capacitor may be charged for a charging period. The method may further comprise electrically connecting at least one of the pairs of battery terminals to the capacitor following the charging period and subsequently indefinitely keeping the at least one of the pairs of battery terminals and the capacitor electrically connected.

[0034] According to another aspect, there is provided a non-transitory computer readable medium having encoded thereon statements and instructions configured to cause a controller to perform any of the foregoing methods.

[0035] This summary does not necessarily describe the entire scope of all aspects. Other aspects, features and advantages will be apparent to those of ordinary skill in the art upon review of the following description of specific embodiments.

## **BRIEF DESCRIPTION OF THE DRAWINGS**

[0036] In the accompanying drawings, which illustrate one or more exemplary embodiments:

[0037] Figure 1 shows a telemetry system that is capable of both electromagnetic and mud pulse telemetry, according to one embodiment.

[0038] Figure 2 is a block diagram of an electronics subassembly that forms part of the system of Figure 1 and that can be used to charge a capacitor used to power MWD equipment, according to another embodiment.

[0039] Figure 3 is a block diagram of a portion of another embodiment of the electronics assembly that can be used to charge a capacitor used to power MWD equipment, according to another embodiment.

[0040] Figures 4 to 7 are waveforms of battery voltage measured during charging of a capacitor, according to additional embodiments.

[0041] Figures 8 and 9 are methods for charging a capacitor used to power MWD equipment, according to additional embodiments.

[0042] Figure 10 is a perspective view of a capacitor bank, according to another embodiment.

## **DETAILED DESCRIPTION**

[0043] Directional terms such as “top,” “bottom,” “upwards,” “downwards,” “vertically,” and “laterally” are used in the following description for the purpose of providing relative reference only, and are not intended to suggest any limitations on how

any article is to be positioned during use, or to be mounted in an assembly or relative to an environment.

[0044] Referring to Figure 1, there is shown a schematic representation of a combined EM and MP telemetry system in which various embodiments of a system for charging a capacitor can be employed. It is to be noted that while Figure 1 depicts a combined EM and MP telemetry systems, embodiments of the capacitor charging system can also be used in EM-only and MP-only telemetry systems (not shown). Downhole drilling equipment including a derrick 1 with a rig floor 2 and draw works 3 facilitate rotation of drill pipe 6 into the ground 5. The drill pipe 6 is enclosed in casing 8 which is fixed in position by casing cement 9. Bore drilling fluid 10 is pumped down the drill pipe 6 and through an electrically isolating gap sub assembly 12 by a mud pump 25 to a drill bit 7. Annular drilling fluid 11 is then pumped back to the surface and passes through a blowout preventer ("BOP") 4 positioned above the ground surface. The gap sub assembly 12 is electrically isolated (nonconductive) at its center joint effectively creating an electrically insulating break, known as a gap between the top and bottom parts of the gap sub assembly 12. The gap sub assembly 12 may form part of the BHA and be positioned at the top part of the BHA, with the rest of the BHA below the gap sub assembly 12 and the drill pipe 6 above the gap sub assembly 12 each forming an antennae for a dipole antennae.

[0045] The combined EM and MP telemetry system comprises a downhole MWD telemetry tool 45 and surface receiving and processing equipment. The telemetry tool 45 comprises an EM telemetry unit having an EM signal generator 13 which generates an alternating electrical current 14 that is driven across the gap sub assembly 12 to generate carrier waves or pulses which carry encoded telemetry data ("EM telemetry transmission"). The low frequency AC voltage and magnetic reception is controlled in a timed/coded sequence by the telemetry tool 45 to energize the earth and create an electrical field 15, which propagates to the surface and is detectable by the surface receiving and processing equipment 18 of the MWD telemetry system. The telemetry tool 45 also includes a MP telemetry unit having a fluid pressure pulse generator 28 for

generating pressure pulses in the drilling fluid 10 which carry encoded telemetry data ("MP telemetry transmission"). The fluid pressure pulse generator 28 can be actuated by an MP transmitter 30 (shown in Figure 2), comprising a motor (not shown), between an open configuration in which no pressure pulse is generated, a reduced flow configuration in which a full positive pressure pulse is generated (represented schematically as full pressure pulse 24), and an intermediate flow configuration in which an intermediate positive pressure pulse (represented schematically as intermediate pressure pulse 23) is generated.

[0046] At surface, the surface receiving and processing equipment includes a receiver box 18, computer 20 and other equipment to detect and process both EM and MP telemetry transmissions. To detect EM telemetry transmissions, communication cables 17 transmit the measurable voltage differential from the top of the drill string and various surface grounding rods 16 located about the drill site to EM signal processing equipment, which receives and processes the EM telemetry transmission. The grounding rods 16 are generally randomly located on site with some attention to site operations and safety. The EM telemetry signals are received by the receiver box 18 and then transmitted to the computer 20 for decoding and display, thereby providing EM measurement-while-drilling information to the rig operator. To detect MP telemetry transmissions, a pressure transducer 26 that is fluidly coupled with the mud pump 25 senses the pressure pulses 23,24 and transmits an electrical signal, via a pressure transducer communication cable 27, to MP signal processing equipment for processing. The MP telemetry transmission is decoded and decoded data is sent to the computer display 20 via the communication cable 19, thereby providing MP measurement-while-drilling information to the rig operator.

[0047] Referring now to Figure 2, there is shown an electronics subassembly 200 that includes sensors for taking various downhole measurements, and encoding and processing equipment for encoding the measurements and other information (collectively, "telemetry data") into a telemetry signal for transmission by the EM transmitter 13. The electronics subassembly 200 is housed within a tubular housing

(not shown). The sensors shown in Figure 2 are a direction and inclination (“D&I”) module 212 and a gamma module 210. The D&I module 212 includes three axis accelerometers, three axis magnetometers and associated data acquisition and processing circuitry. The gamma module 210 includes a gamma ray detector. Other types of sensors, not shown in Figure 2, may also be included in the electronics subassembly 200 in alternative embodiments; for example, one or more additional sensors may be included that measure borehole parameters and conditions including temperature, pressure, shock, vibration, and RPM. The gamma and D&I modules 210,212 are communicatively coupled to and controlled by a D&I and gamma interface 208. An exemplary D&I and gamma interface 208 is a dsPIC<sup>CTM</sup> digital signal controller from Microchip Technology Inc.

[0048] The encoding and processing equipment includes an MP transmitter 30 communicatively coupled to the fluid pressure pulse generator 28, and the EM transmitter 13 and EM receiver 29, each of which is electrically connected to and isolated from the other by the gap sub assembly 12.

[0049] The D&I and gamma interface 208, MP transmitter 30, EM transmitter 13, and EM receiver 29 are each electrically connected to a power bus 218 via switches 203 that are controlled by a power management controller 206, which may be, for example, a PIC18 8-bit PIC<sup>TM</sup> microcontroller from Microchip Technology Inc. In the depicted embodiment these switches 203 are voltage regulators having an enable pin with which the power management controller 206 is communicative. Although not indicated in Figure 2, the controller 206 also is communicatively coupled to each of the D&I and gamma interface 208, the MP transmitter 30, the EM transmitter 13, and the MP receiver 29 in order to receive sensor measurements, to transmit these sensor measurement to the surface, and to receive downlink signals sent downhole from the surface.

[0050] First and second batteries 214a,b (collectively, the “batteries 214”) are connected to first and second pairs of battery terminals (not shown), which are electrically connected in parallel. The first and second pairs of battery terminals are

respectively electrically connected in series to a pair of switches 202 that are each electrically connected in series to the power bus 218. The switch 202 that connects the first pair of battery terminals to the power bus 218 is hereinafter the “first switch 202”, while the switch 202 that connects the second pair of battery terminals to the power bus 218 is hereinafter the “second switch 202”. The controller 206 is independently communicative with the first and second switches 202 via a pair of switching control modules 204, each of which is connected to one of the first and second switches 202 and to the controller 206. The first and second switches 202 are MOSFET switches, and the control modules 204 are MOSFET controllers. In the depicted embodiment the batteries 214 are each J size batteries having ten cells, with each cell rated at 3.6 V and 40 A·h. Each of the batteries 214 is consequently rated at 36 V and 40 A·h.

[0051] Electrically connected in parallel to the power bus 218 is a capacitor bank 216, comprising multiple capacitors. The capacitor bank 216, when charged, stores charge that can be used to temporarily supplement current output by the batteries 214, thereby helping to reduce current draw on the batteries 214 during times of relatively high electrical load. The capacitor bank 216 accordingly helps to provide stable power to electrical loads that draw power from the power bus 218. As discussed in further detail below, the capacitor bank 216 can be charged by closing one or both of the first and second switches 202, which electrically connects the batteries 214 to the capacitor bank 216. An exemplary capacitor bank 216 includes anywhere from one to ten capacitors connected in parallel; for example, five capacitors connected in parallel with the capacitor bank 216 rated at 13.5 mF.

[0052] Referring now to Figure 3, there is shown a portion of another embodiment of the electronics subassembly 200. Like the embodiment of the electronics subassembly 200 of Figure 2, the electronics subassembly 200 of Figure 3 incorporates the batteries 214, controller 206, switching control modules 204, first and second switches 202, the power bus 218, and the capacitor bank 216 in a structurally identical manner as shown in Figure 2. However, the electronics subassembly of Figure 3 also includes data collection circuitry. The data collection circuitry includes first and

second voltmeters 304 electrically connected in parallel to the first and second pairs of battery terminals between the battery terminals and the first and second switches 202, respectively, and first and second ammeters 302 electrically connected in series to the first and second pairs of battery terminals, respectively. The data collection circuitry also includes third and fourth voltmeters 304 electrically coupled in parallel between the first switch 202 and the power bus 218 and the second switch 202 and the power bus 218, respectively. The first and second voltmeters 304 communicate with the controller 206 via voltage control modules 310 that digitize the voltage reading from the voltmeters 304 for processing by the controller 206, whereas the third and fourth voltmeters 304 are directly communicative with and sampled by the controller 206. The first ammeter 302 communicates with the controller 206 via a first overcurrent detector 306, while the second ammeter 302 communicates with the controller 206 via a second overcurrent detector 306. As with the voltage control modules 310, each of the overcurrent detectors 306 digitizes the ammeter readings for subsequent processing by the controller 206. Each of the overcurrent detectors 306 is also able to compare the current discharged by one of the batteries 214 to a shutdown current, and to output a signal that shuts down the electronics subassembly 200 if the current from either of the batteries 214 exceeds the shutdown current. Although not shown in Figure 3, each of the overcurrent detectors 306 may have a shutdown output connected to one or more reset inputs elsewhere in the electronics subassembly 200 to reset the electronics subassembly 200 in the event battery current becomes excessive. In contrast to the embodiment of the electronics subassembly 200 shown in Figure 2, then, the embodiment of Figure 3 provides the controller 206 with feedback information in the form of the voltages of the batteries 214 and the current being discharged by the batteries 302, which the controller 206 can use to determine how to charge the capacitor bank 216, as discussed in further detail below.

[0053] As shown in Figures 2 and 3, the capacitor bank 216 may comprise multiple capacitors as it does in Figure 2, or alternatively may include only a single capacitor, as shown in Figure 3.

[0054] Referring now to Figure 8, there is shown a method 800 for charging a capacitor used to power MWD equipment; when used in conjunction with the electronics subassembly 200 of Figure 3, the method 800 is used to charge the capacitor bank 216. The method 800 is encoded on to a computer readable medium (not shown) within the controller 206 as statements and instructions that cause the controller to perform the method 800. The method 800 does not require the use of voltage or current feedback, and consequently can be performed by the electronics subassembly shown in Figures 2 and 3.

[0055] In one application of the method 800, both the first and second switches 202 are open immediately prior to the method 800 commencing, and the second switch 202 remains open for the duration of the method 800, which means that neither of the batteries 214 are electrically connected to the capacitor bank 216. The method 800 begins at step 802, following which the controller 206 proceeds to step 804 and connects the first battery 214a to the power bus 218 by closing the first switch 202. The controller 206 then proceeds to step 806 where it allows the first battery 214a to remain connected to the capacitor bank 216 for a connection duration. When the capacitance of the capacitor bank 216 is 13.5 mF and each of the batteries 214 is rated at 36 V and 40 A·h, an exemplary connection duration is approximately 1.5 ms.

[0056] The controller 206 then proceeds to step 808 and disconnects the first battery 214a from the power bus 218 by opening the first switch 202. The controller 206 then proceeds to step 810 where it keeps the first battery 214a disconnected from the capacitor bank 216 for a disconnection duration. When the capacitance of the capacitor bank 216 is 13.5 mF and each of the batteries 214 is rated at 36 V and 40 A·h, an exemplary disconnection duration is approximately 2 ms. After waiting for the disconnection duration, the controller 206 proceeds to step 812 and checks to see whether the capacitor bank 216 has been charged for a charging period, which is a period of time empirically determined to result in the capacitor bank 216 being sufficiently charged by performing the actions described in steps 804 to 810. In the depicted embodiment, the charging period is 3 seconds. If the charging period has

elapsed, the controller 206 proceeds to step 814 and the method 800 ends. If the charging period has not yet elapsed, the controller 206 proceeds back to step 804 and again connects the capacitor bank 216 to the first battery 214a.

[0057] After the charging period elapses, the controller 206 keeps one or both of the batteries 214 electrically coupled for an indefinite period of time to the power bus 218 and, consequently, the capacitor bank 216. As the capacitor bank 216 will have been charged, current draw from the batteries 214 electrically connected to the power bus 218 by the capacitor bank 216 is low and keeping at least one of the batteries 214 electrically coupled to the power bus 218 allows the other electronic components in the electronics subassembly to be powered.

[0058] After the charging period elapses, the controller 206 keeps the first battery 214a electrically coupled to the power bus 218 and, consequently, the capacitor bank 216. As the capacitor bank 216 will have been charged, current draw from the first battery 214a by the capacitor bank 216 is low and keeping the first battery 214a electrically coupled to the power bus 218 allows the other electronic components in the electronics subassembly to be powered.

[0059] In the foregoing embodiment, the connection and disconnection durations are determined empirically prior to deploying the MWD tool. These durations are selected so that battery voltage remains above a voltage floor ( $V_{\text{floor}}$ ) and battery current remains below a current ceiling while the capacitor is being charged. The voltage floor represents the minimum voltage to which the voltages of the batteries 214 are allowed to decrease, as a result of the batteries' 214 internal resistances, while charging the capacitor bank 216. In the depicted embodiments in which each of the batteries 214 has a voltage of 36 V when no current is being drawn from them, the voltage floor is set to be approximately 50% of this value, or approximately 18 V. Similarly, the electronics subassembly 200 includes a fuse (not shown) that blows at the current ceiling, which in the depicted embodiments is approximately 7 A. This helps to prevent damage to the electronics subassembly 200 in the event that voltage monitoring alone proves insufficient.

[0060] Referring now to Figures 4 through 7, there are shown examples of waveforms 402 of battery voltage measured during charging of the capacitor bank 216, according to various embodiments.

[0061] Example: Pulse Width Modulation

[0062] Figure 4 displays a waveform 402 of the voltage measured at the first pair of battery terminals when the capacitor bank 216 is being charged with the first battery 214a, as described above in respect of Figure 8. The maximum voltage of the first battery 214a when it is discharging no current is shown as  $V_{\text{batt\_max}}$ . The reset voltage of the entire electronics subassembly 200, which is the voltage at which the electronics subassembly 200 will restart, is depicted as  $V_{\text{reset}}$ . The controller 206 monitors the voltage of the first battery 214a and resets the electronics assembly if it falls below the reset voltage.

[0063] At time  $t_1$ , the controller 206 closes the first switch 202 and electrically connects the first battery 214a to the capacitor bank 216; this corresponds to step 804 of the method 800 of Figure 8. The voltage of the first battery 214a is significantly drawn down due to the high current used to initially charge the capacitor bank 216, but remains about the reset voltage. The controller 206 then allows the capacitor bank 216 to charge for the connection duration, labeled  $\Delta t_{\text{closed}}$  in Figure 4, which corresponds to step 806 of the method 800 of Figure 8. After the connection duration, the controller 206 opens the first switch 202 at time  $t_2$ , which disconnects the first battery 214a from the capacitor bank 216 and which corresponds to step 808 of the method 800 of Figure 8. The controller 206 keeps the capacitor bank 216 and the first battery 214a disconnected for the disconnection duration, labeled  $\Delta t_{\text{open}}$  in Figure 4, which corresponds to step 810 of the method 800 of Figure 8. At time  $t_3$ , the controller 206 determines whether the capacitor bank 216 is fully charged; it can do this by measuring the voltage of the capacitor bank 216 when both the first and second switches 202 are open. Because at time  $t_3$  in Figure 4 the capacitor bank 216 is not fully charged, the controller 206 returns to step 804 of the method 800 of Figure 8, and the charging cycle repeats, as shown in Figure 4. A “charging cycle” in respect of Figure 4 refers to the

time between sequential instances in which the first switch 202 is closed; e.g., one charging cycle is between  $t_1$  and  $t_3$ .

[0064]        Example: Pulse Width Modulation with Voltage Floor

[0065]        Referring now to Figures 6 and 7, there are shown waveforms 402 of the voltage measured at the first pair of battery terminals when the capacitor bank 216 is being charged with the first battery 214a in embodiments in which the controller 206 monitors battery voltage and responds if the battery voltage drops below the voltage floor.

[0066]        Because battery voltage does not fall below the voltage floor in Figure 5, the waveform 402 shown in Figure 5 is identical to that of Figure 4 notwithstanding that the controller 206 is monitoring battery voltage to ensure that it remains above the voltage floor. However, in Figure 6, the waveform 402 does fall below the voltage floor. To prevent the voltage from continuing to fall and dropping below the reset, when the battery voltage drops below the voltage floor the controller 206 opens the first switch 202 and disconnects the first battery 206 from the capacitor bank 216. As shown in Figure 6, the actual closed duration for the first charging cycle of the capacitor bank 216 is  $t_2 - t_1$ , and is shorter than the intended closed duration for the charging cycle, which is  $t_3 - t_1$ . Subsequent charging cycles in which the battery voltage does not drop below the voltage floor occur as described in respect of Figure 4. The actual charging cycle in Figure 5 is  $t_4$  (the next time at which the switch 202 closes after time  $t_1$ )  $- t_1$ .

[0067]        Example: Dual Battery Charging

[0068]        Referring now to Figure 7, there are shown first and second waveforms 402a,b that result from charging the capacitor bank 216 using both the first and second batteries 214a,b. The first waveform 402a is the voltage measured across the first pair of battery terminals and consequently is the voltage of the first battery 214a, while the second waveform 402b is the voltage measured across the second pair of battery terminals and consequently is the voltage of the second battery 214b.

[0069] To generate the waveforms 402 of Figure 7, the controller 206 is configured to alternate between only charging the capacitor bank 216 using the first battery 214a by electrically connecting the first pair of battery terminals to the power bus 218 and electrically disconnecting the second pair of battery terminals from the power bus 218, and only charging the capacitor bank 216 using the second battery 214b by electrically connecting the second pair of battery terminals to the power bus 218 and electrically disconnecting the first pair of battery terminals from the power bus 218. This is now described with reference to Figure 7 and to Figure 9, which shows an embodiment of a method 900 for charging the capacitor bank 216 using the first and second batteries 214. In Figure 7, exemplary charging cycles for the first battery 214a are between 1)  $t_1$  and  $t_3$  and 2)  $t_3$  and  $t_5$ , whereas an exemplary charging cycle for the second battery 214b is between  $t_2$  and  $t_4$ .

[0070] The controller 206 begins performing the method 900 at step 902 and proceeds to step 904. At step 904 the controller 206 disconnects the second battery 214b from the capacitor bank 216 by opening the second switch 202 (or, if the second battery 214b is already disconnected, ensures that the second switch 202 remains open) and closes the first switch 202 to connect the first battery 214a to the capacitor bank 216. This occurs at time  $t_1$  of Figure 7. The controller 206 then proceeds to step 906 where it waits for a first battery connection duration, following which it proceeds to step 908. The first battery connection duration refers to the length of time the first battery 214a is electrically connected to the capacitor bank 216 and that the second battery 214b is consequently disconnected from the capacitor bank 216. At step 908, which occurs at time  $t_2$  of Figure 7, the controller 206 disconnects the first battery 214a from the capacitor bank 216 by opening the first switch 202 and closes the second switch 202 to connect the second battery 214b to the capacitor bank 216. The controller 206 then proceeds to step 910 where it waits for a second battery connection duration, which refers to the length of time the second battery 214b is electrically connected to the capacitor bank 216 and the first battery 214a is consequently disconnected from the capacitor bank 216. The controller 206 then proceeds to step 912 and checks to see whether the capacitor bank 216 has been charged for a charging

period, which is a period of time empirically determined to result in the capacitor bank 216 being sufficiently charged by performing the actions described in steps 904 to 910. In the depicted embodiment, the charging period is 3 seconds. If the charging period has elapsed, the controller 206 proceeds to step 914 and the method 900 ends. If the charging period has not yet elapsed, the controller 206 proceeds back to step 904 and again connects the capacitor bank 216 to the first battery 214a.

[0071] In an alternative embodiment (not depicted), the controller 206 checks the voltage of the capacitor bank 216 using the third and fourth voltmeters 304 to determine whether it is charged. If the capacitor bank 216 has been charged, the controller 206 proceeds to step 914 and the method 900 ends. However, if the capacitor bank 216 has not yet been charged, the controller 206 returns to step 904 and again connects the capacitor bank 216 to the first battery 214a.

[0072] After the charging period elapses, the controller 206 keeps one or both of the batteries 214 electrically coupled for an indefinite period of time to the power bus 218 and, consequently, the capacitor bank 216. As the capacitor bank 216 will have been charged, current draw from the any batteries 214a electrically connected to the power bus 218 by the capacitor bank 216 is low and keeping at least one of the batteries 214 electrically coupled to the power bus 218 allows the other electronic components in the electronics subassembly to be powered.

[0073] The controller 206 then proceeds to step 912 where it checks the voltage of the capacitor bank 216 to determine whether it is sufficiently charged; if yes, the method 900 ends at step 914. If no, which is the case at time  $t_3$  after the controller 206 has waited for the second battery connection duration the first time, the controller 206 proceeds back to step 904 and continues to charge the capacitor bank 216 by alternating between the first and second batteries 214a,b. In Figure 7 for example, from times  $t_3$  to  $t_4$  the capacitor bank 216 is charged only by the first battery 214a, and from times  $t_4$  to  $t_5$  the capacitor bank 216 is charged only by the second battery 214b.

[0074] In the embodiment of Figure 9, the controller 206 keeps one of the batteries 214 electrically connected to the capacitor bank 216 constantly throughout charging assuming that the battery voltages do not drop below the voltage floor. However, in alternative embodiments (not shown), even when charging using multiple batteries the controller 206 may select during the charging period to disconnect all of the batteries from the capacitor 216 to pause charging regardless of whether the battery voltage is above the voltage floor.

[0075] In Figures 8 and 9, the controller 206 electrically opens and closes the first and second switches 202 by applying first and second pulse width modulated control signals to the first and second switching control modules 204, respectively. In the depicted embodiment, when one of the pulse width modulated control signals are high, the switch 202 controlled by that control signal is closed. Similarly, when one of the pulse width modulated control signals is low, the switch 202 controlled by that control signal is open. While in the depicted embodiment the controller 206 directly generates the first and second pulse width modulated control signals by outputting the signals through different output pins, in an alternative embodiment (not depicted) the controller 206 may indirectly generate both signals by outputting only a single control signal and using logic, external to the controller 206, to generate the other control signal. For example, in an embodiment in which the first and second control signals are inverted, an inverter outside of the controller 206 may be used to invert the first control signal, which the controller 206 outputs, to generate the second control signal. The duty cycles of the pulse width modulated control signals are selected so that the voltages of the batteries 214 remain above a minimum operating voltage. In the depicted embodiment this minimum operating voltage is the reset voltage, although in alternative embodiments (not shown) dropping below this minimum operating voltage may result in an action other than a reset being performed. For example, in an embodiment in which the minimum operating voltage corresponds to a shutoff voltage, dropping below the threshold may result in the system shutting down.

[0076] In the depicted embodiments the batteries 214 are identical to each other, and consequently the same reset voltages and voltage floors are used for both batteries 214. In alternative embodiments (not depicted), regardless of whether the batteries 214 are identical different reset voltages and voltage floors may be used for the batteries. For example, the first battery 214a may have a first reset voltage and a first voltage floor, while the second battery 214b may have a second reset voltage and a second voltage floor. Similarly, in embodiments (not shown) that have three or more batteries, any two or more of the batteries may share reset voltages or voltage floors.

[0077] Charging the capacitor bank 216 using both of the batteries 214 helps to preserve the batteries' 214 lives, since each is used only half as much as opposed to embodiments in which only one of the batteries 214 is used to charge the capacitor bank 216. Using both of the batteries 214 also charges the capacitor bank 216 more quickly, since instead of the capacitor bank 216 not being charged during the disconnection durations of Figure 8, in certain embodiments one of the batteries 214 is always connected to and charging the capacitor bank 216.

[0078] The capacitor bank 216 may be contained within a modular housing to permit relatively easy electrical connection to and disconnection from the remainder of the electronics subassembly 200, as is shown in Figure 10. In Figure 10, five capacitors 1006 are electrically connected in parallel to form the modular housing, which is cylindrical in shape. At one of the flat ends of the capacitor bank 216 are a positive terminal 1002 for electrically connecting to the power bus 218 and a negative terminal 1004 for electrically connecting to ground. The tubular housing in which the electronics subassembly 200 is housed has an opening into which the capacitor bank 216 as shown in Figure 10 may be inserted. Any suitable type of detachable coupling may be used to physically couple the capacitor bank 216 to the remainder of the electronics subassembly 200. In Figure 10, the positive and negative terminals 1002,1004 comprise a plug that may be inserted into a socket (not shown) on the electronics subassembly 200. In alternative embodiments (not depicted), however, other types of couplings may be used, such as latches. An exemplary capacitance of the capacitors

1006 used in the capacitor bank 216 is 2,700  $\mu\text{F}$ . Furthermore, while five capacitors 1006 are shown in Figure 10, in alternative embodiments (not depicted) a different number of capacitors 1006 may be used, such as between one and ten capacitors 1006.

[0079] The embodiments described herein can help facilitate the use of relatively high capacitance capacitor banks 216. For example, by preventing the battery voltage from dropping below the voltage floor, large capacitor banks 216 can be charged that otherwise would draw such a high current during charging that they would force the electronics subassembly 200 to reset. In conventional systems this can be a problem particularly during system start-up or initialization when the capacitor bank 216 is being charged for the first time, and consequently draws relatively high current.

[0080] Although not shown in Figure 7, the controller 206 may also be configured to monitor battery voltages and react to the battery voltages dropping below one or both of the voltage floor or the reset voltage, as described above in respect of Figures 5 and 6.

[0081] In the foregoing embodiments, one exemplary type of capacitor that may be used in the capacitor bank 216 is a high temperature electrolytic capacitor. This type of capacitor has relatively good reliability and, if it does fail, tends to fail as an open circuit instead of a short circuit.

[0082] The controller used in the foregoing embodiments may be, for example, a microprocessor, microcontroller, digital signal processor, programmable logic controller, field programmable gate array, or an application-specific integrated circuit. Examples of the computer readable medium are non-transitory and include disc-based media such as CD-ROMs and DVDs, magnetic media such as hard drives and other forms of magnetic disk storage, semiconductor based media such as flash media, random access memory, and read only memory.

[0083] It is contemplated that any part of any aspect or embodiment discussed in this specification can be implemented or combined with any part of any other aspect or embodiment discussed in this specification.

[0084] For the sake of convenience, the exemplary embodiments above are described as various interconnected functional blocks. This is not necessary, however, and there may be cases where these functional blocks are equivalently aggregated into a single logic device, program or operation with unclear boundaries. In any event, the functional blocks can be implemented by themselves, or in combination with other pieces of hardware or software.

[0085] While particular embodiments have been described in the foregoing, it is to be understood that other embodiments are possible and are intended to be included herein. It will be clear to any person skilled in the art that modifications of and adjustments to the foregoing embodiments, not shown, are possible.

**CLAIMS**

What is claimed is:

1. A system for charging a capacitor used to power measurement-while-drilling equipment, the system comprising:
  - (a) a power bus, wherein the capacitor is electrically connected to the power bus;
  - (b) a first pair of battery terminals for connecting to a first battery;
  - (c) switching circuitry operable to electrically connect the power bus to and to disconnect the power bus from the first pair of battery terminals; and
  - (d) a controller, operable to control the switching circuitry, and configured to charge the capacitor by intermittently connecting the power bus to and disconnecting the power bus from the first pair of battery terminals, wherein durations for which the power bus is connected to the first pair of battery terminals are selected such that battery voltage remains above a first minimum operating voltage while the capacitor is being charged.
2. The system of claim 1 wherein the controller is configured to charge the capacitor by applying a first pulse width modulated control signal to control the switching circuitry, wherein the first pulse width modulated control signal has a duty cycle selected such that the voltage of the first battery remains above the first minimum operating voltage while the capacitor is being charged.
3. The system of claim 2 wherein the minimum operating voltage is a reset voltage, and wherein the controller is further configured to reset the system if the voltage of the first battery drops below the reset voltage.
4. The system of claim 3, wherein the controller is further configured to disconnect the first pair of battery terminals from the power bus when the voltage of the first

- battery drops below a voltage floor, wherein the voltage floor is above the reset voltage.
5. The system of claim 4 wherein the voltage floor is approximately 50% of the maximum voltage of the first battery.
  6. The system of any one of claims 2 to 5 wherein the first pulse width modulated control signal is configured to cause the first battery to be connected to the power bus for approximately 1.5 ms and disconnected from the power bus for approximately 2 ms for each charging cycle.
  7. The system of any one of claims 2 to 6 wherein the controller is configured to charge the capacitor for a charging period.
  8. The system of claim 7 wherein the controller is further configured to electrically connect the first pair of battery terminals to the capacitor following the charging period and to subsequently indefinitely keep the first pair of battery terminals and the capacitor electrically connected.
  9. The system of any one of claims 2 to 8 further comprising a first voltmeter, communicative with the controller, and electrically connected in parallel to the first pair of battery terminals such that the voltage of the first battery can be determined.
  10. The system of any one of claims 2 to 9 further comprising a first ammeter electrically connected in series to the first pair of battery terminals such that current flowing out of the first battery can be determined.
  11. The system of claim 2 further comprising a second pair of battery terminals for connecting to a second battery and electrically connected in parallel to the first pair of battery terminals, wherein the switching circuitry is operable to electrically connect the power bus to and to disconnect the power bus from the second pair of battery terminals independently from the first pair of battery terminals and wherein the controller is further configured to apply a second pulse width

modulated control signal to the switching circuitry such that the system alternates between:

- (a) only charging the capacitor using the first battery by electrically connecting the first pair of battery terminals to the power bus and electrically disconnecting the second pair of battery terminals from the power bus; and
- (b) only charging the capacitor using the second battery by electrically connecting the second pair of battery terminals to the power bus and electrically disconnecting the first pair of battery terminals from the power bus,

wherein the second pulse width modulated control signal has a duty cycle selected such that the voltage of the second battery remains above a second minimum operating voltage while the capacitor is being charged.

12. The system of claim 11 wherein the first and second minimum operating voltages are identical.
13. The system of claim 12 wherein the minimum operating voltages are a reset voltage, and wherein the controller is further configured to reset the system if the voltage of the first or second batteries drops below the reset voltage.
14. The system of claim 13, wherein the controller is further configured to disconnect the first pair of battery terminals from the power bus when the voltage of the first battery drops below a first voltage floor and to disconnect the second pair of battery terminals from the power bus when the voltage of the second battery drops below a second voltage floor, wherein the voltage floors are above the reset voltage.
15. The system of claim 14 wherein the first and second voltage floors are identical.

16. The system of claim 15 wherein the voltage floors are approximately 50% of the maximum voltage of the first battery.
17. The system of any one of claims 11 to 16 wherein the pulse width modulated control signals are configured to cause the capacitor to always be charged by at least one of the batteries if the voltage of the capacitor is lower than the voltage of at least one of the batteries.
18. The system of any one of claims 11 to 16 wherein the pulse width modulated control signals are configured to cause each of the batteries to be connected to the power bus for approximately 1.5 ms for each charging cycle and disconnected from the power bus for approximately 2.0 ms for each charging cycle.
19. The system of any one of claims 11 to 18 wherein the controller is configured to charge the capacitor for a charging period.
20. The system of claim 19 wherein the controller is further configured to electrically connect at least one of the pairs of battery terminals to the capacitor following the charging period and to subsequently indefinitely keep the at least one of the pairs of battery terminals and the capacitor electrically connected.
21. The system of any one of claims 11 to 20 further comprising first and second voltmeters, communicative with the controller, and electrically connected in parallel to the first and second pairs of battery terminals, respectively, such that the voltages of the first and second batteries can be determined.
22. The system of any one of claims 11 to 21 further comprising first and second ammeters electrically connected in series to the first and second pairs of battery terminals, respectively, such that current flowing out of the first and second batteries can be determined.
23. A method for charging a capacitor used to power measurement-while-drilling equipment, the method comprising intermittently electrically connecting the

- capacitor to and disconnecting the capacitor from a first battery, wherein durations for which the capacitor and the first battery are electrically connected are selected such that first battery voltage remains above a first minimum operating voltage while the capacitor is being charged.
24. The method of claim 23 wherein a first pulse width modulated control signal is used to intermittently electrically connect the capacitor to and disconnect the capacitor from a first battery, wherein the first pulse width modulated control signal has a duty cycle selected such that the voltage of the first battery remains above the first minimum operating voltage while the capacitor is being charged.
25. The method of claim 24 wherein the minimum operating voltage is a reset voltage, and further comprising:
- (a) monitoring the voltage of the first battery; and
  - (b) resetting circuitry used to charge the capacitor if the voltage of the first battery drops below the reset voltage.
26. The method of claim 25 further comprising disconnecting the first pair of battery terminals from the capacitor when the voltage of the first battery drops below a voltage floor, wherein the voltage floor is above the reset voltage.
27. The method of claim 26 wherein the voltage floor is approximately 50% of the maximum voltage of the first battery.
28. The method of any one of claims 22 to 27 wherein the first pulse width modulated control signal is configured to cause the first battery to be connected to the power bus for approximately 1.5 ms and to be disconnected from the power bus for approximately 2 ms for each charging cycle.
29. The method of any one of claims 22 to 28 wherein the first battery charges the capacitor for a charging period.

30. The method of claim 29 further comprising, following the charging period, electrically connecting the first pair of battery terminals to the capacitor and subsequently indefinitely keeping the first pair of battery terminals and the capacitor electrically connected.
31. The method of claim 24 further comprising applying a second pulse width modulated control signal to intermittently electrically connect the capacitor to and disconnect the capacitor from a second battery, wherein the pulse width modulated control signals are configured to alternate between:
- (a) only charging the capacitor using the first battery by electrically connecting the first pair of battery terminals to the power bus and electrically disconnecting the second pair of battery terminals from the power bus; and
  - (b) only charging the capacitor using the second battery by electrically connecting the second pair of battery terminals to the power bus and electrically disconnecting the first pair of battery terminals from the power bus,
- wherein the second pulse width modulated control signal has a duty cycle selected such that the voltage of the second battery remains above a second minimum operating voltage while the capacitor is being charged.
32. The method of claim 31 wherein the first and second minimum operating voltages are identical.
33. The method of claim 32 wherein the minimum operating voltages are a reset voltage and further comprising:
- (a) monitoring the voltages of the first and second batteries; and
  - (b) resetting circuitry used to charge the capacitor if the voltage of the first battery drops below the reset voltage.

34. The method of claim 33 further comprising disconnecting the first pair of battery terminals from the power bus when the voltage of the first battery drops below a first voltage floor and disconnecting the second pair of battery terminals from the power bus when the voltage of the second battery drops below a second voltage floor, wherein the voltage floors are above the reset voltage.
35. The method of claim 34 wherein the first and second voltage floors are identical.
36. The method of claim 35 wherein the voltages of the first and second batteries are identical and the voltage floors are approximately 50% of the maximum voltage of the first battery.
37. The method of any one of claims 31 to 36 wherein the pulse width modulated control signals are configured to cause the capacitor to always be charged by at least one of the batteries if the voltage of the capacitor is lower than the voltage of at least one of the batteries.
38. The method of any one of claims 31 to 37 wherein the pulse width modulated control signals are configured to cause each of the batteries to alternately be connected to the power bus for approximately 1.5 ms for each charging cycle.
39. The method of any one of claims 31 to 38 wherein the capacitor is charged for a charging period.
40. The method of claim 39 further comprising electrically connecting at least one of the pairs of battery terminals to the capacitor following the charging period and subsequently indefinitely keeping the at least one of the pairs of battery terminals and the capacitor electrically connected.
41. A non-transitory computer readable medium having encoded thereon statements and instructions configured to cause a controller to perform the method of any one of claims 23 to 40.

**AMENDED CLAIMS**  
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**CLAIMS**

What is claimed is:

1. A system for charging a capacitor used to power measurement-while-drilling equipment, the system comprising:
  - (a) a power bus, wherein the capacitor is electrically connected to the power bus;
  - (b) a first pair of battery terminals for connecting to a first battery;
  - (c) switching circuitry operable to electrically connect the power bus to and to disconnect the power bus from the first pair of battery terminals; and
  - (d) a controller, operable to control the switching circuitry, and configured to charge the capacitor by intermittently connecting the power bus to and disconnecting the power bus from the first pair of battery terminals, wherein durations for which the power bus is connected to the first pair of battery terminals are selected such that battery voltage remains above a first minimum operating voltage while the capacitor is being charged.
2. The system of claim 1 wherein the controller is configured to charge the capacitor by applying a first pulse width modulated control signal to control the switching circuitry, wherein the first pulse width modulated control signal has a duty cycle selected such that the voltage of the first battery remains above the first minimum operating voltage while the capacitor is being charged.
3. The system of claim 2 wherein the minimum operating voltage is a reset voltage, and wherein the controller is further configured to reset the system if the voltage of the first battery drops below the reset voltage.
4. The system of claim 3, wherein the controller is further configured to disconnect the first pair of battery terminals from the power bus when the voltage of the first

battery drops below a voltage floor, wherein the voltage floor is above the reset voltage.

5. The system of claim 4 wherein the voltage floor is approximately 50% of the maximum voltage of the first battery.
6. The system of any one of claims 2 to 5 wherein the first pulse width modulated control signal is configured to cause the first battery to be connected to the power bus for approximately 1.5 ms and disconnected from the power bus for approximately 2 ms for each charging cycle.
7. The system of any one of claims 2 to 6 wherein the controller is configured to charge the capacitor for a charging period.
8. The system of claim 7 wherein the controller is further configured to electrically connect the first pair of battery terminals to the capacitor following the charging period and to subsequently indefinitely keep the first pair of battery terminals and the capacitor electrically connected.
9. The system of any one of claims 2 to 8 further comprising a first voltmeter, communicative with the controller, and electrically connected in parallel to the first pair of battery terminals such that the voltage of the first battery can be determined.
10. The system of any one of claims 2 to 9 further comprising a first ammeter electrically connected in series to the first pair of battery terminals such that current flowing out of the first battery can be determined.
11. The system of claim 2 further comprising a second pair of battery terminals for connecting to a second battery and electrically connected in parallel to the first pair of battery terminals, wherein the switching circuitry is operable to electrically connect the power bus to and to disconnect the power bus from the second pair of battery terminals independently from the first pair of battery terminals and wherein the controller is further configured to apply a second pulse width

modulated control signal to the switching circuitry such that the system alternates between:

- (a) only charging the capacitor using the first battery by electrically connecting the first pair of battery terminals to the power bus and electrically disconnecting the second pair of battery terminals from the power bus; and
- (b) only charging the capacitor using the second battery by electrically connecting the second pair of battery terminals to the power bus and electrically disconnecting the first pair of battery terminals from the power bus,

wherein the second pulse width modulated control signal has a duty cycle selected such that the voltage of the second battery remains above a second minimum operating voltage while the capacitor is being charged.

- 12. The system of claim 11 wherein the first and second minimum operating voltages are identical.
- 13. The system of claim 12 wherein the minimum operating voltages are a reset voltage, and wherein the controller is further configured to reset the system if the voltage of the first or second batteries drops below the reset voltage.
- 14. The system of claim 13, wherein the controller is further configured to disconnect the first pair of battery terminals from the power bus when the voltage of the first battery drops below a first voltage floor and to disconnect the second pair of battery terminals from the power bus when the voltage of the second battery drops below a second voltage floor, wherein the voltage floors are above the reset voltage.
- 15. The system of claim 14 wherein the first and second voltage floors are identical.

16. The system of claim 15 wherein the voltage floors are approximately 50% of the maximum voltage of the first battery.
17. The system of any one of claims 11 to 16 wherein the pulse width modulated control signals are configured to cause the capacitor to always be charged by at least one of the batteries if the voltage of the capacitor is lower than the voltage of at least one of the batteries.
18. The system of any one of claims 11 to 16 wherein the pulse width modulated control signals are configured to cause each of the batteries to be connected to the power bus for approximately 1.5 ms for each charging cycle and disconnected from the power bus for approximately 2.0 ms for each charging cycle.
19. The system of any one of claims 11 to 18 wherein the controller is configured to charge the capacitor for a charging period.
20. The system of claim 19 wherein the controller is further configured to electrically connect at least one of the pairs of battery terminals to the capacitor following the charging period and to subsequently indefinitely keep the at least one of the pairs of battery terminals and the capacitor electrically connected.
21. The system of any one of claims 11 to 20 further comprising first and second voltmeters, communicative with the controller, and electrically connected in parallel to the first and second pairs of battery terminals, respectively, such that the voltages of the first and second batteries can be determined.
22. The system of any one of claims 11 to 21 further comprising first and second ammeters electrically connected in series to the first and second pairs of battery terminals, respectively, such that current flowing out of the first and second batteries can be determined.
23. A method for charging a capacitor used to power measurement-while-drilling equipment, the method comprising intermittently electrically connecting the

- capacitor to and disconnecting the capacitor from a first battery, wherein durations for which the capacitor and the first battery are electrically connected are selected such that first battery voltage remains above a first minimum operating voltage while the capacitor is being charged.
24. The method of claim 23 wherein a first pulse width modulated control signal is used to intermittently electrically connect the capacitor to and disconnect the capacitor from a first battery, wherein the first pulse width modulated control signal has a duty cycle selected such that the voltage of the first battery remains above the first minimum operating voltage while the capacitor is being charged.
25. The method of claim 24 wherein the minimum operating voltage is a reset voltage, and further comprising:
- (a) monitoring the voltage of the first battery; and
  - (b) resetting circuitry used to charge the capacitor if the voltage of the first battery drops below the reset voltage.
26. The method of claim 25 further comprising disconnecting the first pair of battery terminals from the capacitor when the voltage of the first battery drops below a voltage floor, wherein the voltage floor is above the reset voltage.
27. The method of claim 26 wherein the voltage floor is approximately 50% of the maximum voltage of the first battery.
28. The method of any one of claims 23 to 27 wherein the first pulse width modulated control signal is configured to cause the first battery to be connected to the power bus for approximately 1.5 ms and to be disconnected from the power bus for approximately 2 ms for each charging cycle.
29. The method of any one of claims 23 to 28 wherein the first battery charges the capacitor for a charging period.

30. The method of claim 29 further comprising, following the charging period, electrically connecting the first pair of battery terminals to the capacitor and subsequently indefinitely keeping the first pair of battery terminals and the capacitor electrically connected.
31. The method of claim 24 further comprising applying a second pulse width modulated control signal to intermittently electrically connect the capacitor to and disconnect the capacitor from a second battery, wherein the pulse width modulated control signals are configured to alternate between:
- (a) only charging the capacitor using the first battery by electrically connecting the first pair of battery terminals to the power bus and electrically disconnecting the second pair of battery terminals from the power bus; and
  - (b) only charging the capacitor using the second battery by electrically connecting the second pair of battery terminals to the power bus and electrically disconnecting the first pair of battery terminals from the power bus,
- wherein the second pulse width modulated control signal has a duty cycle selected such that the voltage of the second battery remains above a second minimum operating voltage while the capacitor is being charged.
32. The method of claim 31 wherein the first and second minimum operating voltages are identical.
33. The method of claim 32 wherein the minimum operating voltages are a reset voltage and further comprising:
- (a) monitoring the voltages of the first and second batteries; and
  - (b) resetting circuitry used to charge the capacitor if the voltage of the first battery drops below the reset voltage.

34. The method of claim 33 further comprising disconnecting the first pair of battery terminals from the power bus when the voltage of the first battery drops below a first voltage floor and disconnecting the second pair of battery terminals from the power bus when the voltage of the second battery drops below a second voltage floor, wherein the voltage floors are above the reset voltage.
35. The method of claim 34 wherein the first and second voltage floors are identical.
36. The method of claim 35 wherein the voltages of the first and second batteries are identical and the voltage floors are approximately 50% of the maximum voltage of the first battery.
37. The method of any one of claims 31 to 36 wherein the pulse width modulated control signals are configured to cause the capacitor to always be charged by at least one of the batteries if the voltage of the capacitor is lower than the voltage of at least one of the batteries.
38. The method of any one of claims 31 to 37 wherein the pulse width modulated control signals are configured to cause each of the batteries to alternately be connected to the power bus for approximately 1.5 ms for each charging cycle.
39. The method of any one of claims 31 to 38 wherein the capacitor is charged for a charging period.
40. The method of claim 39 further comprising electrically connecting at least one of the pairs of battery terminals to the capacitor following the charging period and subsequently indefinitely keeping the at least one of the pairs of battery terminals and the capacitor electrically connected.
41. A non-transitory computer readable medium having encoded thereon statements and instructions configured to cause a controller to perform the method of any one of claims 23 to 40.

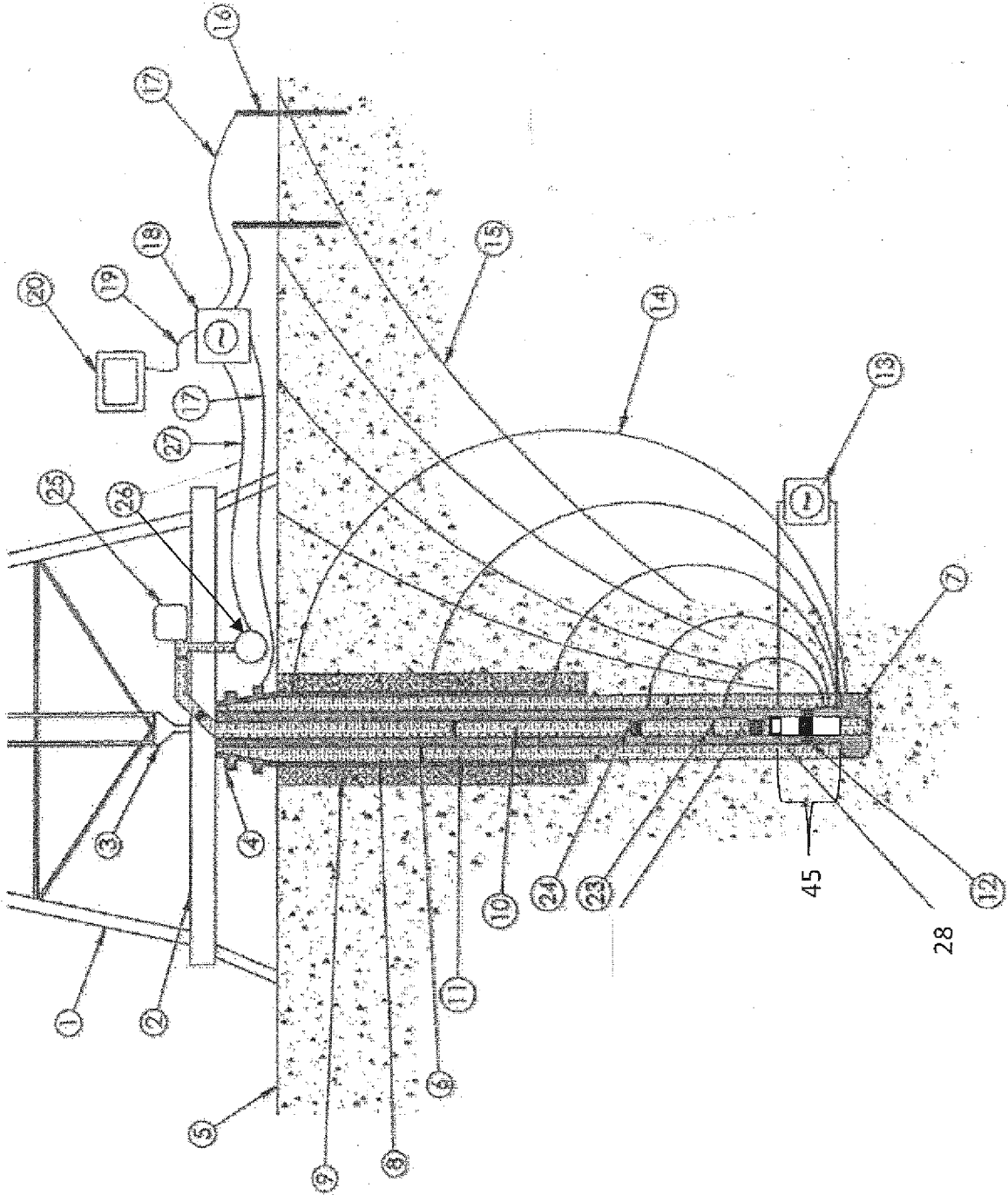


FIG. 1



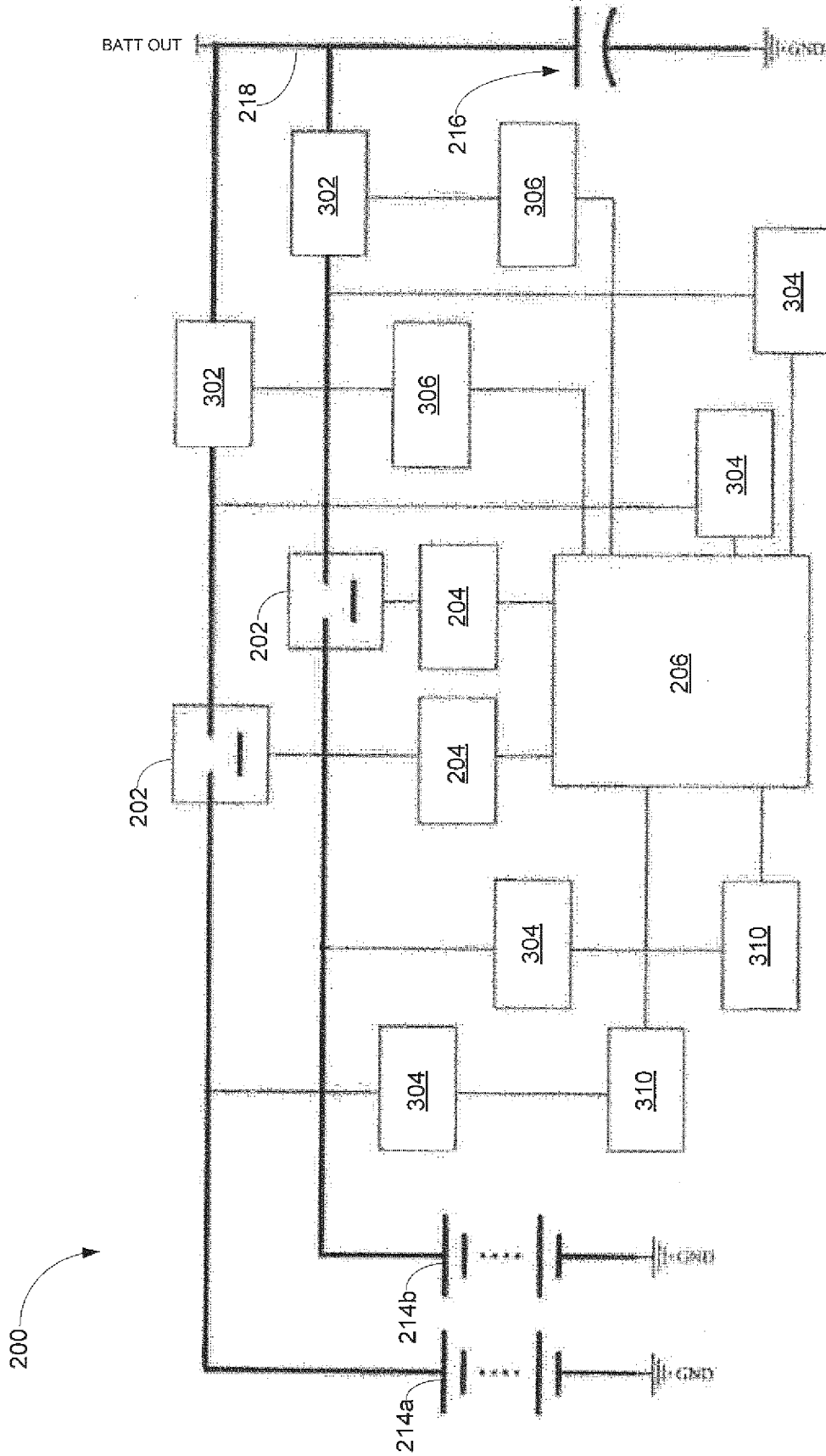


FIG. 3

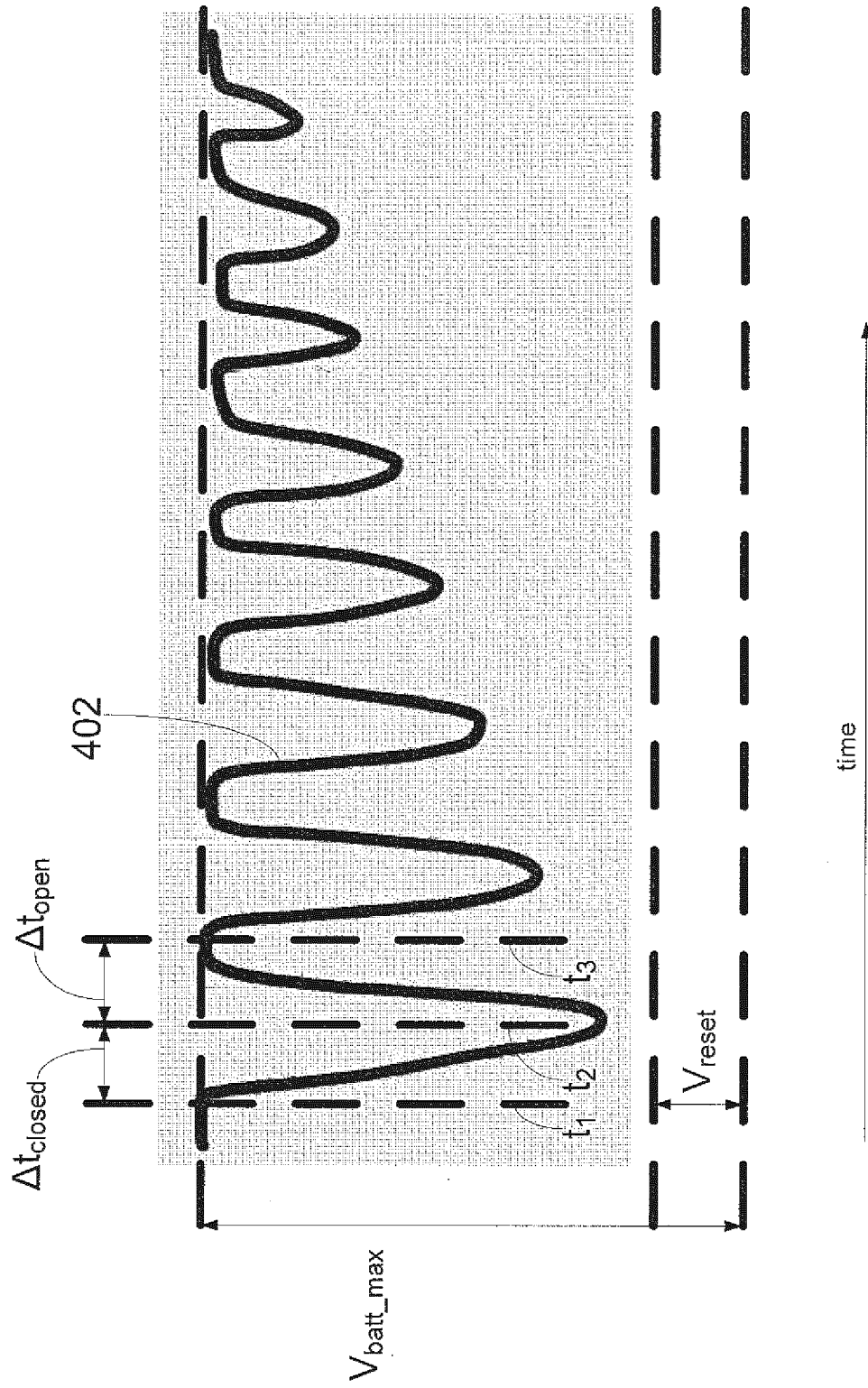


FIG. 4

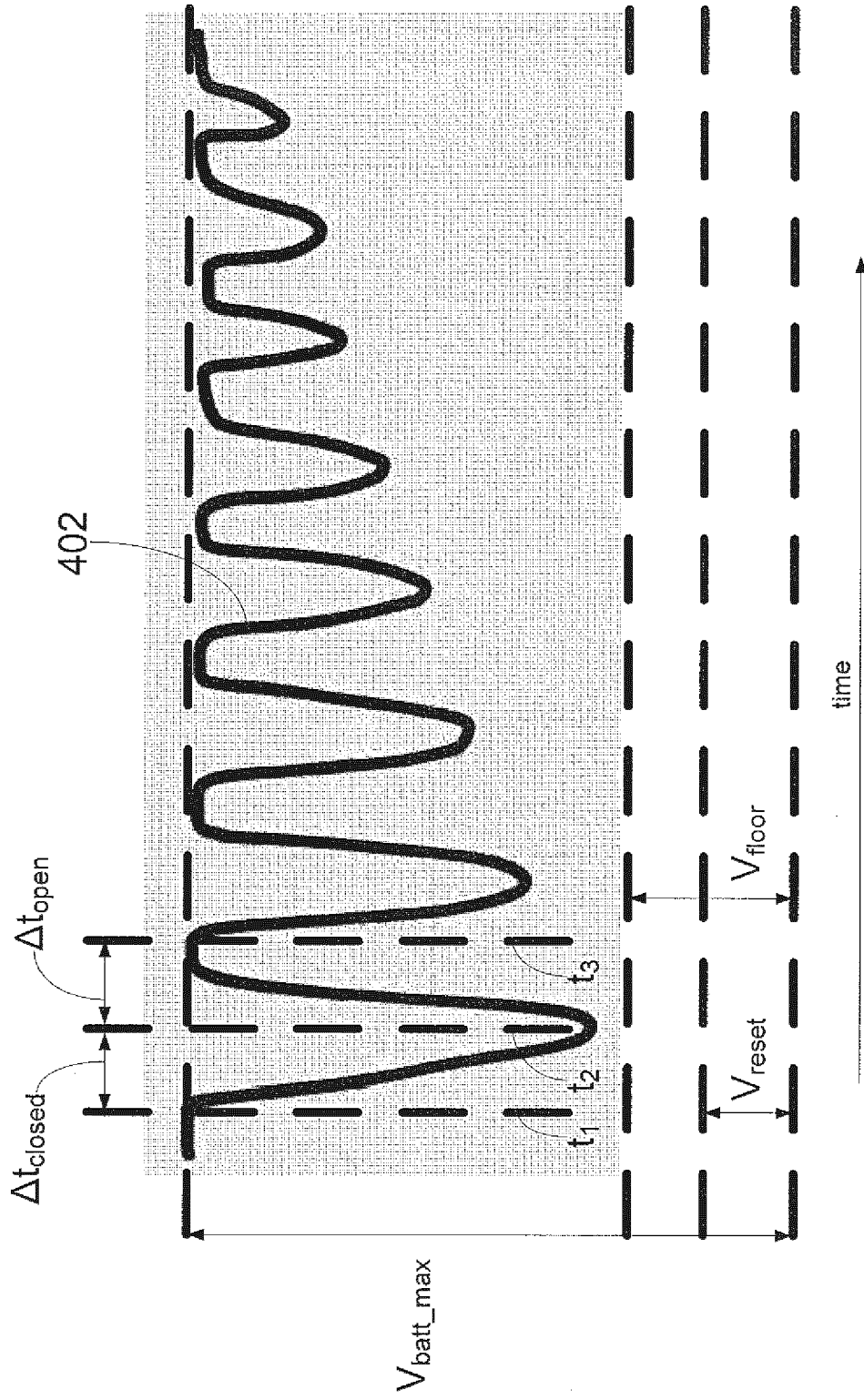


FIG. 5

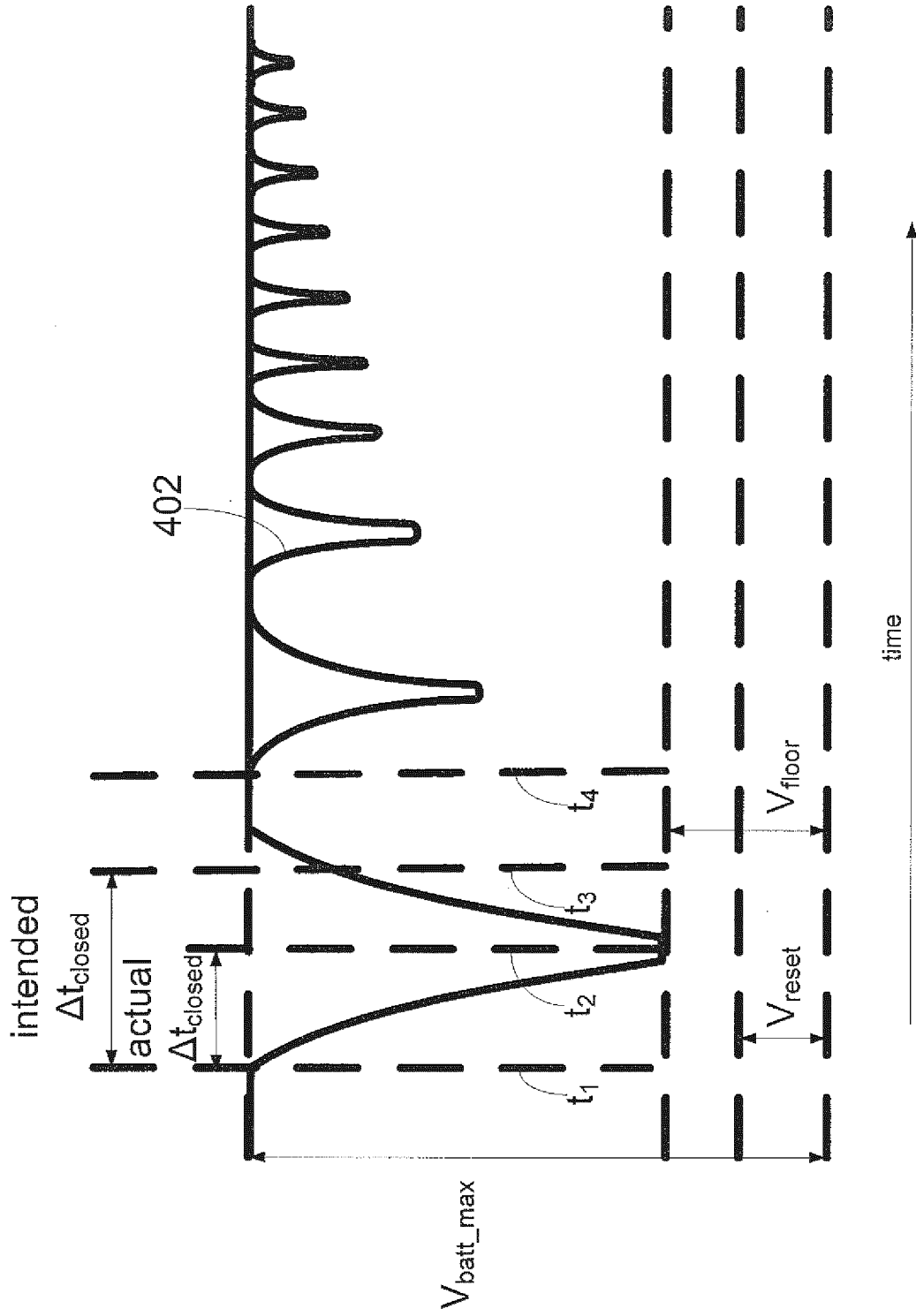


FIG. 6

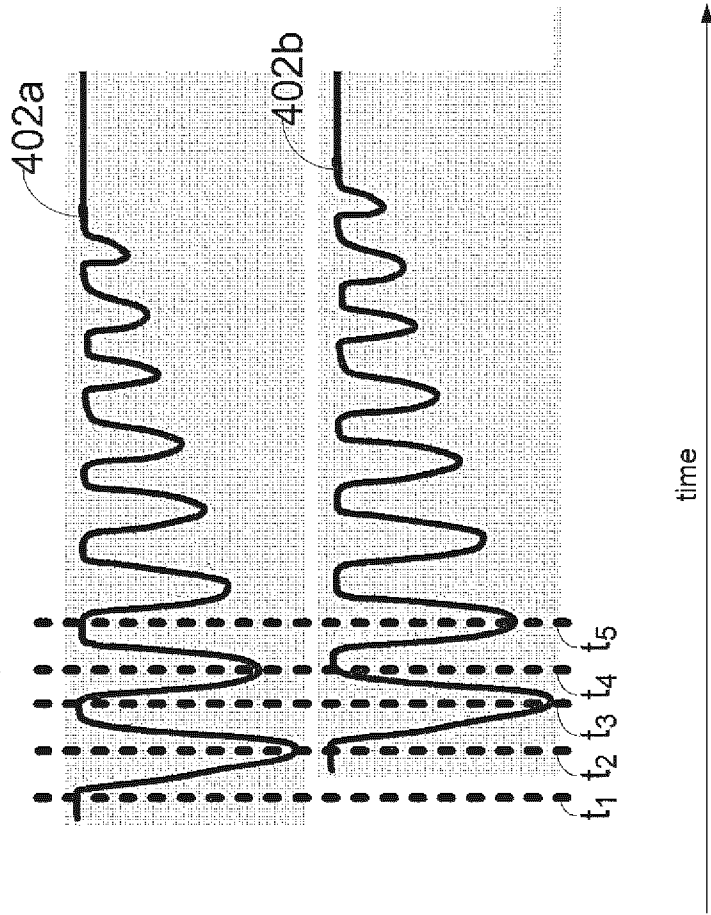


FIG. 7

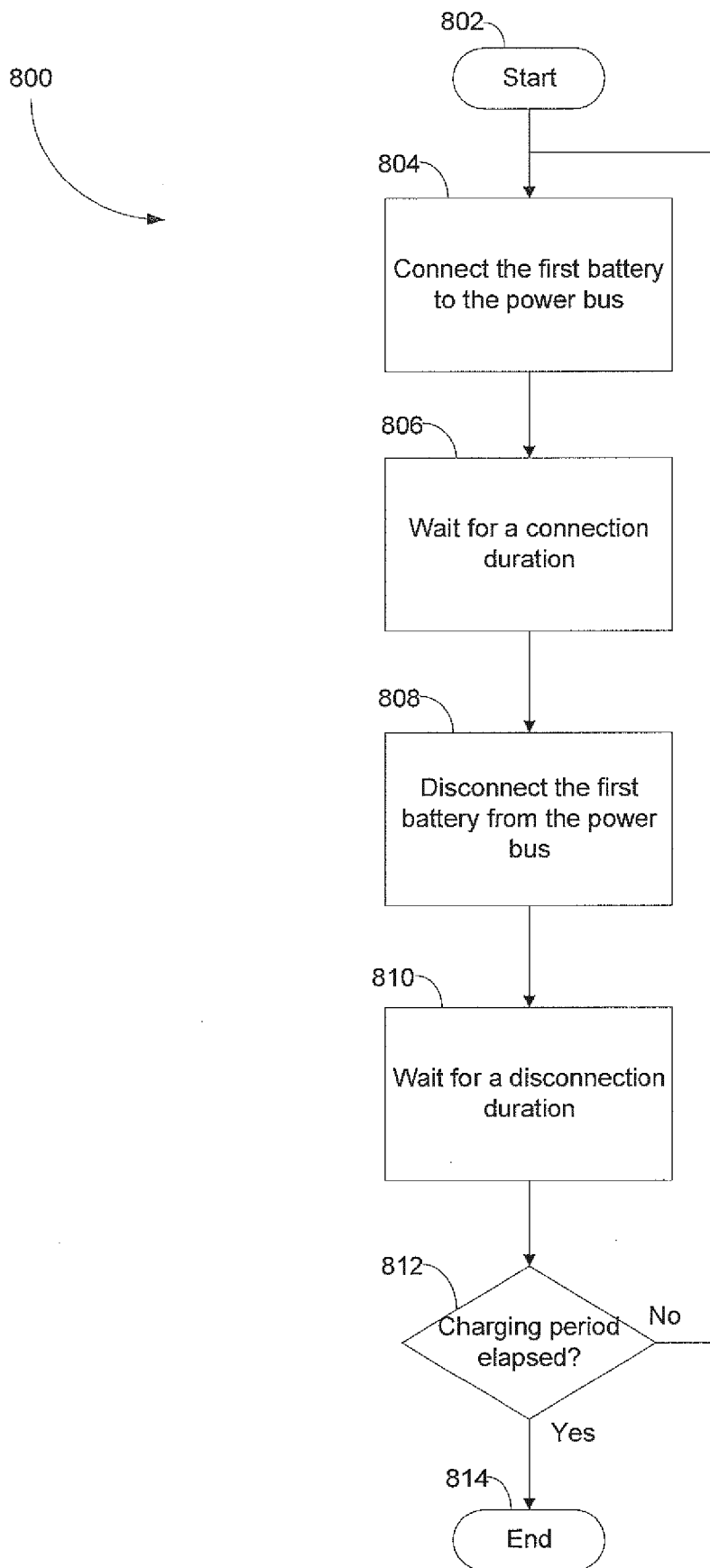


FIG. 8

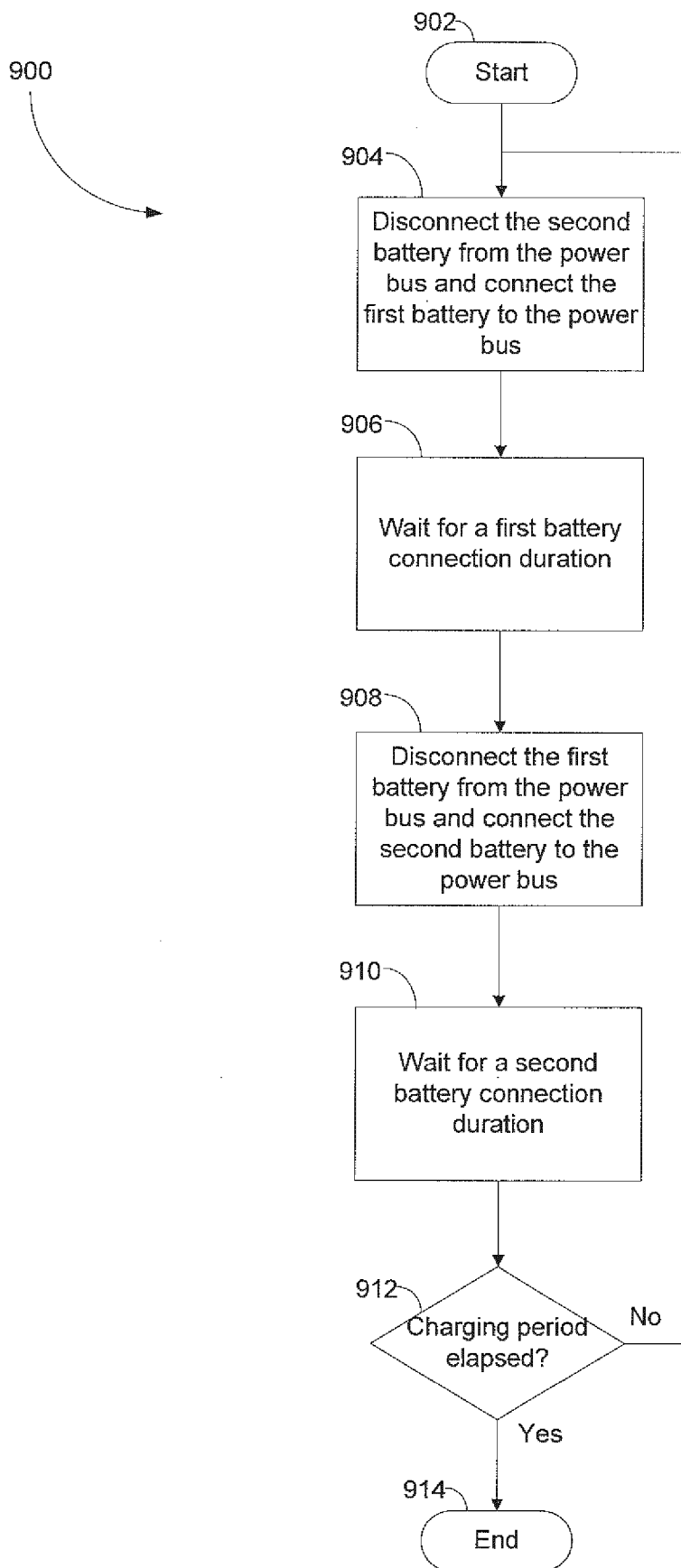


FIG. 9

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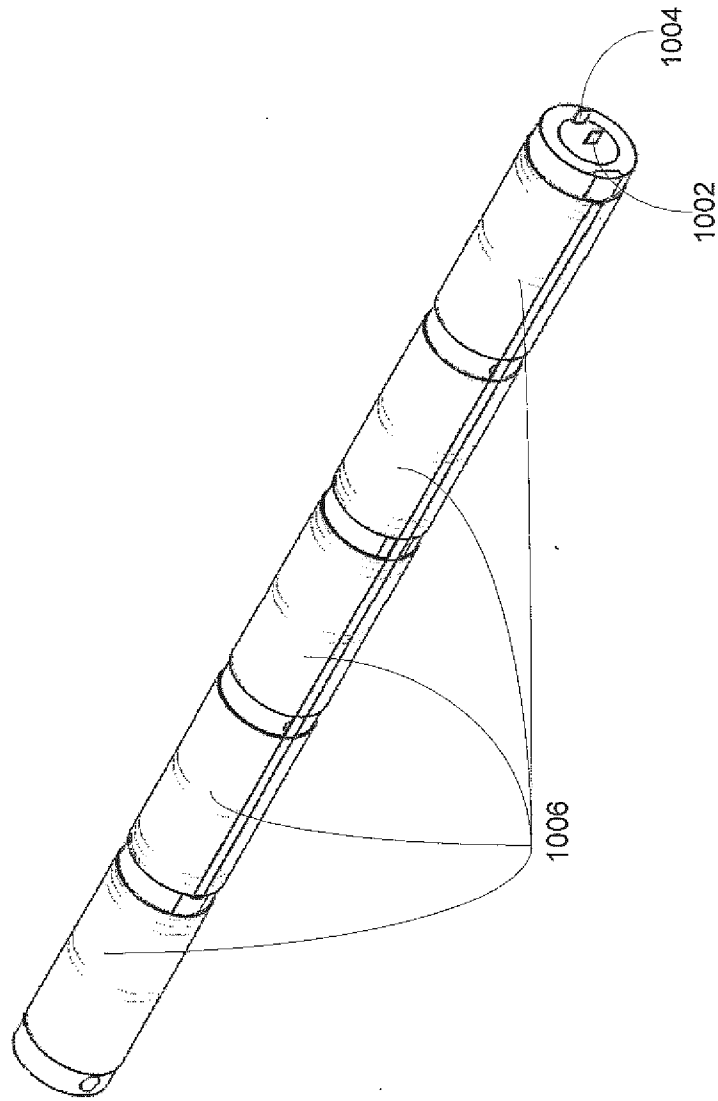


FIG. 10

## INTERNATIONAL SEARCH REPORT

International application No.

**PCT/CA2014/050197**

A. CLASSIFICATION OF SUBJECT MATTER  
 IPC: *H02J 15/00* (2006.01), *H02J 7/00* (2006.01), *E21B 44/00* (2006.01)

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
*H02J 15/00* (2006.01), *H02J 7/00* (2006.01), *E21B 44/00* (2006.01)

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic database(s) consulted during the international search (name of database(s) and, where practicable, search terms used)  
 Databases searched: Canadian Patent Database, TotalPatent, European Patent Database (EPOQUE), Abstracts of Japan, US Patent Database, WIPO-PCT Publications (Full text) and IEEE publications:  
 Keywords: battery, capacitor charging, controller, switch!, operating voltage, disconnect, drilling operation

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP2006246675A (Masaru), 14 September 2006 (14-09-2006) -see abstract; -see figs. 1-8; -see whole document.	1 - 41
A	JP2010246198A (Toru et al.), 28 October 2010 (28-10-2010) -see abstract; -see figs. 1-7; -see whole document.	1 - 41
A	US7,786,620 (Vuk et al.), 31 August 2010 (31-08-2010) -see abstract; -see figs. 1-9; -see whole document.	1 - 41

Further documents are listed in the continuation of Box C.

See patent family annex.

* "A" "E" "L" "O" "P"	Special categories of cited documents: document defining the general state of the art which is not considered to be of particular relevance earlier application or patent but published on or after the international filing date document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) document referring to an oral disclosure, use, exhibition or other means document published prior to the international filing date but later than the priority date claimed	"T" "X" "Y" "&"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art document member of the same patent family
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Date of the actual completion of the international search  
 21 May 2014 (21-05-2014)

Date of mailing of the international search report  
 09 June 2014 (09-06-2014)

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**INTERNATIONAL SEARCH REPORT**  
Information on patent family members

International application No.  
**PCT/CA2014/050197**

Patent Document Cited in Search Report	Publication Date	Patent Family Member(s)	Publication Date
JP2006246675A	14 September 2006 (14-09-2006)	None	
JP2010246198A	28 October 2010 (28-10-2010)	None	
US7,786,620	31 August 2010 (31-08-2010)	US2009206657A1 EP2091055A2	20 August 2009(20-08-2009) 19 August 2009 19-08-2009)