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Declarations under Rule 4.17:

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(54) Title: APPARATUS AND METHODS FOR ADAPTIVE THREAD SCHEDULING ON ASYMMETRIC MULTIPROCESSOR

(57) Abstract: Techniques for adaptive thread scheduling on a plurality of cores for reducing system energy are described. In one embodiment, a thread scheduler receives leakage current information associated with the plurality of cores. The leakage current information is employed to schedule a thread on one of the plurality of cores to reduce system energy usage. On chip calibration of the sensors is also described.

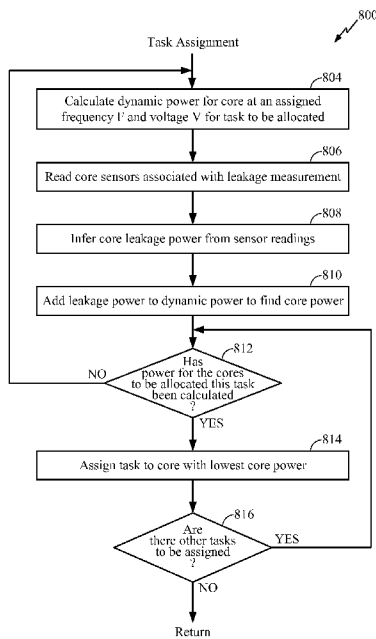


FIG. 8

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- *as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))*
  - *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))*
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**INTERNATIONAL SEARCH REPORT**

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<b>A. CLASSIFICATION OF SUBJECT MATTER</b> INV. G06F1/32                      G06F9/50 ADD.		
According to International Patent Classification (IPC) or to both national classification and IPC		
<b>B. FIELDS SEARCHED</b> Minimum documentation searched (classification system followed by classification symbols) G06F G01R H03K		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal		
<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
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A	US 2006/255828 A1 (FURUKAWA YASUO [JP]) 16 November 2006 (2006-11-16) figures 1,5	1,2,11, 12,14,15
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<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input checked="" type="checkbox"/> See patent family annex.		
* Special categories of cited documents :		
"A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed		"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. "&" document member of the same patent family
Date of the actual completion of the international search  <p align="center"><b>3 November 2010</b></p>		Date of mailing of the international search report  <p align="center"><b>09/12/2010</b></p>
Name and mailing address of the ISA/ European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016		Authorized officer  <p align="center"><b>Leineweber, Hubert</b></p>

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PCT/US2009/067654

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A	Lawrence T. Clark: "Reverse-Body Bias and Supply Collapse for Low Effective Standby Power" IEEE Transactions on Very Large Scale Integration (VLSI) Systems vol. 12, no. 9, 9 September 2004 (2004-09-09), pages 947-955, XP002608019 Retrieved from the Internet: URL: <a href="http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&amp;arnumber=1327631">http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&amp;arnumber=1327631</a> [retrieved on 2010-11-01] page 947	3,4,16

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