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(54) **Title:** MULTI-PIXEL ADDRESSING METHOD FOR VIDEO DISPLAY DRIVERS

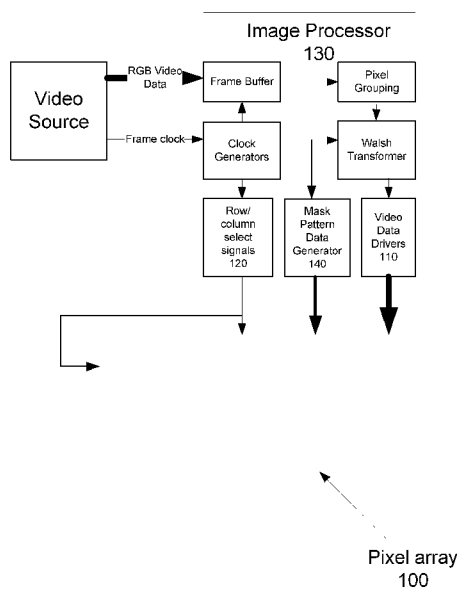


FIG. 6

(57) **Abstract:** A video display system is described which is formed by an array of pixels comprised of fast responding light elements, row select and column select switches and pixel data drivers, and a computation subsystem which generates the control signals for the select lines and the video data. The overall system reconstructs the intended image or video to be displayed through successively displaying subframes of images corresponding to orthogonal image basis function components of the original image acting on a grouping of pixels selected using multiple row and column lines. The resultant system is an architecture which enables one to implement certain video decompression techniques directly on the light elements, as opposed to implementing these techniques in digital processing, and can have a considerably reduced raw video data requirement than a system in which pixels are addressed individually, and enables higher dynamic range to be achieved with similar digital-analog-converter specifications. Embodiments with LED based displays are described herein.



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## MULTI-PIXEL ADDRESSING METHOD FOR VIDEO DISPLAY DRIVERS

### CROSS-REFERENCE TO RELATED APPLICATION

This application claims the benefit of U.S. Provisional Patent Application No. 61/157,698 filed March 5, 2009.

### BACKGROUND OF THE INVENTION

#### 1. Field of the Invention

This invention relates to image and video displays, more particularly flat panel displays used as still image and/or video monitors, and methods of generating and driving image and video data onto such display devices.

#### 2. Prior Art

Flat panel displays such as plasma, liquid crystal display (LCD), and light-emitting-diode (LED) displays generally use a pixel addressing scheme in which the pixels are addressed individually through column and row select signals. In general, for M by N pixels – or picture elements - arranged as M rows and N columns, we will have M row select lines and N data lines (see FIGURE 1). For each frame, video data is loaded by applying a row-select signal to a particular row, then scanning the row column by column until the end is reached. In common LCD and LED based embodiments, the video data is written to each pixel in that row using a single or multiple data source demultiplexing a digital-analog converter output to the N columns. Each pixel is loaded with the required pixel voltage or pixel current information. Upon reaching the end of a row, the row-select signal is deselected and another row is selected in a progressive scan mode, or an interlaced scan mode. In a general active-matrix type LCD or LED embodiment, the video information is a voltage stored in a capacitor unique to the particular pixel (see FIGURE 2). When the row and column signals de-select the pixel, the image information is retained on the capacitor. In contrast, in a passive-matrix type LCD embodiment, rows and columns are arranged as stripes of electrodes making up the top and bottom metal planes oriented in a perpendicular manner to each other (see FIGURE 3). Single or multiple row and column lines are selected

with the crossing point or points defining the pixels which have the instantaneous video information. In such a case, either the row or column signal will have a voltage applied which is proportional to the pixel information. In a light-emitting-diode display type embodiment in the passive matrix approach, the information is an instantaneous current passing through the pixel LED which results in the emission of light proportional to the applied current, or, in embodiments using fixed current sources, proportional to application time – which is also known as pulse width modulation. In all these display types mentioned, the amount of data required to drive the screen pixels is substantial. The total information conveyed to the display arrangement per video frame is then given as  $M \times N \times 3 \times \text{bit-width}$ , where the factor 3 comes from the three basic colors constituting the image, i.e. red, green and blue, and the bit-width is determined from the maximum resolution of the pixel value. Most common pixel value resolution used for commercial display systems is 8 bits per color. For example, in a VGA resolution display, the total information needed to convey will be  $640 \times 400 \times 3 \times 8$  equal to 6 Mbits per frame of image, which is refreshed at a certain frame refresh rate. The frame refresh rate can be 24, 30, 60, etc. frames per second (fps). The faster rate capability of the screen is generally used to eliminate motion blurring which occurs in LCD type displays, in which screen refresh rates of 120 or 240 fps implementations can be found in commercial devices. For a gray-scale image, the information content is less by a factor of three since only the luminance information is used.

Video and still images are generally converted to compressed forms for storage and transmission, such as MPEG2, MPEG4, JPEG2000 etc. formats and systems. Image compression methods are based on orthogonal function decomposition of the data, data redundancy, and certain sensitivity characteristics of the human eye to spatial and temporal features. Common image compression schemes involve the use of Direct Cosine Transform as in JPEG or motion JPEG, or Discrete Walsh Transform. In addition, video compression may involve skipping certain frames and using forward or backward frame estimation, skipping color information, or chroma subsampling in a luminance-chrominance (YCrCb) representation of the image etc. A video decoder is used to convert the spatially and temporally compressed image information to row and column pixel information in the color (RGB) representation to produce the image information, which will be for example at 6 Mbits per frame as in VGA resolution displays. However, from an information content point of view, much of this video information is actually spatially redundant as the image had

originally been processed to a compressed form, or it has information content which the human eye is not sensitive to. All these techniques pertain to the display system's components in the software or digital processing domain, and the structure of the actual optical display comprised of  $M \times N$  pixels is not affected by any of the techniques used for the video format, other than the number of pixels and frame rate.

Prior art in the field does not address image compression and decompression techniques directly. Data is generally made available on a pixel-by-pixel basis, with which the video system displays at a certain refresh rate. Image and/or video compression is generally applied to the transmission, storage and image reconditioning of data for the display (as in U.S. Patent No. 6,477,279). Multiple line addressing in passive matrix displays is also an established technique (as in Lueder, E., "Liquid Crystal Displays – Addressing Schemes and Electro-Optical Effects", John Wiley & Sons 2001, pp. 176-194, or U.S. Patent No. 6,111,560, ). Time-domain Walsh function based orthogonal waveforms are applied to column and rows such that crossing points in the row and columns will generate shades of gray through amplitude modulation as desired. This is in contrast to employing two-dimensional orthogonal basis function expansions used in video and image compression. In U.S. Patent Application Publication No. 2010/0007804, an image construction based video display system is described, which uses orthogonal Walsh function based the current application, an extension of these techniques are made for application to fine-arrays of pixels, with which individual row and column control are possible, and a spatial light modulator is therefore not necessary.

### BRIEF DESCRIPTION OF THE DRAWINGS

FIG 1. depicts the pixel selection method used in active matrix flat panel displays, specifically an active matrix liquid crystal display. Each pixel is addressed through row and column select signals, with the video information applied through either one of the select signals. For an  $M \times N$  pixel system, there are  $M$  row select signals, and  $N$  data lines. The data (video information) is generated by a Digital-Analog Converter, and the voltage is stored in a capacitor for each pixel. The voltage is applied to two parallel plates composed of a transparent electrode such as ITO (Indium Tungsten Oxide).

FIG 2. shows typical active matrix pixel circuit topologies for LCD and LED based displays in which image information is retained through the use of a capacitor as a memory device when the pixel's row and column select switch signals are de-selected.

FIG 3. depicts the pixel selection method employed in passive matrix LCD displays. There are M row select signals and N data signals. Signal timing determines which location will have an instantaneous voltage applied between the two electrodes, to which the liquid crystal molecules in between will react to.

FIG 4. shows the basis functions which need to be implemented as a masking pattern for a 4 x 4 pixel grouping.

FIG 5. shows the basis functions which need to be implemented as a masking pattern for a 8 x 8 pixel grouping.

FIG 6. shows the block diagram of the video display system employing a pixel array, row/column select circuitry operating on macro-pixels, masking pattern generation block, computation device for image processing which calculates discrete Walsh transform coefficients, and timing generator blocks.

FIG 7. shows row and column select table used to generate the masking patterns for 4 x 4 pixel grouping. Note that some high order patterns can not be generated in a single select step with this type of implementation. In these cases, the second pattern is generated with the inverse of the row and column select signals, with the column video data signal staying same. If the switching is fast enough, the two patterns can be squeezed in one subframe, if not, the second pattern can either use a subframe of its own, or be displayed in the next frame.

FIG 8 shows an alternative switching structure for generating masking patterns for a 4 x 4 pixel grouping, based on a LED display architecture as shown in FIG 2. The switch states are loaded through a serial data bus and stored in local registers. At every subframe, 16 bits are loaded serially corresponding to the on or off states of the pixels. A common video data signal is then applied to the 4 x 4 pixel grouping.

FIG 9. shows example subframe patterns for three different macro-pixels exhibiting three different compression scenarios. The first macro-pixel is a lossless reconstruction of

the image. The image is reset every 16 subframe durations. The second macro-pixel employs lossy image reconstruction such that terms image coefficients higher than 2<sup>nd</sup> order for oblique spatial frequencies are neglected ( $D_{21}$ ,  $D_{12}$ ,  $D_{13}$ ,  $D_{31}$ ,  $D_{22}$ , etc.). The effective frame rate of this macro-pixel is twice the first one, as the image is reset every 8 subframe durations. The third macro-pixel employs a higher compression, and neglects all oblique spatial frequencies, exhibiting a higher effective frame rate than the other two. The order of coefficients need not be the same as each macro-pixel's pattern can be uniquely addressed, and also the phase of the pattern, depending on the  $D_{uv}$  coefficient being positive or negative, can be different. The particular reconstruction to be decided upon is determined by examining the image coefficients of the macro-pixel, and possibly previous frames to determine how fast the content is moving across the screen and the amount of resolution required for satisfactory viewing.

The present invention may have various modifications and alternative forms from the specific embodiments depicted in the drawings. These drawings do not limit the invention to the specific embodiments disclosed. The invention covers all modifications, improvements and alternative implementations which are claimed below.

### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

The invention is a display method and system which constructs an image and/or video through successively displaying image components or summations of image components at a high frame rate. The image construction uses image compression to calculate orthogonal image coefficients, and drive these coefficients as video signals to pixel arrays in time domain through the use of time-dependent spatial masking of image information within a pixel array. The purpose of the invention is to enable content driven optimization of frame rate and/or video data rate for minimizing power consumption. In each frame, the source image to be driven is first grouped together to a certain size consisting of  $n_x \times n_y$  pixels. For example, we can divide the image into rectangular groupings of  $4 \times 4$  or  $8 \times 8$  pixels,  $4 \times 1$ ,  $8 \times 1$ , or any other arbitrary group size.  $1 \times 1$  grouping case corresponds to conventional pixel-by-pixel driving, and offers no compression benefit. The grouping size is limited by the frame rate, which in turn is limited by the switching speed of the pixels and driver components described herein and the image

compression ratio. Each image grouping, or macro-pixel as will be referred from here on, is then decomposed into components proportional to certain orthogonal image basis functions. These image functions are implemented through masking the row select and column data signals of the pixels so that the desired spatial profile of the orthogonal image basis functions are achieved. The image basis functions are shown in FIG. 4 for 4 x 4 and FIG. 5 for 8 x 8 pixel groupings. These particular basis functions shown are also commonly known as Walsh functions. Other basis functions, such as Direct Cosine Transform basis functions can also be used for basis function patterns with certain provisions. For 4 x 1 or 8 x 1 grouping, the basis functions are those in the first row of each figure. In figures 4 and 5, for image compression purposes, the basis functions take on values of -1 and +1, denoted by the black and white areas. For image decompression, or construction of the image using light sources, a negative light value is not physically possible, and an implementation in which the dark areas denote a light intensity 0%, or masking of the transmission of light, and white areas denote a transmission of ideally 100% is disclosed. A method to take into account and correct the decompressed (or constructed) image when using a (0, +1) set for basis function values is described herein. For the first grouping of 4 x 4 pixels, there are 16 basis function patterns, while for the latter grouping of 8 x 8 pixels, there are 64 basis function patterns. Denote the basis functions as  $w_{uv}(x,y)$  where  $u$  and  $v$  are the basis function indices and  $x, y$  are rectangular coordinates spanning the area of the pixel grouping dimensions. Denote  $w_{uv}^*(x,y)$  as spatial functions derived from the basis functions  $w_{uv}(x,y)$  such that the function values are in the (0,1) set. Such a transformation can be easily done through a simple arithmetic operation, as  $w^* = (w + 1)/2$ . Denote  $f^c(x,y)$  as the two dimensional image information for a color component. Here, the superscript  $c$  denotes the color red, green or blue. The method is identical for gray-scale images, in which case  $f(x,y)$  would be proportional to the luminance of the image. For an image decomposition based scheme, light emission or transmission is turned off in half the pixels for non-zero spatial components of the image,  $D_{uv}w_{uv}(x,y)$ , whose coefficients  $D_{uv}$  are in general smaller than  $D_{00}$ , described in EQ. 1.

Any image can be decomposed into orthogonal components, whose coefficients are found by integrating the image data with the basis functions shown in FIG 4 and FIG 5. For a video pixel array, which is a spatially discrete function, this integration takes the form of a summation. Denote the coefficient of the image component related to the basis function



$w_{uv}(x,y)$  as  $D_{uv}$  where  $u$  and  $v$  are the basis function indices in two dimensions. Then,  $D_{uv}$  are determined from:

$$D_{uv}^c = \sum_{x=1}^{nx} \sum_{y=1}^{ny} f^c(x,y) * w_{uv}(x,y) \quad \text{EQ. 1}$$

The invention is based on the inverse transform of EQ. 1, i.e. that an image  $f(x,y)$  can be constructed as a summation of image components  $D_{uv}^c * w_{uv}(x,y)$ .

$$f^c(x,y) = \sum_{u=0}^{nx-1} \sum_{v=0}^{ny-1} D_{uv}^c * w_{uv}(x,y) \quad \text{EQ. 2}$$

The summation of the image components is performed in time domain through successively displaying patterns corresponding to the basis functions  $w_{uv}$  with a light strength proportional to coefficients  $D_{uv}$  and a certain subframe duration  $\tau_{sf}$ . Further, we transform into a basis function set  $w^*$  from  $w$ , as described below, such that the image components are positive for all  $x,y$ . The human eye would integrate the image patterns in time, and perceive a single image corresponding to  $f(x,y)$ . If the pixel electronics have a capacitor to which the pixel image data is stored, it can also be used in integrating the image pattern along with the viewer. In this case, the image is updated with each pattern, and not re-written. Since the capacitor voltage is not reset at each step, a smaller amount of charge needs to be added to the capacitor at each subframe - this will result in lowering the power consumption of the data drivers. In pulse-width-modulation (PWM) based implementations, the 'on' time of selected pixels conforming to a  $w_{uv}$  pattern is common. In essence, a single PWM generator is used for the whole group of pixels.

In orthogonal function implementations used in conventional Discrete Walsh Transform compression techniques, the basis functions  $w_{uv}(x,y)$  take on values of +1 or -1, thereby they can satisfy orthogonality properties, in which the integration over the macro-pixel region of the cross product of two different basis functions is zero. i.e.

$$\sum_{x=1}^{nx} \sum_{y=1}^{ny} w_{uv}(x,y) w_{u'v'}(x,y) = n_x n_y$$

for (u,v) equal to (u',v'), and zero when the indices do not match. When the basis functions are mapped to +1 or 0 instead of +1 or -1, as in U.S. Patent Application Publication No. 2010/0007804, this creates a non-zero integration value of the cross product of two different basis functions over the macro-pixel area. Such functions, because of their non-orthogonal nature, can not be used in deconstructing the image into components, hence the original orthogonal basis functions having values of +1 or -1 are used in determining image coefficients  $D_{uv}$  using EQ. 1. In performing an image construction using EQ. 2 in which coefficients  $D_{uv}$  are computed using orthogonal basis functions, each component of the image, given by the function  $D_{uv} * w_{uv}$  will have both positive and negative values throughout the macro-pixel, for u,v components other than 0,0. When we restrict the image components to be non-negative, through the use of basis functions in the +1, 0 domain, we are introducing averaging artifacts. Displaying an image component  $D_{uv} * w_{uv}^*(x,y)$  will create an average value of  $0.5x D_{uv}$  for u,v other than 0,0. The 0,0 image component  $D_{00} * w_{00}^*(x,y)$  is equal to the sum of the image over the macro-pixel, and is effectively the image averaged out over the macro-pixel area.

$$D_{00}^c = \sum_{x=1}^{nx} \sum_{y=1}^{ny} f^c(x,y)$$

Since each image component having u,v indices other than 0,0 will now contribute 1/2th of the  $D_{uv}$  value to the macro-pixel average, we should really be displaying the 0,0 image component with a strength equal to

$$D_{00}^c - \frac{1}{2} \sum_{u=1}^{nx-1} \sum_{v=1}^{ny-1} D_{uv}^c$$

In general,  $D_{00}$  is greater than or equal to the sum of the rest of the image components derived using the +1 and 0 mapping. Hence, subtracting out each of these non-zero integration components from  $D_{00}$  will be greater than or equal to zero. Consider for example the  $D_{01}$  component. Denote  $w_{uv}$  as the original Walsh function having the values of +1 and -1. Using the new basis functions  $w^* = (w+1)/2$ , substituting  $w_{uv}$  which can take

on values of 0 and 1 instead of -1 and +1,  $w_{uv}^*$  will transform the image construction equation EQ.2 to

$$f^c(x, y) = 2 \sum_{u=0}^{nx-1} \sum_{v=0}^{ny-1} D_{uv}^c * w_{uv}^*(x, y) - \sum_{u=1}^{nx-1} \sum_{v=1}^{ny-1} D_{uv}^c \quad \text{EQ. 3}$$

To reproduce the image correctly, the component value when the basis function is equal to all 1's ( $w_{00}$ ) has to be corrected with the summation over all  $D_{uv}$  except for the 00 component as in the second term of EQ. 3. Note that if a subset of basis functions are used as in lossy compression/construction, the summation will need to span only the  $D_{uv}$  coefficients that are used. The updated  $D_{00}$  coefficient is used in the image construction instead of the original value, since now the total sum of the average of the image components will equal the original  $D_{00}$  value.  $D_{00}$  may run negative in certain cases, which will cause artifacts. This can be treated in a lossy construction manner through hard limiting the number of dominant components to be displayed, or reducing the high frequency content in a more graceful manner, in essence spatially low pass filtering the image. Such artifacts can also be eliminated by reducing the pixel-grouping size for the region of interest. For example, transforming the 8x8 pixel region into four 4x4 block regions and implementing the algorithm at the reduced pixel group size level. Since the correction amount applied to the  $D_{00}$  coefficient needs to be bounded by the  $D_{00}$  value, having a smaller number of components in the image construction will result in this bound to be satisfied with a higher spatial frequency bandwidth than a larger macro-pixel case.

The image coefficients  $D_{uv}$  can have positive or negative values for all components having higher order than the 00 component. In implementing the display component, the value of  $D_{uv} * w_{uv}^*(x, y)$  can only be positive. In the case of 'negative'  $D_{uv}$ , the image component is generated using the absolute value of  $D_{uv}$  and the inverse of the basis function pattern  $w_{uv}^*(x, y)$ . The inverse pattern is defined by interchanging the 0 values with +1 values in the  $w_{uv}^*(x, y)$  pattern, i.e., inverting or reversing the switch pattern for that orthogonal basis function.

A block diagram showing the whole system is in FIG 6.

For each frame, the video image is constructed through

1. Dividing the video image and display of  $M \times N$  pixels into  $P \times Q$  macro-pixels, which are subarrays of pixels of dimension  $n_x \times n_y$ .
2. Calculating the image component strength  $D_{uv}$  related to the image  $f(x,y)$  for each macro-pixel, and for each component if lossless compression method is sought, or for a subset of components which will be deemed satisfactory by the viewer, and for each color.
3. Set the  $uv$  index of the image component to be displayed – note that for each macro-pixel, this index need not be the same with other macro-pixels, and different macro-pixels can at any time can display different basis functions.
4. In the display, select the macro-pixel through scanning macro-pixel rows and macro-pixel columns. These are  $n_x$  and  $n_y$  size groupings of the pixel rows and columns.
5. Applying a spatial signal mask which generates a light intensity profile corresponding to  $w_{uv}^*(x,y)$  for the macro-pixel of interest. In an active-matrix type embodiment, this mask will select only the pixels which will be updated in the subframe.
6. Applying a voltage or current signal which will correspond to light emission proportional to  $D_{uv}$  for each pixel selected to be in the on state in the macro-pixel. For color displays, three color light elements are used per pixel grouping. The light intensities of the red, green and blue sources are adjusted according to the calculated  $D_{uv}$  for each color. The  $D_{uv}$  coefficients can actually take positive or negative values. In the case of a negative coefficient, the light intensity is the absolute value of the coefficient, but in the reconstruction of the image, we use the inverse of the masking pattern (as applied in step 2 above).
7. Repeat for all macro-pixels.
8. Select the next  $uv$  component index to be treated and repeat from line 3.

A subframe mask can be generated by selecting multiple row and columns spanning a macro-pixel. Assume a  $4 \times 4$  pixel array forming the macro-pixel. The basis functions of Figure 4 can be generated through the use of a digital function generator which turns on or

off the select lines for each pixel in the macro-pixel. Figure 7 shows the truth table for such a system. Note that some coefficients can be implemented in two steps for a 4 x 4 pixel array, and three or four steps for an 8 x 8 pixel array. Figure 8 shows a register based implementation of a masking pattern generation function using serial data.

To arrive at a single frame of the intended image, each image component in a subframe is displayed successively. An observer's eye will integrate the displayed image components to visually perceive the intended image, which is the sum of all displayed image components. The  $D_{uv}$  coefficients calculated in EQ. 1 assume equal subframe durations. The subframe duration can be made varying with the uv index, in which case the particular  $D_{uv}$  will need to be normalized with the subframe time  $\tau_{uv}$ . Such a scheme may be used to relax the data driver's speed and precision requirements. The subframe image integration can also be partially performed in pixel structures which can retain the image data, as in active matrix pixels. In this case, instead of resetting the image information at each subframe, the corresponding signal stored in a capacitor is updated at each subframe. This is explained below.

A lossy compression based decomposition allows one to neglect higher spatial frequency component coefficients  $D_{uv}$ . These are generally components which have high order oblique spatial frequencies, which the human eye has reduced sensitivity to. Taking the example of 4 x 4 pixel grouping, which will have 16 image components with coefficients from  $D_{00}$ ,  $D_{01}$ ,  $D_{02}$ ,  $D_{03}$ ,  $D_{10}$ ,  $D_{11}$ , etc. up to  $D_{33}$ , and transformed basis functions  $w_{00}^*$  through  $w_{33}^*$ , and the inverses of these functions (except for the inverse of  $w_{00}^*$  which is a blank image), the original image will be exactly reconstructed if we use all 16 components, assuming the corrected  $D_{00}$  coefficient remains non-negative. However, in a general moving video case, the oblique spatial components may be neglected to some extent. A display system which uses only horizontal and vertical image components can be satisfactory in some cases. To improve image accuracy, the dominant of the diagonal spatial frequency basis functions such as  $w_{11}^*$ ,  $w_{22}^*$ , and or  $w_{33}^*$  having coefficients  $D_{11}$ ,  $D_{22}$  and/or  $D_{33}$  can also be added. The oblique components such as  $w_{12}^*$ ,  $w_{13}^*$ ,  $w_{23}^*$  etc. may also be neglected if the picture quality is deemed satisfactory by applying a threshold below which we will neglect the component. In image and video compression techniques like

JPEG and MPEG2 intra frame compression, the sequence of spatial frequency components are in a 'zig-zag' order, which allows for an 'EOB' (end-of-block) signal to denote that remaining coefficients in the sequence are negligible. The sequence goes as  $w_{00}^*$ ,  $w_{01}^*$ ,  $w_{10}^*$ ,  $w_{20}^*$ ,  $w_{11}^*$ ,  $w_{02}^*$ ,  $w_{03}^*$ ,  $w_{12}^*$ ,  $w_{21}^*$ ,  $w_{30}^*$ ,  $w_{40}^*$ , etc. until an EOB is sent.

Components before the EOB may also have negligible coefficient value. The video source coding can therefore have a variable sequence length, to which the display system will match. If none of the components are non-negligible, we would resort to lossless operation on the macro-pixel. Note also that different macro-pixels can have different levels of compression depending on the source video at the same time. Such a case can occur for example in a computer monitor, where during operation, regions of the screen may have stagnant images, but require a high accuracy such as a window showing a text and high resolution imagery, or portions having a fast moving image in which we need a high frame rate for motion compensation, but not necessarily need a lossless image reproduction scheme. By masking out different macro-pixel regions where we can skip certain image components, or updating the macro-pixel image less frequently, the image accuracy and power can be optimized. We can decide on which macro-pixel to run which accuracy mode by calculating the  $D_{uv}$  coefficients and comparing them to the component coefficients in the earlier image frames. A fast moving image vs. slow moving or stagnant image, and an accurate image vs. a lossy compressed image can be differentiated thus. Figure 8 shows how different macro-pixels on different regions of the screen can have different effective frame rates through the use of a smart controller.

In active matrix displays, in which the pixel circuitry may have a capacitor to hold the  $D_{uv}$  coefficient value, we may partition the dominant components over several subframes. This is so that the capacitor charge does not change as much when we reset the value. For example, in transitioning from the  $w_{00}^*$  component to the  $w_{01}^*$  component, the capacitor voltage on half the pixels in a macro-pixel will be reset to zero, and the capacitor voltages on the remaining half of the pixels will be set to the  $D_{01}$  coefficient value. This requires the column data drivers to charge and/or discharge up to the full capacitor voltage within a subframe duration, which costs power. Instead, the previous subframe data can be retained until the end of the frame, with the provision that it is normalized with the number of subframes the data will remain on the capacitor. To illustrate this, assume we have a

lossless construction over 16 subframes, each subframe with equal duration. The time integrated voltage over the frame is given by EQ. 3. In this equation, the components  $D_{uv}^* w_{uv}^*$  are assumed to be ON for one subframe duration, and the capacitors are reset to the next component voltage when the subframe duration ends. Instead, a portion of each previous component can be retained on the capacitor. The  $w_{00}^*$  component duration will then be 16 subframes, hence its value will be normalized by 16. Assume the second subframe is the  $w_{01}^* D_{01}$  component. This component will last for 15 subframes. This macropixel capacitors will be recharged such that the voltage at the second subframe is equivalent to  $D_{00} w_{00}^*/16 + D_{01} w_{01}^*/15$ . The process repeats for each component, which will be normalized with the number of remaining subframes till the end of the frame. The last component to be displayed,  $w_{33}^* D_{33}$  will only be effective for one subframe, so its value is not normalized. The net effect will be that at the end of the frame, we have the same integrated image information as EQ. 3.

Taking the example of a VGA resolution display operating at 30 frames per second, and a 4 x 4 pixel grouping to define the macro-pixels, the display device to satisfy VGA resolution employing this invention will use

1. 640 x 400 pixel array grouped as a 160 x 100 macro-pixel array for each color component.
2. A row and column select signal masking pattern generator which will generate the sixteen orthogonal basis patterns and the inverted patterns.
3. A computation device which calculates the corresponding  $D_{uv}$  components for each color from a VGA resolution image at each frame.
4. Determining the desired effective frame rate by comparing key coefficients  $D_{uv}$  with the previous frame's stored values.
5. Setting the row and column select pattern corresponding to the  $D_{uv}$  coefficient to be displayed.
6. Applying a light signal proportional to  $D_{uv}$ , to all the selected pixels.

By using a pixel addressing mask pattern, the number of pixels which is addressed uniquely is reduced from 768000 (for three colors) by a factor of 16 down to 48000 (for three colors) for the VGA resolution display. There are 16000 macro-pixels in the display.

The raw image data rate which the pixel drivers depends on the level of image compression desired. For a lossless image reconstruction, there are 16 image components per macro-pixel per color. Consider an 8 bit color system. If each component coefficient  $D_{uv}$  is described with 8 bit accuracy, we would need a 184Mbps data rate. This corresponds to 16 components  $\times$  8 bits = 128 bits per macro-pixel per color per frame. In reality, only the  $D_{00}$  component needs to have the full 8 bit accuracy, while the higher order components can have less accuracy. The higher order components will in general be limited in amplitude by a factor of 0.5 to the lower order component. Hence, the first order coefficients  $D_{01}$  and  $D_{10}$  can be described with a 7 bit precision, the second order coefficient  $D_{02}$ ,  $D_{20}$ ,  $D_{11}$  can be described with a 6 bit precision and so on. We would therefore not need more than 80 bits per macro-pixel per color per frame, which optimizes the data rate down to 120Mbps. The video data driver precision need not satisfy the full 8-bit resolution throughout the frame, and can be made to have a dynamic resolution by turning off unnecessary components when not needed. Define arbitrarily three compression levels for clarification purposes – lossless compression, medium and high level compression. In actual implementation these definitions may have different forms based on the desired image quality. Assume that in a medium compression level, we cut off oblique spatial frequency components such as  $w_{12}^* D_{12}$ ,  $w_{13}^* D_{13}$ ,  $w_{23}^* D_{23}$  etc. but not  $w_{11}^* D_{11}$ ,  $w_{22}^* D_{22}$ ,  $w_{33}^* D_{33}$ . Then we are working with 10 components in total. These components would require a total of 60 bits per macro-pixel per color per frame. The total data rate is reduced to 86Mbps. Define the high compression level as an operation mode in which we neglect  $D_{11}$ ,  $D_{22}$ ,  $D_{33}$ . Then we would use 46 bits per macro-pixel per color per frame. The total data rate is then 66Mbps. The row and column select pattern needs to be updated 16 times each frame for the lossless compression case, 10 times each frame for the medium level compression case, and 7 times each frame for the high level compression case. For 30 frames per second, displaying 7 subframes requires 210 patterns to be generated per second, or 4.7 msec per subframe. Using 10 components, we would need to generate 300 patterns per second, or 3.3 msec per subframe. For lossless image reproduction, a total of 16 subframes are needed, which equals 480 patterns per second, requiring 2 msec per subframe. These values provide a settling time bound for the data drivers.



In a particular embodiment of the invention, a LED based active-matrix display system is considered, though the invention is not so limited. The display system consists of:

1. A LED array of 640 x 400 red, green and blue light generating LEDs 100, totaling 768000 active elements.
2. A multitude of video digital-analog converter data drivers 110 which outputs the analog signals to the macro-pixels.
3. A row and column switch matrix 120 which scans the macro-pixel array, selecting the macro-pixel to be loaded with mask pattern and video data.
4. An image processing computation device 130 which determines the macro-pixel image coefficients using equation 1, and the timing control of the coefficients.
5. A mask pattern generation switch network 140 which turns on/off pixels within a macro-pixel to correspond to the orthogonal basis function to be displayed.

The pixels are grouped in 4x4 arrays, thus each red, green and blue LED defines a macro-pixel, thereby 48000 macro-pixels exist for three colors. The macro-pixels for different colors can be selected at the same time since the column video data is coming from different digital-analog converters. A fast enough digital-analog converter can service all pixels, or a larger number of digital-analog converters can be employed to relax the speed and driving requirements if necessary.

In the image processor 130, the image is divided into macro-pixel arrays for processing. For each macro-pixel, the image decomposition algorithm determines the coefficients corresponding to each orthogonal basis function for each color to be used. The decomposition coefficients  $D_{uv}$ , where  $u$  and  $v$  run from 0 through 3 are calculated. These coefficients are summations of 16 pixel values comprising the macro-pixel according to the corresponding masking patterns  $w_{uv}$ . The number of decomposition coefficients to be used can be selected from one to sixteen, in increasing resolution. The full set of sixteen coefficients is used when lossless reconstruction of the image is necessary. This mode is determined when all  $D_{uv}$  coefficients are greater in magnitude from a threshold value. Portions of the display can also have different compression levels during operation, which the image processor can decide depending on the decomposition coefficient value it

calculates. The row and column select block 120 scans and selects the macro-pixel to be operated on. Masking pattern generator 140 is a secondary switch network which drives the patterns related to the  $D_{uv}$  coefficient to be displayed through a counter based logic, or a look-up table. The patterns are shown in Figures 4 and 5 for two different macro-pixel sizes. For 4 x 4 array comprising the macro-pixel, the sequence of patterns is  $w_{00}^*$ ,  $w_{01}^*$ ,  $w_{02}^*$ ,  $w_{03}^*$ ,  $w_{10}^*$ ,  $w_{20}^*$ ,  $w_{30}^*$ ,  $w_{11}^*$ ,  $w_{22}^*$ ,  $w_{33}^*$ ,  $w_{12}^*$ ,  $w_{21}^*$ ,  $w_{13}^*$ ,  $w_{31}^*$ ,  $w_{23}^*$ , and  $w_{32}^*$ . The particular order may be different depending on implementation and video statistics. For example, a zig-zag scan order is commonly used in image compression, in which case the order will be  $w_{00}^*$ ,  $w_{10}^*$ ,  $w_{01}^*$ ,  $w_{02}^*$ ,  $w_{11}^*$ ,  $w_{20}^*$ ,  $w_{30}^*$ ,  $w_{21}^*$ ,  $w_{12}^*$ ,  $w_{03}^*$ ,  $w_{13}^*$ ,  $w_{22}^*$ ,  $w_{31}^*$ ,  $w_{32}^*$ ,  $w_{23}^*$ , and  $w_{33}^*$ . The counter may reset or skip at any point if the decomposition coefficients are negligible for higher order terms, thereby reducing the total data rate.

The display is scanned at each frame starting with the  $w_{00}^*D_{00}$  component of macro-pixels. The row and column select signal mask generated by 140 is all 1's in this case, meaning 4 rows and 4 columns are all selected. The necessary voltage signal is loaded to the video data memory, which can be a single capacitor for a macro-pixel array, and the macro-pixel scan proceeds to the next array. The subframe scan ends upon visiting all 48000 macro-pixels. The next subframe will load the  $w_{01}^*D_{01}$  component to each macro-pixel. In this case, the mask generator 140 will generate the required signals for loading the pattern  $w_{01}$  to the 4 x 4 pixel array. It can also load the inverse of the pattern if the  $D_{uv}$  coefficient is negative. The signal masks can change for each macro-pixel in the scan, as there is no restriction as to which image coefficient is to be loaded during the scan. One macro-pixel can be loaded with a particular  $D_{uv}$  with a masking pattern of  $w_{uv}$ , while the next macro-pixel in the scan can be loaded with a different component having a different masking pattern, since for one macro-pixel, a particular  $D_{uv}$  term may be negligible and eliminated from displaying, while for another macro-pixel it may be non-negligible. Each macro-pixel can have a different effective frame rate. While the subframe update rate is common, since each frame may be composed of a different number of subframes. A macro-pixel can also have its frame rate changed by the image processor when the nature of the video content changes. This can happen as shown in Figure 9, in which case a background image need not have a high effective frame rate, but can be represented at a higher accuracy

by incorporating more  $D_{uv}$  coefficients in the image construction, while a moving object can be represented by a smaller number of  $D_{uv}$  coefficients, but updated at a higher frame rate.

A similar embodiment with an LCD based active-matrix display is also possible. In this case, since the pixel switching speeds may be considerably slower than that of a LED based display, subframe durations are longer. The maximum possible number of subframes that can be squeezed in a frame will be limited. In such a case, one may resort to driving modes in which a certain subset of  $w_{uv}^* D_{uv}$  components are displayed in a frame, and the remaining components are displayed in an alternate frame such that the picture will have minimum loss of fidelity. In such a case the  $D_{uv}$  coefficients will need to be normalized appropriately.

In certain LED based arrays (see U.S. Provisional Patent Application No. 60/975,772 filed September 27, 2007), or MEMS based digital micromirror device (U.S. Patent Application No. 5,452,024 filed September 19, 1995), light elements can only be in ON or OFF states. The desired light value can be determined through pulse width modulation, or through bitplane modulation. In such an embodiment, pixels can be addressed as a group of macro-pixels, having a common ON time duration, but the data is AND'ed with the known basis function patterns of 1's and 0's. The number of subframes is again equal to the number of components that is used, or the maximum number of components pertaining to the macro-pixel size.

## CLAIMS

What is claimed is:

1. A video system comprising:
  - a video display having M by N pixels, each pixel having red, blue and green light elements for color operation, or a white light element for gray-scale operation, the intensity of each light element being controllable by a voltage or current;
  - a switch network coupled to address any pixel of the video display through row and column select switches;
  - switch driver circuitry coupled to select each of a plurality of macro-pixel groupings, the macro-pixel groupings collectively forming the M by N pixel video display, and to select pixels within each macro-pixel to generate switch patterns corresponding to orthogonal basis functions;
  - at least one digital-to-analog converter or pulse width modulator circuitry coupled to supply image information to the switch driver circuitry;
  - a digital computation device coupled to receive video information to be displayed and to control the switch driver circuitry and the light elements corresponding to the switch patterns corresponding to orthogonal basis functions;
  - whereby the video system is capable of displaying video and still images with programmable resolution depending on the video content.
2. A method of displaying an image having M by N pixels, comprising:
  - selecting each of a plurality of macro-pixel groupings collectively forming the M by N pixel display;
  - generating switch patterns corresponding to on-off switch states of orthogonal basis functions for each macro-pixel;
  - determining image coefficients for respective switch patterns;
  - controlling a switch drive network by the switch patterns to generate masking patterns for each macro-pixel corresponding to the orthogonal basis functions for image decomposition; and
  - controlling pixel illumination within a macro-pixel for masking patterns responsive to an image coefficient for the respective masking pattern.

3. The method of claim 2 further comprising:  
correcting for the averaging artifact which arises from using blocking patterns corresponding to orthogonal basis functions which multiply the light by 0 or 1 (on-off switch states instead of -1 or 1 for a discrete Walsh transform type basis function implementation) by determining the average image coefficient applicable to the entire macro-pixel (a zero coefficient), and correcting the average image coefficient applicable to the entire macro-pixel by subtracting the average image coefficients applicable to all other masking patterns (the averages of nonzero coefficients).
4. The method of claim 3 wherein when the averages of the non-zero coefficients are greater than the zero coefficient, then spatial frequency filtering to eliminate some non-zero coefficients and respective masking patterns employed to keep the sum of the non-zero averages equal to or smaller than the zero coefficient.
5. The method of claim 3 wherein when the averages of the non-zero coefficients are greater than the zero coefficient, then reducing the number of pixels in the macro-pixel grouping to keep the sum of non-zero averages equal to or smaller than the average component term.
6. The method of claim 3 further comprising:  
for image coefficients which are negative, reversing the spatial masking pattern from blocking to passing state ('0' to '1') and vice versa by inverting the switch pattern for the respective orthogonal basis functions.
7. The method of claim 3 wherein only a subset of image coefficients to which human eye is sensitive to are displayed used, and image coefficients which human eyes are not sensitive to are discarded.
8. The method of claim 3 wherein the image coefficients to be used to reconstruct an image is varied for different macro-pixels within an image, and image to image.
9. The method of claim 3 wherein lower order image coefficients have a greater bit precision than higher order image coefficients.

10. A method of displaying an image having M by N pixels, comprising:  
selecting each of a plurality of macro-pixel groupings collectively forming the M by N pixel display;  
generating switch patterns corresponding to on-off switch states of orthogonal basis functions for each macro-pixel;  
determining image coefficients for respective switch patterns;  
correcting for the averaging artifact which arises from using blocking patterns corresponding to orthogonal basis functions which multiply the light by 0 or 1 (on-off switch states instead of -1 or 1 for a discrete Walsh transform type basis function implementation) by determining the average image coefficient applicable to the entire macro-pixel (a zero coefficient), and correcting the average image coefficient applicable to the entire macro-pixel by subtracting the average image coefficients applicable to all other masking patterns (the averages of nonzero coefficients);  
for image coefficients which are negative, reversing the spatial masking pattern from blocking to passing state ('0' to '1') and vice versa by inverting the switch pattern for the respective orthogonal basis functions.  
controlling a switch drive network by the switch patterns to generate masking patterns for each macro-pixel corresponding to the orthogonal basis functions for image decomposition; and  
controlling pixel illumination within a macro-pixel for masking patterns responsive to an image coefficient for the respective masking pattern.

11. The method of claim 10 wherein when the averages of the non-zero coefficients are greater than the zero coefficient, then spatial frequency filtering to eliminate some non-zero coefficients and respective masking patterns employed to keep the sum of the non-zero averages equal to or smaller than the zero coefficient.

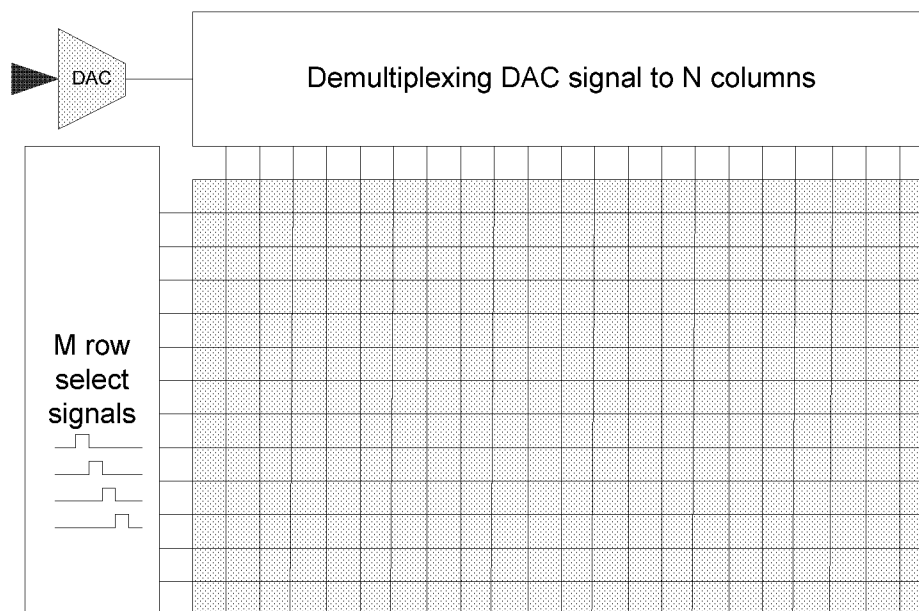
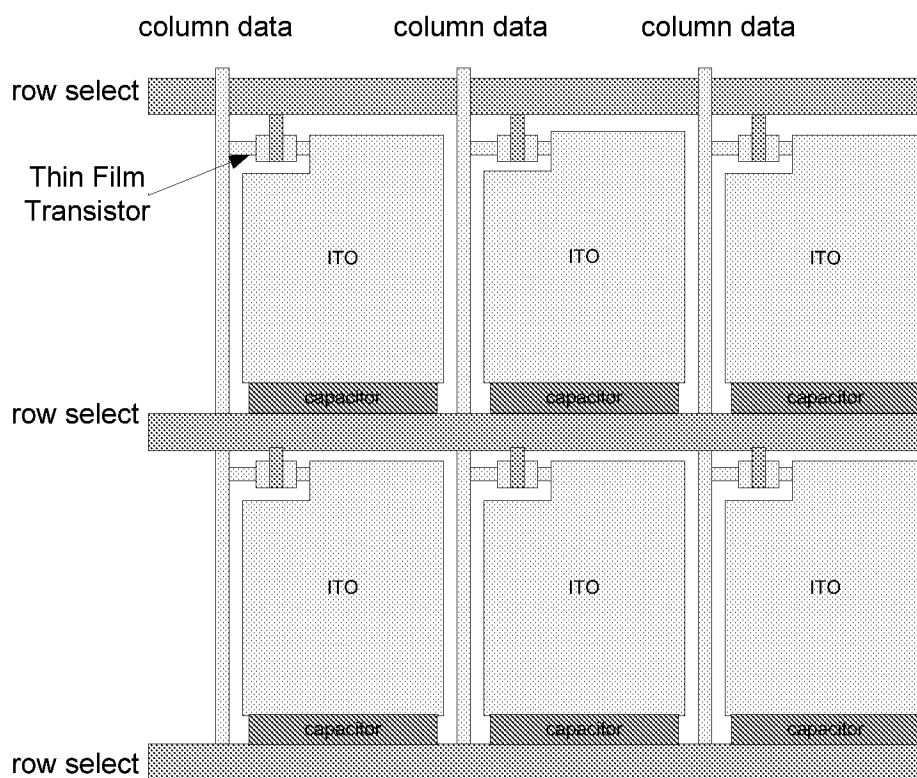
12. The method of claim 10 wherein when the averages of the non-zero coefficients are greater than the zero coefficient, then reducing the number of pixels in the macro-pixel grouping to keep the sum of non-zero averages equal to or smaller than the average component term.

13. The method of claim 10 wherein only a subset of image coefficients to which human eye is sensitive to are displayed used, and image coefficients which human eyes are not sensitive to are discarded.

14. The method of claim 10 wherein the image coefficients to be used to reconstruct an image is varied for different macro-pixels within an image, and image to image.

15. The method of claim 10 wherein lower order image coefficients have a greater bit precision than higher order image coefficients.

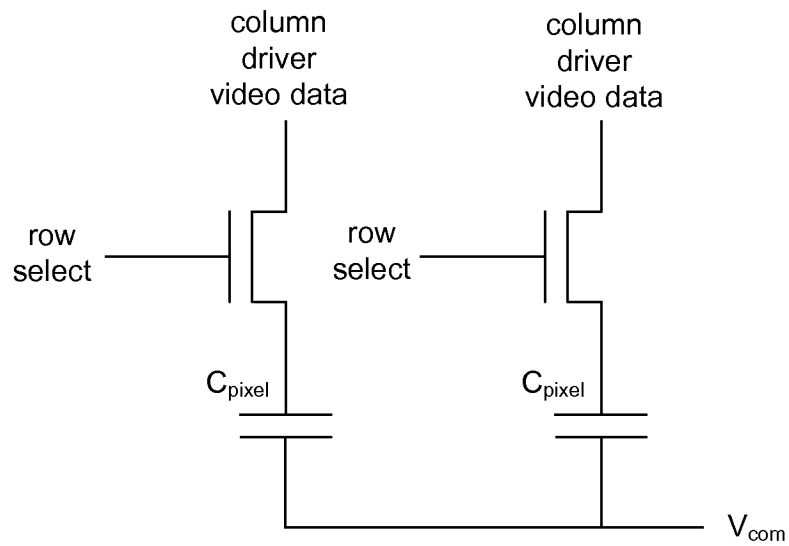
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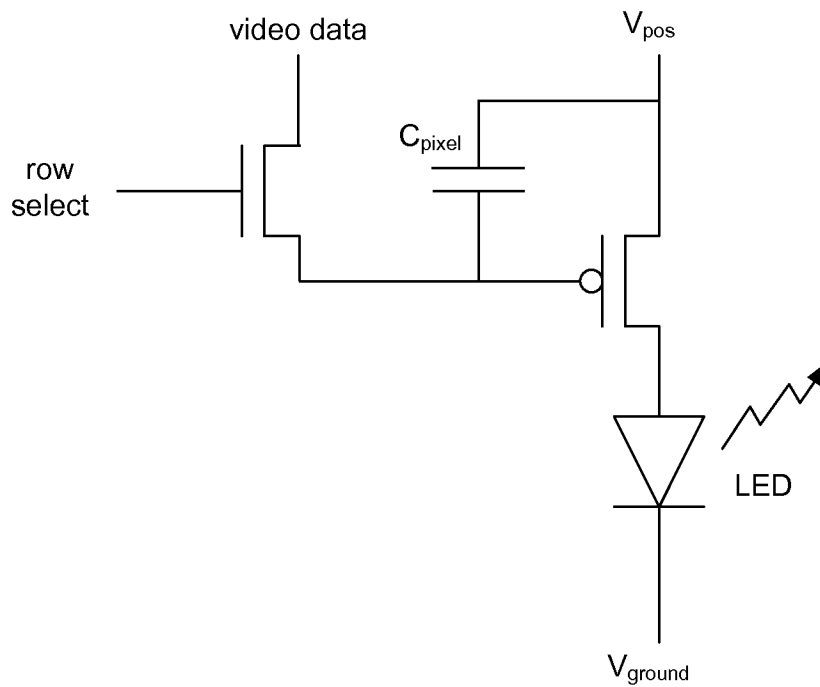
**FIG. 1**



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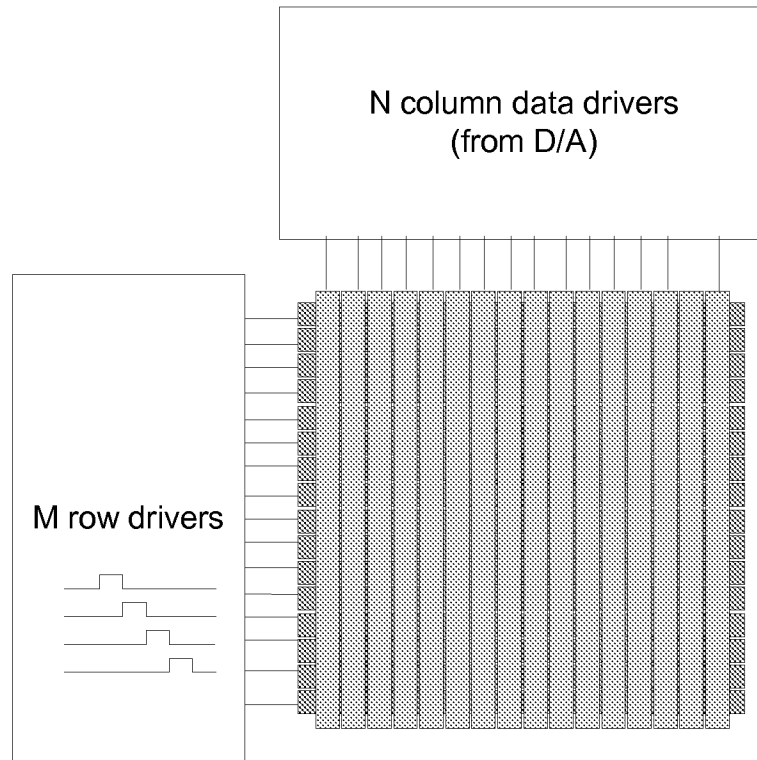
Active Matrix TFT select transistors charging LCD pixel capacitors  
(2 pixels shown)

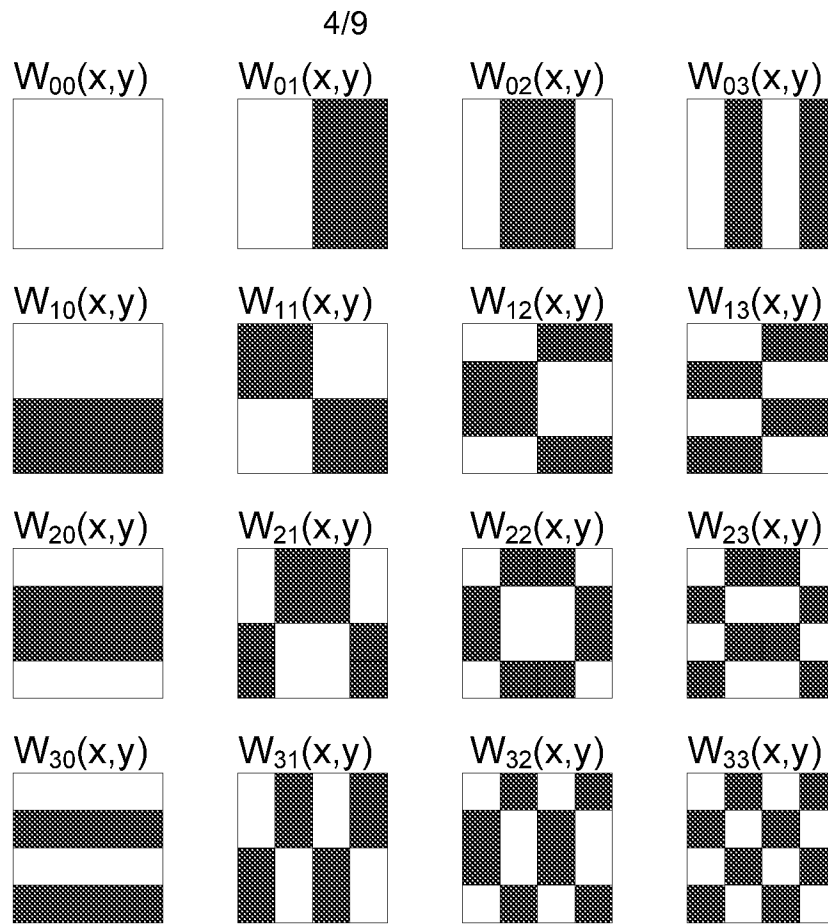


Active Matrix TFT select transistors charging OLED pixel capacitors

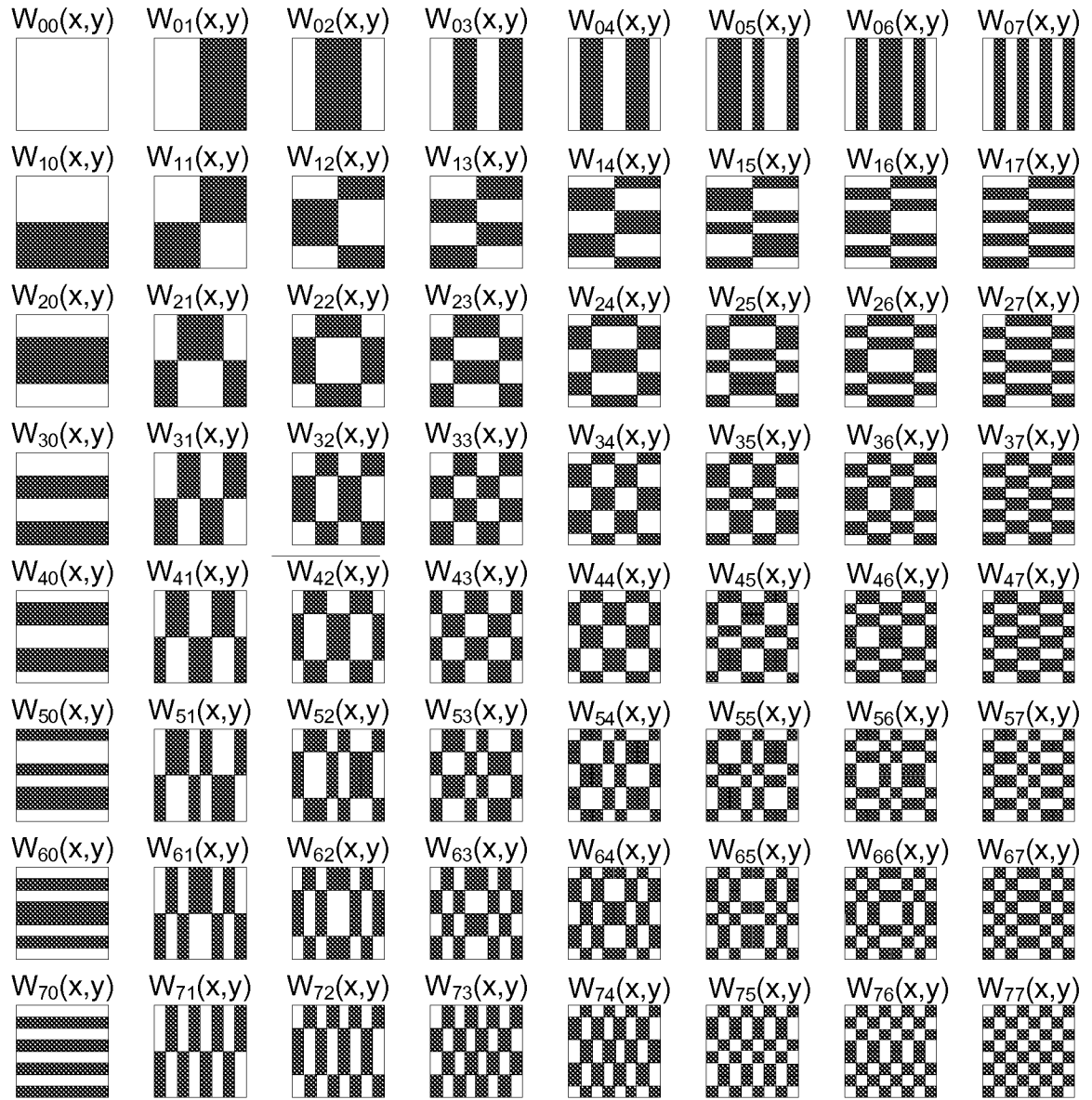
**FIG. 2**

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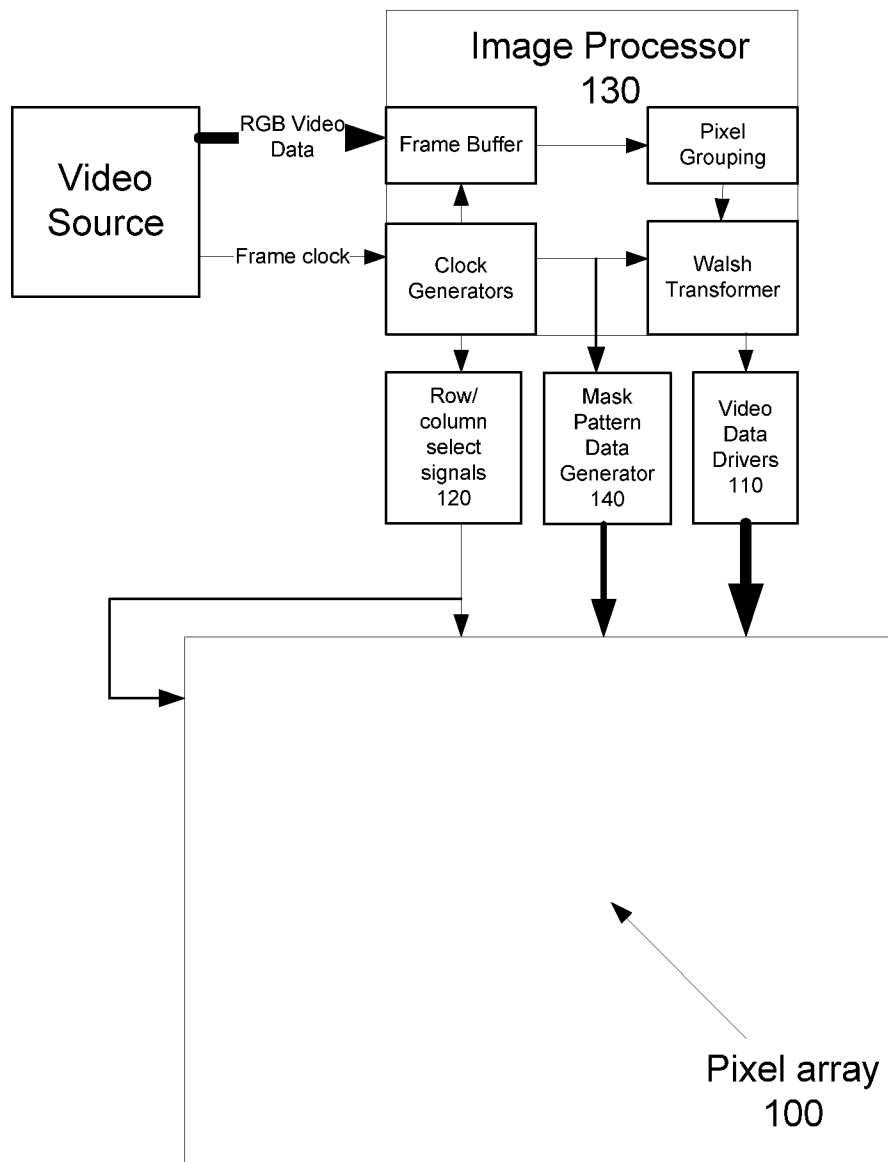
*FIG. 3*

*FIG. 4*

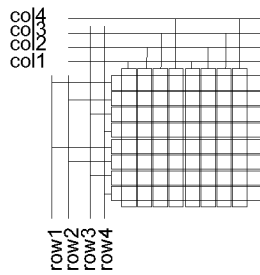
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*FIG. 5*

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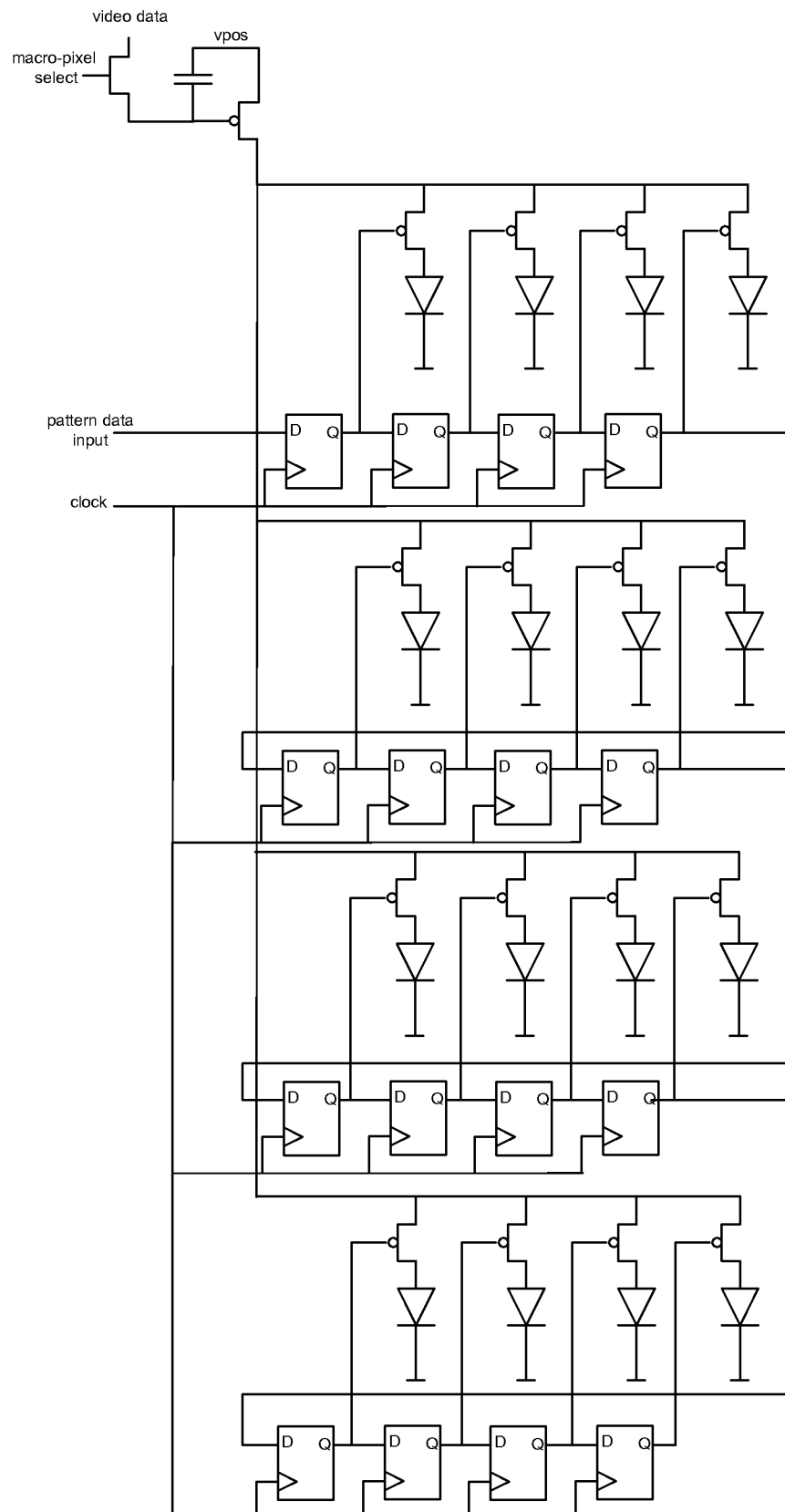
*FIG. 6*

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col4	col3	col2	col1	row4	row3	row2	row1	coefficient	macropixel pattern
0	0	0	0	0	0	0	0	not used	
1	1	1	1	1	1	1	1	00	
0	0	1	1	1	1	1	1	01	
1	1	0	0	1	1	1	1	01*	
1	0	0	1	1	1	1	1	02	
0	1	1	0	1	1	1	1	02*	
1	1	1	1	0	0	1	1	10	
1	1	1	1	1	1	0	0	10*	
1	1	1	1	1	0	0	1	20	
1	1	1	1	0	1	1	0	20*	
0	1	0	1	1	1	1	1	03	
1	0	1	0	1	1	1	1	03*	
1	1	1	1	0	1	0	1	30	
1	1	1	1	1	0	1	0	30*	
0	0	1	1	0	0	1	1	11	
1	1	0	0	1	1	0	0		
1	0	0	1	1	0	0	1	22	
0	1	1	0	0	1	1	0		
1	0	0	1	1	1	0	0	21	
0	1	1	0	0	0	1	1		
1	0	0	1	1	0	1	0	23	
0	1	1	0	0	1	0	1		
1	0	1	0	1	1	0	0	33	
0	1	0	1	0	0	1	1		

FIG. 7



**FIG. 8**

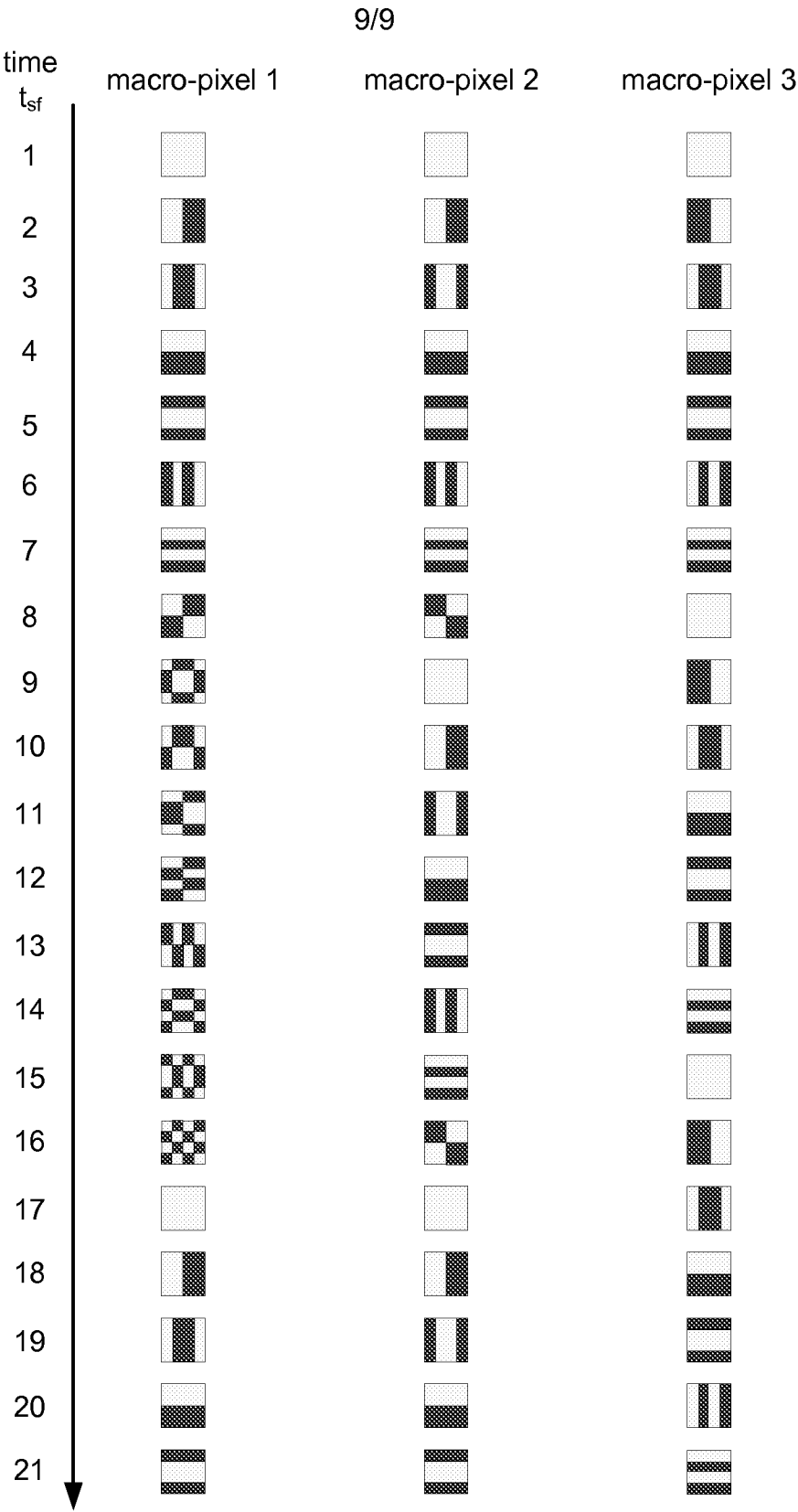


FIG. 9



# INTERNATIONAL SEARCH REPORT

International application No  
PCT/US2010/026325

## A. CLASSIFICATION OF SUBJECT MATTER

INV. G09G3/20 G09G3/36 G09G3/34  
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G09G H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y A	US 2008/018624 A1 (CERNASOV ANDREI [US]) 24 January 2008 (2008-01-24) abstract; figures 1-7	1-3, 7-9  6, 10, 13-15 4, 5, 11, 12
X  Y	WO 2004/006219 A1 (KONINKL PHILIPS ELECTRONICS NV [NL]; FISH DAVID A [GB]; VAN BERKEL COR) 15 January 2004 (2004-01-15) page 1, line 5 - page 12, line 8	1, 2  6, 10, 13-15
X	US 6 229 583 B1 (YASUNISHI NORIO [JP]) 8 May 2001 (2001-05-08) column 3, line 15 - column 23, line 2  -/--	1, 2

☒ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

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"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention  
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"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.  
"&" document member of the same patent family

Date of the actual completion of the international search

31 May 2010

Date of mailing of the international search report

10/06/2010

Name and mailing address of the ISA/

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Authorized officer

Njibamum, David

# INTERNATIONAL SEARCH REPORT

International application No  
PCT/US2010/026325

## C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

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Information on patent family members

International application No

PCT/US2010/026325

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