

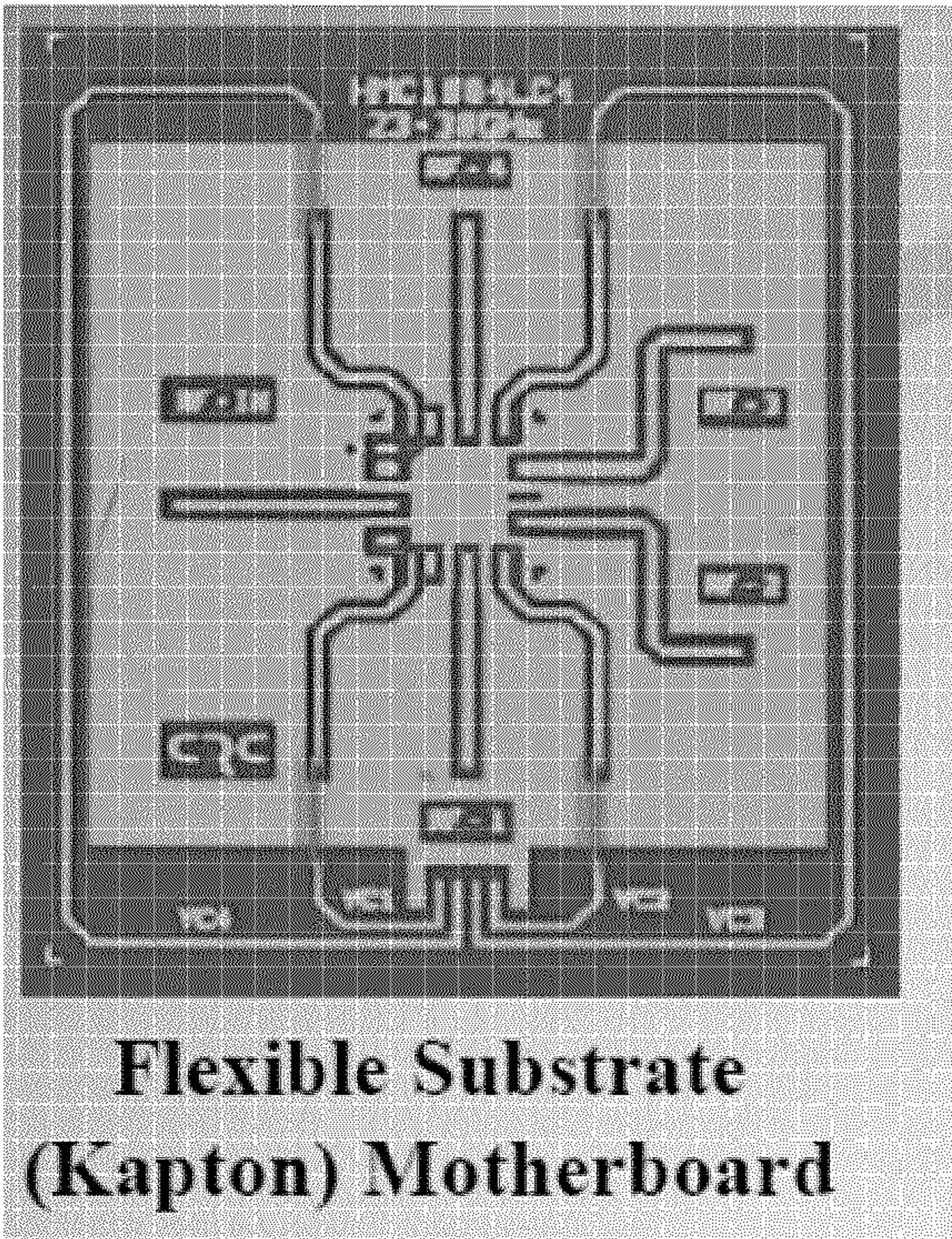


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(54) Titre : ELECTRONIQUE IMPRIMEE
(54) Title: PRINTED ELECTRONICS

FIGURE 19A



(57) **Abrégé/Abstract:**
The present invention relates to an electronic device comprising a printed substrate comprising a trace of molecular ink thereon, the molecular ink being sintered to form a conductive metal trace forming the electronic device, wherein the molecular ink is chosen

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(57) **Abrégé(suite)/Abstract(continued)**:

from a) a flake-less printable composition of 30-60 wt% of a C₈-C₁₂ silver carboxylate, 0.1 -10 wt% of a polymeric binder and balance of at least one organic solvent, all weights based on total weight of the composition; or b) a flake-less printable composition of 5-75 wt% of bis(2-ethyl-1 - hexylamine) copper (II) formate, bis(octylamine) copper (II) formate or tris(octylamine) copper (II) formate, 0.25-10 wt% of a polymeric binder and balance of at least one organic solvent, all weights based on total weight of the composition.

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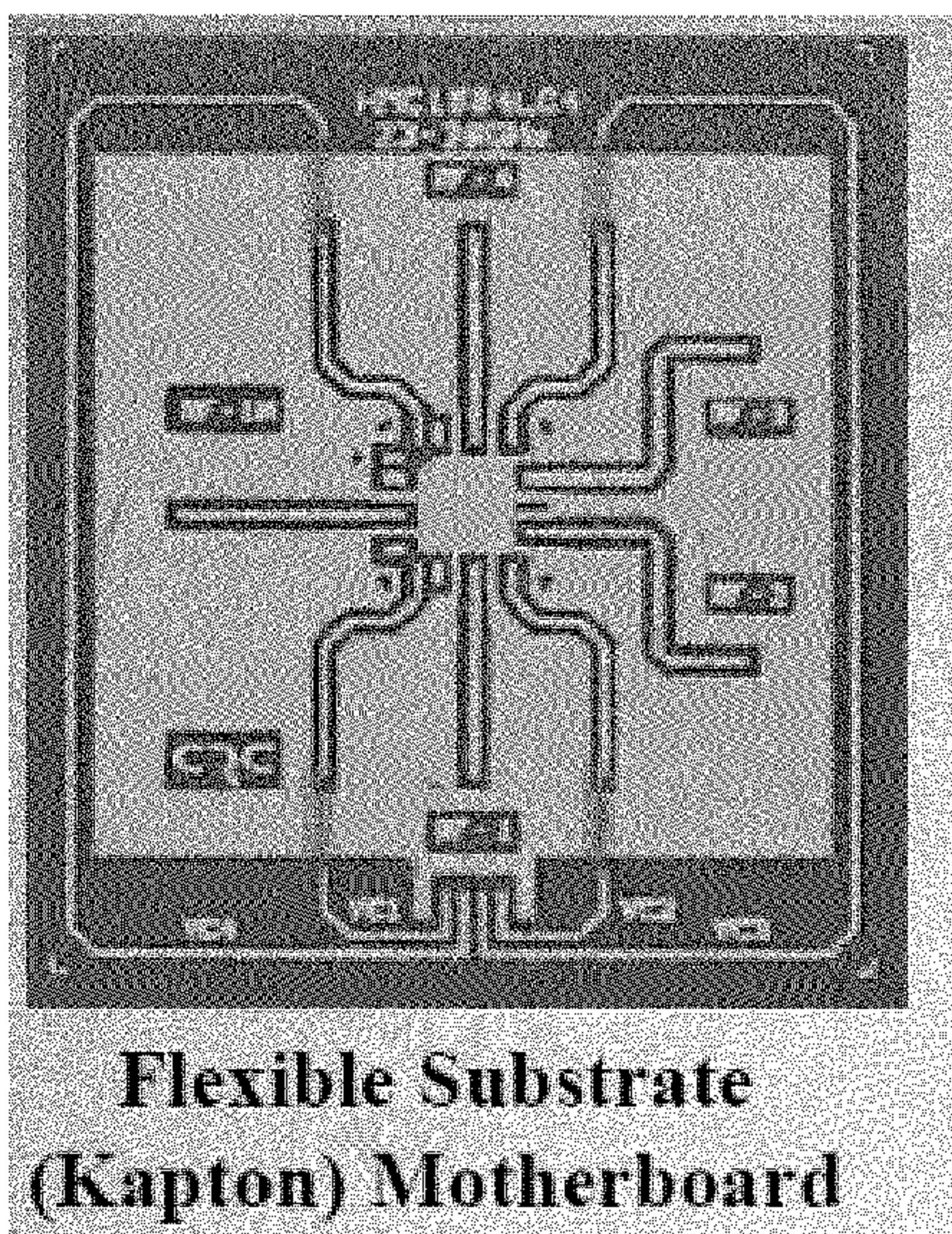
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(54) Title: PRINTED ELECTRONICS

FIGURE 19A**Flexible Substrate
(Kapton) Motherboard**(57) Abstract: The present invention relates to an electronic device comprising a printed substrate comprising a trace of molecular ink thereon, the molecular ink being sintered to form a conductive metal trace forming the electronic device, wherein the molecular ink is chosen from a) a flake-less printable composition of 30-60 wt% of a C₈-C₁₂ silver carboxylate, 0.1 -10 wt% of a polymeric binder and balance of at least one organic solvent, all weights based on total weight of the composition; or b) a flake-less printable composition of 5-75 wt % of bis(2-ethyl-1 - hexylamine) copper (II) formate, bis(octylamine) copper (II) formate or tris(octylamine) copper (II) formate, 0.25-10 wt% of a polymeric binder and balance of at least one organic solvent, all weights based on total weight of the composition.

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PRINTED ELECTRONICS

BACKGROUND

(a) Field

[0001] The subject matter disclosed generally relates to printed electronics. More specifically, it relates to printed electronics applications using molecular ink or flake ink.

(b) Related Prior Art

[0002] There are inks, such as molecular inks or flake inks, designed to give specific properties to the inks being printed.

[0003] The advent of printable inks with new properties brings possibilities to rethink the use of electrical conducting surfaces.

SUMMARY

[0004] According to an embodiment, there is provided an electronic device comprising a printed substrate comprising a trace of molecular ink thereon, the molecular ink being sintered to form a conductive metal trace forming the electronic device, in which the molecular ink is chosen from a flake-less printable composition of 30-60 wt% of a C₈-C₁₂ silver carboxylate, 0.1 -10 wt% of a polymeric binder and balance of at least one organic solvent, all weights based on total weight of the composition or a flake-less printable composition of 5-75 wt% of bis(2-ethyl-1-hexylamine) copper (II) formate, bis(octylamine) copper (II) formate or tris(octylamine) copper (II) formate, 0.25-10 wt% of a polymeric binder and balance of at least one organic solvent, all weights based on total weight of the composition.

[0005] There is provided an electronic device in which the molecular ink is stretchable and thermoformable.

[0006] There is provided an electronic device in which the substrate is flexible.

[0007] There is provided an electronic device which is printed as an electronic component.

[0008] There is provided an electronic device which is printed as a metal-insulator-metal (MIM) device.

[0009] There is provided an electronic device in which the trace of ink is on the insulator of the electronic device.

[0010] There is provided an electronic device which comprises an inductor.

[0011] There is provided an electronic device which comprises a capacitor.

[0012] There is provided an electronic device which comprises an electronic filter.

[0013] There is provided an electronic device which is printed as a solderable printed circuit board.

[0014] There is provided an electronic device which is printed as one of: a coplanar waveguide, an ultra high-frequency RF antenna, an ultra high-frequency RF filter and a hybrid RF device.

[0015] There is provided an electronic device which is printed as an engineered surface for frequency-selective RF filtering, reflecting or orienting.

[0016] There is provided an electronic device which is printed as a solar cell.

[0017] There is provided an electronic device which is printed as a semiconductor device.

[0018] There is provided an electronic device which is printed as a transparent electrode.

[0019] There is provided an electronic device which is printed as an electroluminescent lamp.

[0020] There is provided an electronic device which is printed as a wearable electronic device or an in-mold electronic device.

[0021] There is provided an electronic device which is printed as one of a physicochemical and an electromechanical sensor.

[0022] According to a second embodiment, there is provided a heater comprising an electronic device comprising a printed substrate with a trace of ink thereon, the ink being sintered to form a conductive metal trace forming the electronic device, in which the ink is chosen from a molecular ink or a flake ink.

[0023] There is provided an electronic device in which the molecular ink is stretchable and thermoformable.

[0024] There is provided an electronic device in which the substrate is flexible.

[0025] There is provided an electronic device which is printed as a resistive electrical circuit.

[0026] There is provided an electronic device which is coated in a portion of an aircraft.

[0027] There is provided an electronic device which is installed in a wing of an aircraft to heat the wing.

[0028] There is provided an electronic device which is used to maintain the performance of aircrafts, emergency systems, electric vehicles, lighting systems or building automation system.

[0029] There is provided an electronic device which is installed on a battery to heat the battery.

[0030] There is provided an electronic device in which the battery is a lithium battery.

[0031] According to a third embodiment, there is provided a touch interface comprising a printed substrate comprising a trace of molecular ink thereon, the molecular ink being sintered to form a conductive metal trace forming a transparent conductive film.

[0032] There is provided a touch interface further comprising printed capacitive or resistive elements.

[0033] There is provided a touch interface further comprising a capacitive micro-wire.

[0034] There is provided a touch interface in which the capacitive micro-wire comprises a grid or pattern of printed silver electrodes and the substrate is flexible.

[0035] There is provided a touch interface in which the capacitive micro-wire is made by laser trimming the conductive metal trace and thereby making the micro-wire capacitive.

[0036] There is provided a touch interface in which the capacitive micro-wire is made by performing a selective sintering of the conductive metal trace.

BRIEF DESCRIPTION OF THE DRAWINGS

[0037] Further features and advantages of the present disclosure will become apparent from the following detailed description, taken in combination with the appended drawings, in which:

[0038] Fig. 1 is a combination of graphs and tables illustrating the result of traces production printed to make up a circuit, according to an embodiment;

[0039] Fig. 2 is a combination of a top view, side views and a table illustrating the mechanical properties tested using ASTM F1683-02 bend and crease testing on a printed electronic device, according to an embodiment;

[0040] Fig. 3 is a combination of pictures, tables and graphs illustrating the electrical properties and trace resolution of screen printable molecular silver inks, according to an embodiment;

[0041] Fig. 4 is a combination of a picture, a table and a graph illustrating the shear force properties and environmental testing results for screen printable molecular silver inks, according to an embodiment;

[0042] Fig. 5A is a perspective view illustrating a metal-insulator-metal (MIM) device, according to an embodiment;

[0043] Fig. 5B is a top view and table illustrating a printed MIM inductor, according to an embodiment;

[0044] Fig. 5C is a top view and table illustrating a printed MIM capacitor, according to an embodiment;

[0045] Fig. 5D is a top view illustrating a printed MIM low-pass filter, according to an embodiment;

[0046] Fig. 5E is a graph illustrating the electrical behavior of a printed MIM low-pass filter, according to an embodiment;

[0047] Figs 6A-E are a profilometer image comparison of the molecular silver ink printed directly on Kapton substrate with no dielectric (a), encapsulated with a printed dielectric (b) and as a second layer on a printed dielectric layer (c), performance of the inductors printed on each surface (d) and a representative quality factor of the inductors (e), according to an embodiment;

[0048] Figs. 7A-C are a schematic (a) and a photograph (b) of a three layer, all-printed Metal-Insulator-Metal (MIM) capacitor on plastic substrate (Kapton) using the molecular silver ink and measurement results of three shunt MIM capacitors versus frequency (c), according to an embodiment;

[0049] Figs 8A-C are an optical micrograph of the TFT as an inset at the top right (a) and the linear fit to the transfer curve as an inset in the bottom left

(a), an optical profilometer image of the transistor (b) and a cross section of the channel width (c), according to an embodiment.

[0050] Fig. 9 is a top view illustrating an ultra high-frequency RF antenna, according to an embodiment;

[0051] Fig. 10 is a graph illustrating the filtering behavior of an ultra high-frequency RF antenna, according to an embodiment;

[0052] Fig. 11 is a perspective view illustrating the filtering class 1 and class 2 engineered surfaces interacting with RF signals, according to an embodiment;

[0053] Figs. 12A-12D are pictures illustrating the printed class 1 engineered surfaces, according to an embodiment;

[0054] Figs. 13A-13B are a picture and a graph illustrating the multi-layer FSS performance of printed class 1 engineered surfaces, according to an embodiment;

[0055] Fig. 14 is a top view and perspective view illustrating the installation of class 2 reflectarray gratings interacting with RF signals, according to an embodiment;

[0056] Fig. 15 is front views illustrating suitable ink-trace patterns of class 2 FSS-backed reflectarray grating, according to an embodiment; and

[0057] Figs. 16A-16B are a perspective view and a graph illustrating a device and results for FSS-Backed reflectarray grating performance, according to an embodiment.

[0058] Figs. 17A is a top and bottom view illustrating a selected discrete MMIC chip device for hybrid integration (Single pole, 4 Throw (SP4T) GaAs MMIC Switch), according to an embodiment;

[0059] Figs. 17B is a functional diagram and outline drawing of a selected discrete MMIC chip device for hybrid integration (Single pole, 4 Throw (SP4T) GaAs MMIC Switch), according to an embodiment;

[0060] Figs. 18A is a top and bottom view illustrating a selected discrete MMIC chip device for hybrid integration (Single Pole Double Throw (SPDT) GaN MMIC Switch), according to an embodiment;

[0061] Figs. 18B is a functional diagram and outline drawing of a selected discrete MMIC chip device for hybrid integration (Single Pole Double Throw (SPDT) GaN MMIC Switch), according to an embodiment;

[0062] Figs. 19A is a top view illustrating a discrete SP4T chip device assembled on flexible substrate, according to an embodiment;

[0063] Figs. 19B is a zoom in of a bottom and top view illustrating a discrete SP4T chip device assembled on flexible substrate, according to an embodiment;

[0064] Figs. 20A is a top view illustrating a discrete SPDT chip device assembled on flexible substrate (version based on CPW probes), according to an embodiment;

[0065] Figs. 20B is a zoom in of a bottom and top view illustrating a discrete SPDT chip device assembled on flexible substrate (version based on CPW probes), according to an embodiment;

[0066] Figs. 21A-21B is a top view and a diagram illustrating a discrete SPDT chip device assembled on flexible substrate (version based on connectors), according to an embodiment;

[0067] Figs. 21C is a zoom in of a bottom and top view illustrating a discrete SPDT chip device assembled on flexible substrate (version based on connectors), according to an embodiment;

[0068] Fig. 22 are graphs illustrating the performance of discrete SP4T chip assembled on flexible substrate, according to an embodiment;

[0069] Fig. 23 are graphs illustrating the performance of discrete SPDT chip assembled on flexible substrate (version based on CPW probes), according to an embodiment;

[0070] Fig. 24 are graphs illustrating the performance of discrete SPDT chip assembled on flexible substrate (version based on connectors), according to an embodiment;

[0071] Figs. 25A-25B are diagrams and a picture illustrating a screen printed multilayer CPW transmission line, according to an embodiment;

[0072] Figs 26A-26B are graphs and PET substrate specification illustrating wide-band characterization of a screen printed multilayer CPW interconnect, according to an embodiment;

[0073] Fig 27 is a combination of a picture and a graph illustrating screen-printed RF filters on Polyethylene Terephthalate (PET) substrate, according to an embodiment;

[0074] Figs 28A-28B are a picture and a graph illustrating a screen printed millimeter wave CPW bandpass filter on a flexible plastic substrate PET and performance results, according to an embodiment;

[0075] Fig. 29 are pictures illustrating a micro-wire touch interface, according to an embodiment.

[0076] It will be noted that throughout the appended drawings, like features are identified by like reference numerals.

DETAILED DESCRIPTION

[0077] There are described below flake and molecular inks and their fabrication method. There are further described printed electronics that can be

made using flake and molecular inks. Various applications for printed electronics are contemplated further below.

Flake Inks

[0078] Printable electronics, an additive manufacturing technology, combines electronic materials with conventional printing processes in order to enable large area, flexible and/or low-cost manufacturing of OFETs, membrane switches, photovoltaics, antennas, displays and sensors. Conductive inks make up one of the largest markets in printed electronics as they enable the fabrication of key components such as electrodes, OLEDs, antennas and bus bars for current collectors using gravure, aerosol jet, inkjet and screen printing.

[0079] Each of these printing techniques has its attributes, but the screen printing of silver flake inks is one of the most mature technologies, which is currently widely used in the manufacture of conductive components in industry. These flake-based inks can produce traces with good sheet resistivity values (10-15 mOhm/sq/mil) and typically have thicknesses of ~4-15 microns. The printed electronics industry is driven by a desire to produce inexpensive functional devices, and simply decreasing the silver content and the resulting trace thickness is an obvious means to decrease costs. However, decreasing the amount of silver deposited is typically not a viable option to decrease material consumption or cost with flake based inks because achieving high conductivity (low volume resistivity) and mechanical robustness in printed conductive traces require overlap of multiple layers of silver flakes and minimum thicknesses of ~4 μ m.

[0080] Screen printing is a commonly used technique to produce conductive features on flexible substrates and the most common ink employed in the printed electronics industry are based on metal flakes. Industrial facilities typically have problems producing trace widths less than 100 μ m and trace thicknesses less than 5 μ m. Silver flake inks do not produce sufficiently

conductive traces when less than 4 microns in thickness. Furthermore, silver flake inks do not lead to conductive traces that are flexible, nor can they be creased when the thickness is less than 4 microns. Silver flake inks also produce traces that suffer from adhesion limitations which require encapsulation in order to strengthen a joint with a conductive adhesive. Due to the size of the silver flakes (several microns) it is not possible to print sub-micron thick conductive traces, which is highly desired in order to minimize the aspect ratio of narrow traces. In addition, current screen printing inks cannot produce topographically flat surfaces.

[0081] As highlighted above, most commercial screen printable inks make use of silver flake formulations. The problems encountered with these flake-based inks stem from their large dimensions (several micron flake size). Because the flake is large, there can be difficulty physically printing it through small screen dimensions and producing uniform traces where all of the flakes overlap well to produce a conductive trace. In the case where screen inks are printed on polymer substrates, the inks must be sintered at lower temperatures and as a result, the flakes are only mildly sintered, generating traces with sheet resistance values typically ranging from 10-50 mOhm/sq/mil. In addition, because the resulting trace is comprised of large overlapping silver flakes, the surface topography is typically rough. Rough surfaces are particularly problematic in RFID applications where performance of the antenna is determined in part by surface roughness. There are examples of using nanoparticles (<100 nm diameters) to overcome this problem, but nanoparticles are relatively expensive to produce, and the performance gain is not sufficient to justify the additional cost.

[0082] There remains a need for printable molecular inks that can produce flexible conductive traces, especially molecular inks for screen printing.

Molecular Inks

[0083] There is described a flake-less molecular ink suitable for printing (e.g. screen printing, although other printing methods are suitable, as mentioned further below) conductive traces on a substrate has 30-60 wt% of a C8-C12 silver carboxylate or 5-75 wt% of bis(2-ethyl-1-hexylamine) copper (II) formate, bis(octylamine) copper (II) formate or tris(octylamine) copper (II) formate, 0.1-10 wt% of a polymeric binder (e.g. ethyl cellulose) and balance of at least one organic solvent. Conductive traces formed with the molecular ink are thinner, have lower resistivity, have greater adhesion to a substrate than metal flake inks, have better print resolution and are up to 8 times less rough than metal flake inks. In addition, the shear force required to remove light emitting diodes bonded to the traces using Loctite 3880 is at least 1.3 times stronger than for commercially available flake-based inks.

[0084] The molecular inks of the present invention comprise three main components: a metal precursor molecule, a binder and at least one organic solvent. Decomposition of the metal precursor molecule during ink processing produces conductive metal particles, the binder binds together the conductive metal particles and provides traces with adequate mechanical properties and adhesion to a substrate, and the solvent is mainly used to help make the ink printable, although the solvent may also dissolve the molecular ink to provide a more homogeneous ink and traces made therefrom. When deposited on a substrate to form traces and appropriately processed (e.g. by heat or light), the metal precursor molecules form conductive nanoparticles that are bound by the binder. The resulting traces are comprised of interconnected metal nanoparticles, but the nanoparticles are produced in situ, so the cost of the ink can compete with that of commercial flake based inks. In addition, due to the interconnected nanoparticle structure, the resistivity values are lower than metal flake-based inks. Further, traces derived from molecular inks show improved bonding to

adhesives than metal flake-based inks, have better print resolution than metal flake-based inks and are up to 8 times less rough than metal flake-based inks.

[0085] The molecular ink is flake-less, not making use of metallic flakes to provide conductivity. Instead, the molecular ink comprises a metal precursor molecule, specifically a metal carboxylate, more specifically a C8-C12 silver carboxylate or bis(2-ethyl-1-hexylamine) copper (II) formate, bis(octylamine) copper (II) formate or tris(octylamine) copper (II) formate. Because the ink does not contain flakes, conductive traces formed from the ink comprise interconnected metal nanoparticles, which permit the formation of very thin and narrow conductive traces.

[0086] The molecular ink comprises a composition comprising about 30-60 wt% of a C8- C12 silver carboxylate or about 5-75 wt% of bis(2-ethyl-1-hexylamine) copper (II) formate, bis(octylamine) copper (II) formate or tris(octylamine) copper (II) formate, weights based on total weight of the composition. Preferably, the composition comprises about 45-55 wt%, for example about 50 wt%, of the silver carboxylate, or about 65-75 wt%, for example about 72 wt%, of the bis(2-ethyl-1-hexylamine) copper (II) formate, bis(octylamine) copper (II) formate or tris(octylamine) copper (II) formate.

[0087] While the ink may be formulated for any kind of printing, the ink is particularly suited for screen printing. In this regard, the ink preferably has a viscosity of about 1500 cP or greater. Further, the solvent preferably has a sufficiently high boiling point so that the ink evaporates slowly during printing. This is known to increase the number of printing cycles that can be carried out before the ink must be thinned to improve performance.

[0088] The molecular ink may be deposited, for example printed, onto a substrate to form a trace of the ink on the substrate. Drying and decomposing silver salts within the trace to form conductive traces may be accomplished by any suitable technique, where the techniques and conditions are guided by the

type of substrate on which the traces are deposited. For example, drying and decomposing silver salts may be accomplished by heating and/or photonic sintering.

[0089] Inks of the present invention may be deposited on a substrate by any suitable method, for example screen printing, inkjet printing, flexography printing (e.g. stamps), gravure printing, off-set printing, airbrushing, aerosol printing, typesetting, or any other method. After deposition, the ink may be dried or cured, for example by allowing the ink to dry in ambient conditions or heating the ink for an appropriately long period of time to evaporate the solvent. The inks of the present invention are particularly suited to screen printing.

[0090] Molecular inks of the prior art are generally not formulated for screen printing and result in screen-printed conductive traces having limited adhesion to substrates. Limited adhesion leads to open circuit breaks and total loss in conductivity (i.e. infinite resistivity) as the trace delaminates from the substrate surface or as the trace forms macrostructural or microstructural cracks. In contrast, conductive traces of the present invention have good adhesion to substrates as discussed above, and do not develop open circuit breaks over a period of at least 1 day, preferably at least 1 month, more preferably at least 1 year. Printed traces from the present invention get a grade of 5B (no flaking occurred) following the Cross-Hatch Adhesion Test (ASTM F1842-09).

[0091] Conductive traces formed with the molecular ink are thinner, have lower resistivity, have better print resolution and are up to 8 times less rough than metal flake inks. In addition, the shear force required to remove light emitting diodes (LED) bonded to the traces using an epoxy adhesive is at least 1.2 times stronger than for commercially available flake-based inks. The substrate may be any printable surface. Printable surfaces may include, for example polyethylene terephthalate (PET) (e.g. Melinex™), polyolefin (e.g. silica-filled polyolefin (Teslin™)), polydimethylsiloxane (PDMS), polystyrene, polycarbonate, polyimide

(e.g. Kapton™), silicone membranes, textiles (e.g. cellulosic textiles), paper, glass, metal, dielectric coatings, among others. Flexible substrates are preferred.

[0092] The printed conductive trace on the substrate may be incorporated into an electronic device, for example electrical circuits, conductive bus bars (e.g. for photovoltaic cells), sensors, antennae (e.g. RFID antennae), touch sensors, thin film transistors, diodes, and smart packaging (e.g. smart drug packaging). The molecular ink of the present invention enables miniaturization of such electronic devices.

Electric components based on printed electronics

[0093] Molecular inks are used as described above to print traces of ink making up a circuit, or a portion thereof. Results of traces production are shown in Fig. 1.

[0094] Mechanical properties were tested using ASTM F1683-02 bend and crease testing, as shown in Fig. 2.

[0095] There are now described the advantageous properties of screen printable molecular silver inks. The purpose is to develop a screen printable ink that can produce functional, thin and high resolution traces with high conductivity and robust mechanical properties.

[0096] Electrical properties and trace resolution of screen printable molecular silver inks are shown in Fig. 3.

[0097] Shear force properties and environmental testing results are shown in Fig. 4.

[0098] Various devices can benefit from the properties described above. The basis for various devices includes building metal-insulator-metal (MIM) devices generally shown in Fig. 5A (metal on insulator on metal). Fig. 5B shows an inductor made of ink as described above printed on a substrate or on a

dielectric material (i.e., insulator between the metals). It shows that inductances can be built successfully by printing molecular ink.

[0099] The same result applies to capacitors (Fig. 5C) and electronic filters such as low-pass filters (Fig. 5D), which can also be built successfully by printing molecular ink. The electric behavior of the low-pass filter is also shown in Fig. 5E. It demonstrates that molecular-ink printed low-pass filters have a sharp rejection slope (i.e., it filters more sharply than flake inks) and have very low insertion loss in passband (i.e., the signal is not attenuated in useful frequencies). The response of this device is spurious-free up to 20 GHz. Because of the nature of the device (i.e., printed with the high circuit density allowed by molecular ink), the device is particularly compact.

[00100] Figs 6A-E are a profilometer image comparison of the molecular silver ink printed directly on Kapton substrate with no dielectric (a), encapsulated with a printed dielectric (b) and as a second layer on a printed dielectric layer (c), performance of the inductors printed on each surface (d) and a representative quality factor of the inductors (e), according to an embodiment;

[00101] Figs. 7A-C are a schematic (a) and a photograph (b) of a three layer, all-printed Metal-Insulator-Metal (MIM) capacitor on plastic substrate (Kapton) using the molecular silver ink and measurement results of three shunt MIM capacitors versus frequency (c), according to an embodiment;

[00102] Figs 8A-C are an optical micrograph of the TFT as an inset at the top right (a) and the linear fit to the transfer curve as an inset in the bottom left (a), an optical profilometer image of the transistor (b) and a cross section of the channel width (c), according to an embodiment.

Circuits made of molecular inks

[00103] Molecular inks can serve as new conductors for advanced user interfaces, circuits and devices.

[00104] Printed electronics is a disruptive manufacturing technique that combines functional materials and printing to make electronic devices in new form factors and enables innovative products. Printed electronics will yield breakthrough technologies in sensing, displays and wireless communication.

[00105] New molecular inks can be used to enhance the field of applications for printed electronics. This applies to both silver molecular inks and copper molecular inks described above.

[00106] Molecular inks described above provide the electrical performance of nanoparticle inks with cost of flake inks.

[00107] Molecular inks can be used to fabricate traces of ink with sub-micron trace thickness to enable production of narrow traces.

[00108] Molecular inks used in circuits have robust mechanical properties.

[00109] The use of molecular inks also allows the creation of new or improved polyimide flex circuits and a new solder-able surface finish for polyimide flex circuits or rigid epoxy printed boards.

[00110] These properties of molecular inks can be used to fabricate existing electronic products with these molecular inks to provide improved properties, or entirely new products.

[00111] There will now be described advantageous properties of molecular inks. These properties are suitable for circuit fabrication.

[00112] Molecular copper inks have low sintering temperature. Despite the ease of sintering and excellent electrical properties of silver inks, the high cost of silver is becoming an issue for printed applications where the main driver for printing is cost. Thus, there has been developed a low cost copper screen ink with the following properties: Low cost copper precursors; Sinters at 150°C making it compatible with PET (inexpensive flexible substrate); Resistivities 2 to 5 times bulk copper values; Good mechanical properties (ASTM F1683-02).

[00113] Copper inks are robust with respect to oxidation. They require simple preparation, they are stable and screen printable. Circuits made of molecular copper inks have the following properties: High resolution (2-20 mil line features); Sintering on PET as well as Kapton; Thermal sintering under N₂ (500 ppm O₂); Photonic sintering in Air; Resistivity 20-30 mΩ/sq/mil; Oxidation resistance Cu traces.

Applications of printed electronics using molecular inks and other inks

[00114] Printed electronics circuits, components, and devices can be developed using flexible or rigid substrates with silver flake, nanoparticles or molecular inks and semi-conductive, carbon nanotubes or dielectric inks. For reasons explained below, molecular inks are preferred for most applications and bring many advantages. In some cases (UHF filters), they are the only suitable choice.

[00115] Various applications are contemplated using these inks, such as the following:

- Passive or Active rigid or flexible Printed electronic components
- Flexible or rigid Printed circuits boards
- Solder-able Printed circuits boards
- Printed and Flexible heating elements, e.g., for aerospace applications
- Printed coplanar waveguides, RF antennas, RF filters and hybrid RF devices
- Printed RF engineered filtering or reflecting or orienting surfaces at selective frequency
- Printed flexible or rigid solar cells
- Printed semiconductor devices
- Printed Micro-wire flexible or rigid Touch interface

- Printed flexible or rigid transparent electrodes
- Printed capacitive or resistive flexible or rigid interface
- Printed and flexible electroluminescent lamps
- Wearable electronics printed on flexible substrate
- Printed physicochemical and electromechanical sensors

[00116] As described above, molecular inks have very advantageous mechanical and electrical properties, notably a higher trace resolution which allows for a greater circuit density and a better uniformity and thickness of the printed trace. These traces have greater mechanical flexibility and can withstand higher current densities. These advantages can be used for example in making filters (e.g., low-, high- or band-pass filters) with improved spectral resolutions over those achieved using flake inks. It can also be used to make an antenna usable with much higher frequencies than those that flake inks can withstand.

RF applications

[00117] An application of molecular inks is a screen printed ultra-high frequency (UHF) antenna. An embodiment of such an antenna is shown in Fig. 9. The use of molecular ink, with its electrical properties and great control of the surface profile of the trace, allows interacting with signals of frequencies that would be unreachable for other types of inks, such as flake inks.

[00118] Empirical results of the spectral behavior of an embodiment of antenna are shown in Fig. 10.

[00119] Measurement results: Top of graph: Location (in frequency) of TV channels in VHF-low, VHF-high and UHF bands. Lower value of S11 indicates higher received power (higher probability of receiving a channel at its designated frequency)

[00120] Measurement highlights: Antennas on Kapton (polyimide) and Melinex (PET) substrates produced similar performance in the UHF band. Antennas on Melinex provided a better response in the upper VHF band.

[00121] Various properties and remarks are shown in Table 1.

[00122] **Table 1: Properties of printed antennas**

Property	Highlight
Multiple Modes of Processing	• Thermal and Photonic sintering possible on Kapton and PET-based substrates with similar results
Sheet resistivity	• 3-8 mΩ/□/mil with both thermal and photonic sintering
Resolution	• Print lines as narrow as 41 μm
L/S	• Print 76 μfeatures with as little as 36 μm space between features
Multilayer Printability	• Printed ink produces uniform traces on rough dielectric surfaces with excellent electrical properties • Demonstrated MIM devices, UHF antenna
Flexibility	• < 10% change in resistance following ASTM F1683-02 flex and crease testing
Adhesion	• Grade 5B following ASTM F1842-09 testing
Silver Migration	• None - following encapsulation with PF455 dielectric
Component Assembly	• Compatible with commercially available Ag adhesive, allowing 2X stronger bond of LED than to typical commercially available flake ink

Telecom applications

[00123] Mobile wireless traffic is expected to increase by a factor of a thousand in the upcoming years. Adopting smaller cells and packing them closer together are among the methods to keep up with this significant surge. Drawbacks to this approach include the increased number of base station towers required and the increased intercellular interference. Such interference will degrade the system performance causing packet losses and throughput reductions, and it may also compromise the security and privacy of the system. Therefore, it is critical to develop innovative solutions that allow systems to operate effectively in the presence of interference and achieve increased mobile broadband capacity. Two approaches are being considered: engineered surfaces and wrap-around shaped-beam arrays. Both involve printed RF engineered

filtering or reflecting or orienting surfaces at a selective frequency band using the inks mentioned above.

[00124] Engineered surfaces are single or multiple sheets of large and thin flexible dielectric substrates patterned with a periodic lattice of small metallic or dielectric elements and which can be advantageously made of the inks mentioned above.

[00125] These surfaces are designed to selectively reflect or transmit electromagnetic signals based on their frequency and can be used to block, re-direct or transmit signals. These engineered surfaces can be deployed on flat or curved surfaces to reduce interference and/or improve wireless coverage and spectrum efficiency. More specifically, they can be deployed on buildings or urban infrastructures to environmentally manage electromagnetic signals in a controlled manner to improve broadband mobile coverage. This opens up new possibilities for using higher-frequency bands, since the current propagation models for urban environments, which indicate poor signal performance at higher bands, are based on the assumptions that the electromagnetic propagation cannot be controlled. These resources have long gone unnoticed and underused as a result of their sheer expanse and the technical complexity of covering them with low-cost, durable, and flexible materials with superior electrical performance.

[00126] Printed electronics (PE) can overcome the technical barriers of the integration of engineered surfaces into buildings and urban infrastructures, thanks to their fabrication methods. Furthermore, they can henceforth be used to alter and manage the signal environment within the coverage area.

[00127] Wrap-around shaped-beam arrays can be deployed for outdoor environments, for example, on lamp-posts, to convert this prevalent and ubiquitous piece of infrastructure into the backbone of a network of base stations with large angle coverage, eliminating the costly deployment of visually unappealing base station towers. Such a development will be low cost due to the

current availability of real estate and electrical power at each post. In addition, the radiation beams of these arrays can be adjusted in order to reduce the amount of spillover into adjacent cells, thus reducing interference. For indoor environments, this same strategy can be used to blend wrap-around shaped-beam arrays into pillars to extend the reach of signals into dead zones and increase the channel capacity by shaping the coverage.

[00128] Printed electronics (PE) can be used advantageously to enable the design and facilitate deployment of large-area engineered surfaces and wrap-around shaped-beam arrays. This can be achieved by deploying large and flexible PE generated sheets as wallpaper onto the facade and/or inner surfaces of buildings or urban infrastructures. These flexible PE generated sheets can use various inks as mentioned above, e.g., flake inks but also molecular inks.

[00129] Notable examples of such a development are installation of frequency selective surfaces (FSS) to block/allow certain frequency bands or optically semi-transparent ink-based structures to control signal penetration into buildings without compromising natural lighting exposure of the indoor area. PE generated surfaces can also be designed to selectively shape the coverage to remove dead zones or shadow areas. Another relevant and highly versatile extension of these advanced developments will be on-demand exclusion of certain coverage areas in compliance with cyber-security requirements. The second objective is the deployment of wrap-around shaped beam arrays around lamp-posts or pillars using PE, thus reducing the need for additional tower infrastructure. Such a development is now possible due to the flexible nature of PE generated surfaces. Novel semiconductor and ferroelectric inks, that have been under intensive development in the facilities of NRC and PE consortium members, will significantly broaden the functionality of these wrap-around shaped-beam arrays by enabling dynamic variation of operating frequency, coverage, and/or signal polarization, which are key features in the realization of higher channel capacity.

[00130] This control of the electromagnetic environment is enabled by the unique features of PE to realize engineered surfaces and wrap-around antenna arrays on flexible substrates requiring conformal realization (operates independently of orientation, which is not possible in conventional planar counterparts) that can cover large footprints. In addition, this technology can take advantage from the potential of emerging adaptive materials such as ferroelectrics for the implementation of tunable structures, shaped beam arrays, and tunable engineered surfaces. Flexible engineered surfaces have been developed at ISM bands or other microwave frequencies bands of interest using different printing technologies such as screen printing and ink jet.

[00131] A practical implementation of this technology would be engineered surfaces (ES) for 5G mobile wireless communications.

[00132] In order to be used in this context engineered surfaces are deployed at strategic locations to alter the propagation environment for coverage control, security enhancement and/or interference mitigation. For coverage control, two types of engineered surfaces need to be provided: class 1 surfaces, which are frequency-selective whose reflection/transmission properties are a function of frequency, normally used on the envelope of a building; and class 2 surfaces, which are reflectarray gratings or diffusers that redirect signals, normally used within building (e.g., on walls). Installation of both classes of ES is shown in Fig. 11.

[00133] Printed electronics unique features make possible the fabrication of both classes of engineered surfaces with the inks mentioned above. Flake inks, conductive inks comprising nanoparticles and semiconductor inks (either organic or inorganic) can be used for this purpose. Molecular inks as those described above are also contemplated.

[00134] Class I: Frequency Selective Surfaces (FSS) can be deployed on walls/windows to block/transmit signals for given zones (security). They can also

mitigate interference between adjacent zones. Class 1 FSS are shown in Figs. 12A-12D, where various appropriate ink-trace patterns are illustrated. Class 1 FSS can be printed on flexible substrates, including transparent substrates for greater subtlety or for installation in windows. Multi-layer FSS performance is shown in Figs. 13A-13B.

[00135] Class 2 reflectarray gratings or diffusers can be deployed at strategic locations to illuminate blind spots in a building, enhance multipath signal transmission, enhance coverage or create quiet zones. This kind of advantageous signal alteration is shown in Fig. 14.

[00136] Suitable ink-trace patterns for Class 2 reflectarray gratings are shown in Fig. 15.

[00137] An FSS-backed Reflectarray Grating performance can be characterized using the device shown in Fig. 16A. The resulting reflection spectrum is shown in Fig. 16B.

[00138] Printed electronics also enable various functions useful for implementing 5G mobile network technology. For example, multilayer ES can be developed based on a combination of metal and dielectric inks for higher frequencies in the 5G bands. Reconfigurable ES are also made possible using printed electronic or electro-mechanical technologies to enable real-time dynamic control.

Aerospace applications

[00139] As described above, various types of inks can be used on a variety of substrates. For example, silver flake, nanoparticles or molecular inks and semi-conductive, carbon nanotubes or dielectric inks can be used on flexible or rigid substrates.

[00140] Mechanical and electrical properties described above demonstrate the potential reliability of products made of such inks and substrates.

[00141] Aerospace is a field where high reliability is required for product integration into aircraft subsystems. An example of an application of printed electronics in aircrafts is the introduction of printed and flexible heating elements into components that need to be heated. Wings of an aircraft are an example of an aircraft subsystem that needs to be heated during travel to avoid ice buildup on the wings exposed to very low temperatures.

[00142] Currently, plane wings are heated using aluminum tubes that bring hot air from the engine into the wing to prevent the formation of ice on the wings. These aluminum tubes are nonetheless heavy and change the weight profile of the wings.

[00143] It would be preferable if these aluminum tubes were absent from the wings.

[00144] The use of printed and flexible heating elements comprising printed electronics made of the inks mentioned above can replace these aluminum tubes.

[00145] The flexible nature of these heating elements allows coating the inside of the wings with these heating elements with great ease.

[00146] Moreover, the substrate of these heating elements is usually thin and lightweight. The substantial addition of weight in the wings of the aircraft is thereby avoided. This has an advantageous impact on the weight of the wings and of the aircraft, improving the overall performance.

[00147] Furthermore, the substrate can coat the inner surface of the wings. Since the circuits make up a greater fraction of the substrate's surface, the wings can be heated substantially uniformly over their surface. The heating by the heat dissipation from the circuit can also be tailored to specific parts of the wing to heat more the wing parts that need more heat to prevent ice buildup. Therefore, heat is used more efficiently and energy waste is reduced.

Solar cells

[00148] According to an embodiment, molecular ink as described above can be used for the manufacture of solar cells and solar panels. Because of its high conductivity and high resolution capabilities, the molecular ink as described is particularly suitable for the printing of solar cells bus bars.

Conductive films and touch interfaces

[00149] According to an embodiment, the molecular ink can be used to manufacture printed transparent conductive electrodes or transparent conductive films because of its high conductivity and high resolution capabilities. This transparent conductive film enables the manufacturing of printed capacitive micro-wire touch interfaces.

[00150] The advantage with this embodiment is the ability to design and build quickly custom prototypes of touch screens. For example, laser companies have developed an ITO laser etching process that enables capacitive touch screen manufacturing. This technology is based on laser ablation of an ITO film followed by bus bar printing and/or trimming. The embodiment described herein is rather based on printing silver electrodes grid or pattern on a flexible substrate, which if necessary could be followed by a laser ablation process to trim the printed traces and produce the capacitive interface. This is more efficient to design and build quickly custom prototypes of touch screens.

[00151] Another embodiment comprises performing a selective sintering of traces printed using the Ag molecular ink. This embodiment has the advantage to produce much more flexible.

[00152] Therefore, molecular ink can be advantageously used in a process where conductive traces produced by direct printing and laser trimming and/or printing and selective sintering of a molecular conductive ink are used to produce flexible or rigid micro-wire capacitive touch interfaces.

Hybrid integration of discrete active switches

[00153] This activity focused on heterogeneous integration platform of off-the-shelf discrete MMIC switch chip and flexible substrate enabled by screen printing. Combines traditional small-area low-power and high-performance System-on-Chip (SoC), flexible substrate and cost-effective Printed Electronics (Figs. 17-24).

[00154] Proof of concept of the traditional system-on-chip and printed electronics combined in the hybrid system was developed that offer both their advantages. The preliminary measurement result indicates that printed electrodes have comparable performance with the commercial ones. The SoC and printed electronics based hybrid system offers a promising solution for the next generation of fully reconfigurable and adaptive engineered surfaces.

CPW lines and filters

[00155] Broadband Material Characterization of CPW Lines and Filters Implemented on Plastic Substrate (Figs. 25-28).

Micro-wires

[00156] Capacitive technology "Field coupled" with "printed Micro-wires" is shown in Fig. 29. It is a matter of printing a grid of conductive traces connected to one or more common traces in X and Y with a screen printing process. The width of the printed traces would be between 10 μM and 50 μM so that the conductivity and transparency of the device is acceptable.

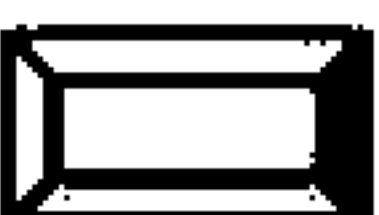
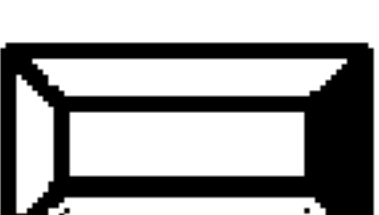
Thermoforming High Temperature Molecular Silver Inks

[00157] It is demonstrated that the silver molecular inks can be photonicallly sintered on Kapton substrate to produce conductive traces sheet resistivity values of $\sim 8.3 \text{ m}\Omega/\square/\text{mil}^7$. Photonic sintering is of interest because it enables the processing of traces in milliseconds as opposed to tens of minutes with thermal processing methods. Also it is demonstrated that photonic sintering enables

processing of the molecular inks on low temperature substrates such as polyethylene terephthalate, polyethylene naphthalate, polycarbonate and glycol-modified polyethylene terephthalate (PETG). The ability to use IPL to process inks on PETG is valuable as it opens up the potential to investigate unique methods to produce in-mold electronic circuits. A potential advantage to using a combination of molecular inks and photonic sintering to produce in-mold electronics lies in the fact that the traces can be molded prior to being converted to conductive traces and subsequently converted to a conductive 3D trace using photonic energy. This not possible with silver flake inks, and is a unique process that can only be carried out using molecular inks.

[00158] In generating conductive 3D molded traces, it is important to first understand how the geometry of the features formed into the traces will impact the substrate, and more importantly, the traces printed onto the substrates. As summarized in Table 2, the average and maximum strain imparted on the substrate when oval and circular domes are formed into it are ~ 16% and 40%, respectively. When objects with more advanced geometries such as rectangular prisms with 55 to 75° off-of-vertical angles are formed into the substrate, the average and maximum strain imparted on the substrate can be as much as 40% and 75%, respectively. In addition, crater based objects with combinations of angled and rounded features also impart significant strain on the substrate. With knowledge of the strains imparted on the substrate following the thermoforming of the objects of interest, we next investigated how electrical properties of traces were affected through the thermoforming/photosintering process.

[00159] **Table 2.** The geometry of the objects used to mold 3D shapes into the PETG substrate and the measured strain imparted on the substrate as a result of the thermoforming process.

geometry	Side view of template	Top view of template	Angle (° off of vertical)	strain average (%)	strain maximum (%)
Oval			-	15 ± 1	30
Dome			-	16 ± 6	26
Rectangular prism			75	40 ± 16	75
Rectangular prism			65	37 ± 14	60
Rectangular prism			55	23 ± 10	30
Crater (outside)			75	32 ± 12	50
Crater (inside)			75	39 ± 13	50
Crater (outside)			65	28 ± 12	40
Crater (inside)			65	38 ± 9	50
Crater (outside)			55	23 ± 8	30
Crater (inside)			55	36 ± 10	50

[00160] As highlighted in Table 3, the relative resistance increase (R/R_0) is less than 1.4 in comparison to unformed traces when molding oval and round dome features into the molecular ink traces. Also of note, the process of thermoforming the traces prior to producing conductive traces enables the generation of functional traces with linewidths as narrow as 160 μm . In expanding the scope of geometries, this process also enabled the generation of conductive 3D rectangular prism features into the silver traces. As highlighted in

Table 3, the relative resistance increase for 160 to 550 μm features was also under 1.4 in comparison to unformed traces. This is particularly impressive considering the traces are under average strains of ~20-40% and experience maximum strains as high as 75% (Table 2). There are very few examples in the literature where highly conductive traces can be subjected to strains greater than 50% without significant decrease in electrical performance. Finally, crater structures that have both angled and rounded features were also molded into the traces. Though the average strain imparted on the traces is not as high as in the rectangular prisms, these structures are of interest because a large area of the trace is experiencing the strain (2/3 by area). Despite this significant area of the traces experiencing strain, the relative resistance increase for 160 to 550 μm features was between 1.5 and 2.8 in comparison to the unformed traces (Table 3).

[00161] **Table 3.** The relative resistance changes measured for the traces the have been printed onto PETG, thermoformed and photosintered to produce a conductive silver trace.

geometry	Side view of template	Top view of template	Angle ($^{\circ}$ off of vertical)	Relative resistance ($R_{\text{formed}}/R_{\text{unformed}}$)				
				Line width (μm)				
				551 $\pm$ 22	469 $\pm$ 19	358 $\pm$ 14	215 $\pm$ 8	164 $\pm$ 6
Oval			-	1.4 $\pm$ 0.1	1.3 $\pm$ 0.2	1.0 $\pm$ 0.1	0.8 $\pm$ 0.1	1.3 $\pm$ 0.1
Dome			-	1.1 $\pm$ 0.1	1.2 $\pm$ 0.1	1.2 $\pm$ 0.1	1.2 $\pm$ 0.2	1.3 $\pm$ 0.1
Rectangular prism			75	1.3 $\pm$ 0.2	1.3 $\pm$ 0.1	1.2 $\pm$ 0.1	1.0 $\pm$ 0.1	1.4 $\pm$ 0.2
Rectangular prism			65	1.1 $\pm$ 0.1	1.1 $\pm$ 0.1	1.1 $\pm$ 0.1	1.0 $\pm$ 0.1	1.3 $\pm$ 0.1
Rectangular prism			55	1.2 $\pm$ 0.1	1.3 $\pm$ 0.1	1.1 $\pm$ 0.1	1.0 $\pm$ 0.1	1.4 $\pm$ 0.5
Crater			75	1.5 $\pm$ 0.2	1.8 $\pm$ 0.1	2.0 $\pm$ 0.9	1.8 $\pm$ 0.6	2.6 $\pm$ 0.5
Crater			75	1.5 $\pm$ 0.2	1.6 $\pm$ 0.1	1.5 $\pm$ 0.1	1.8 $\pm$ 0.2	2.8 $\pm$ 0.4
Crater			65	1.4 $\pm$ 0.1	1.5 $\pm$ 0.1	1.5 $\pm$ 0.2	1.7 $\pm$ 0.3	2.3 $\pm$ 0.1

[00162] In conclusion, the ability to thermoform printed traces of the molecular ink prior to converting the ink into its metallic state enables the production narrow, molded traces with line widths as narrow as 164 μm linewidths formed into quite complex shapes and large areas of strain (2/3 of the traces formed over the traces experience strain) with excellent electrical properties.

[00163] While preferred embodiments have been described above and illustrated in the accompanying drawings, it will be evident to those skilled in the art that modifications may be made without departing from this disclosure. Such modifications are considered as possible variants comprised in the scope of the disclosure.

CLAIMS:

1. An electronic device comprising a printed substrate comprising a trace of molecular ink thereon, the molecular ink being sintered to form a conductive metal trace forming the electronic device, wherein the molecular ink is chosen from:

a) a flake-less printable composition of 30-60 wt% of a C₈-C₁₂ silver carboxylate, 0.1 -10 wt% of a polymeric binder and balance of at least one organic solvent, all weights based on total weight of the composition; or

b) a flake-less printable composition of 5-75 wt% of bis(2-ethyl-1-hexylamine) copper (II) formate, bis(octylamine) copper (II) formate or tris(octylamine) copper (II) formate, 0.25-10 wt% of a polymeric binder and balance of at least one organic solvent, all weights based on total weight of the composition,

wherein the molecular ink is stretchable and thermoformable, and

wherein the molecular ink is printed as an in-mold electronic device.

2. The electronic device of claim 1, wherein the substrate is flexible.

3. The electronic device of claim 1, wherein the trace of molecular ink is molded prior to being converted to the conductive metal trace.

4. The electronic device of claim 3, wherein the conductive metal trace is converted to a conductive 3D trace on the substrate using photonic energy.

5. The electronic device of claim 1, wherein the electronic device comprises an inductor.

6. The electronic device of claim 1, wherein the electronic device comprises a capacitor.

7. The electronic device of claim 1, wherein the electronic device comprises an electronic filter.

8. The electronic device of claim 1, wherein the electronic device is printed as a solder-able printed circuit board.
9. The electronic device of claim 1, wherein the electronic device is printed as one of: a coplanar waveguide, an ultra high-frequency RF antenna, an ultra high-frequency RF filter and a hybrid RF device.
10. The electronic device of claim 1, wherein the electronic device is printed as an engineered surface for frequency-selective RF filtering, reflecting or orienting.
11. The electronic device of claim 1, wherein the electronic device is printed as a solar cell.
12. The electronic device of claim 1, wherein the electronic device is printed as a transparent electrode.
13. The electronic device of claim 1, wherein the electronic device is printed as an electroluminescent lamp.
14. The electronic device of claim 1 or 2, wherein the electronic device is printed as a wearable electronic device.
15. The electronic device of claim 1, wherein the electronic device is printed as one of a physicochemical and an electromechanical sensor.
16. A heater comprising an electronic device comprising a printed substrate with a trace of molecular ink thereon, the molecular ink being sintered to form a conductive metal trace forming the electronic device,

 wherein the molecular ink is stretchable and thermoformable, and

 wherein the molecular ink is printed as an in-mold electronic device.
17. The heater of claim 5, wherein the substrate is flexible.
18. The heater of claim 16, wherein the trace of molecular ink is molded prior to being converted to the conductive metal trace.
19. The heater of claim 18, wherein the conductive metal trace is converted to a conductive 3D trace on the substrate using photonic energy.

20. The heater of claim 16, wherein the electronic device is printed as an in-mold resistive electrical circuit.

21. The heater of claim 20, wherein the electronic device is installed on a portion of an aircraft.

22. The heater of claim 21, wherein the electronic device is installed on a wing of an aircraft to heat the wing.

23. The heater of claim 22, wherein the electronic device is used to maintain the performance of aircrafts, emergency systems, electric vehicles, lighting systems or building automation system.

24. The heater of any one of claims 16-20, wherein the electronic device is installed on a battery to heat the battery.

25. The heater of claim 24, wherein the battery is a lithium battery.

26. A touch interface comprising a printed substrate comprising a trace of molecular ink thereon, the molecular ink being sintered to form a conductive metal trace forming a transparent conductive film,

wherein the molecular ink is stretchable and thermoformable, and

wherein the molecular ink is printed as an in-mold electronic device.

27. The touch interface of claim 26, wherein the trace of molecular ink is molded prior to being converted to the conductive metal trace.

28. The touch interface of claim 27, wherein the conductive metal trace is converted to a conductive 3D trace on the substrate using photonic energy.

29. The touch interface of claim 26, further comprising printed capacitive or resistive elements.

30. The touch interface of claim 26, further comprising a capacitive micro-wire.

31. The touch interface of claim 29, wherein the capacitive micro-wire comprises a grid or pattern of printed silver electrodes and the substrate is flexible.

32. The touch interface of claim 30, wherein the capacitive micro-wire is made by laser trimming the conductive metal trace and thereby making the micro-wire capacitive.

33. The touch interface of claim 30, wherein the capacitive micro-wire is made by performing a selective sintering of the conductive metal trace.

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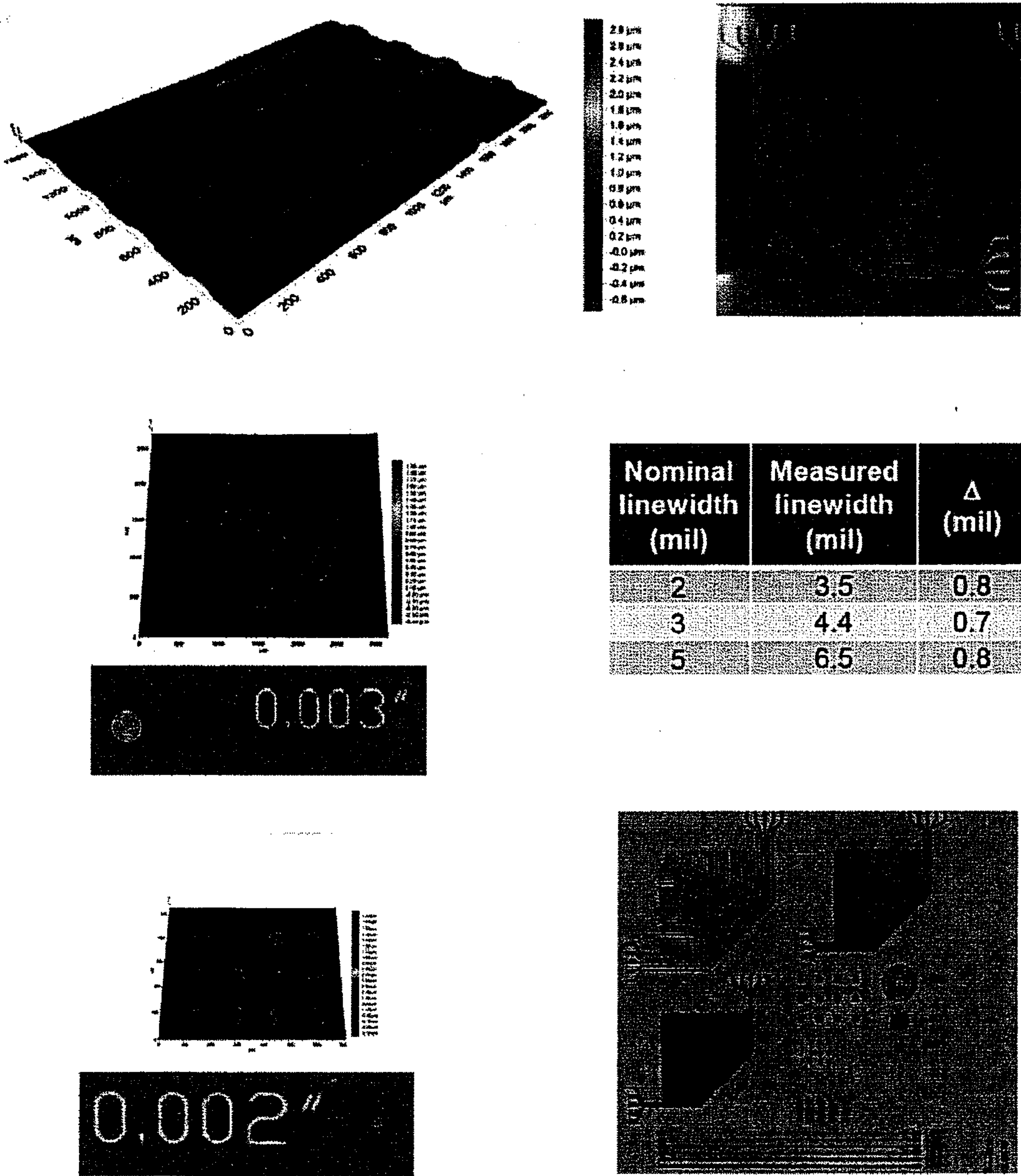
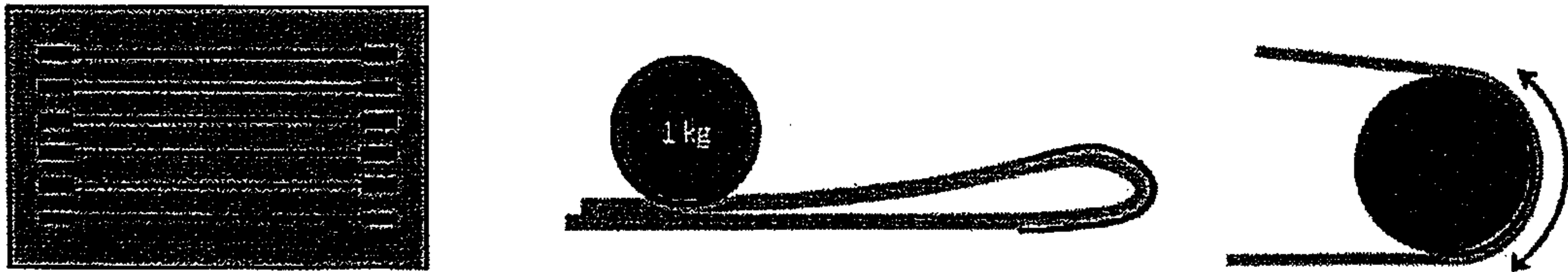


FIGURE 1

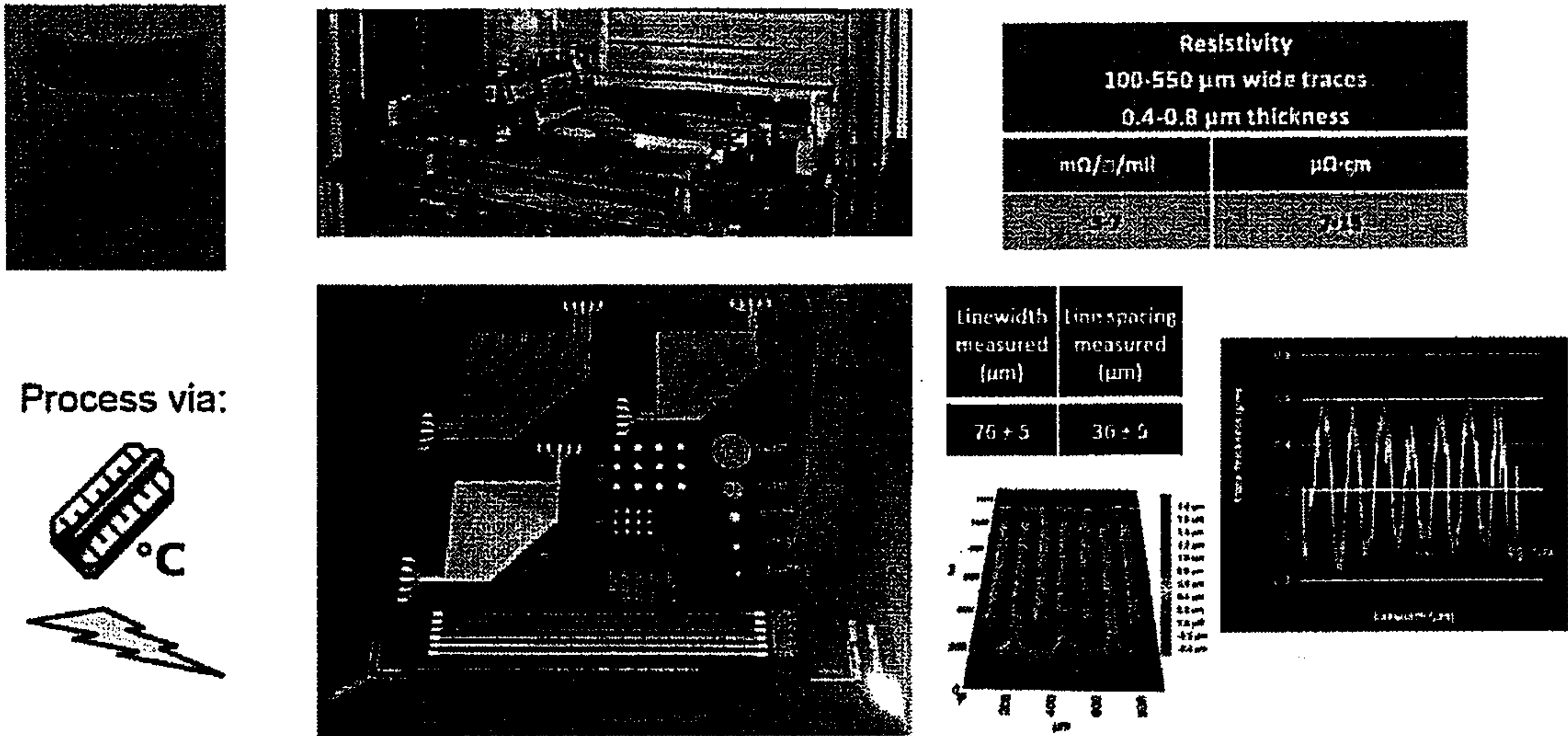
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Processing Method	Thickness (µm)	Flex (100-550 µm traces)	Crease (100-550 µm traces)
Photonic	~1	☑ R increase < 10%	☑ R increase < 10%
Thermal	~1	☑ R increase < 10% (compressive)	☑ R increase < 20%

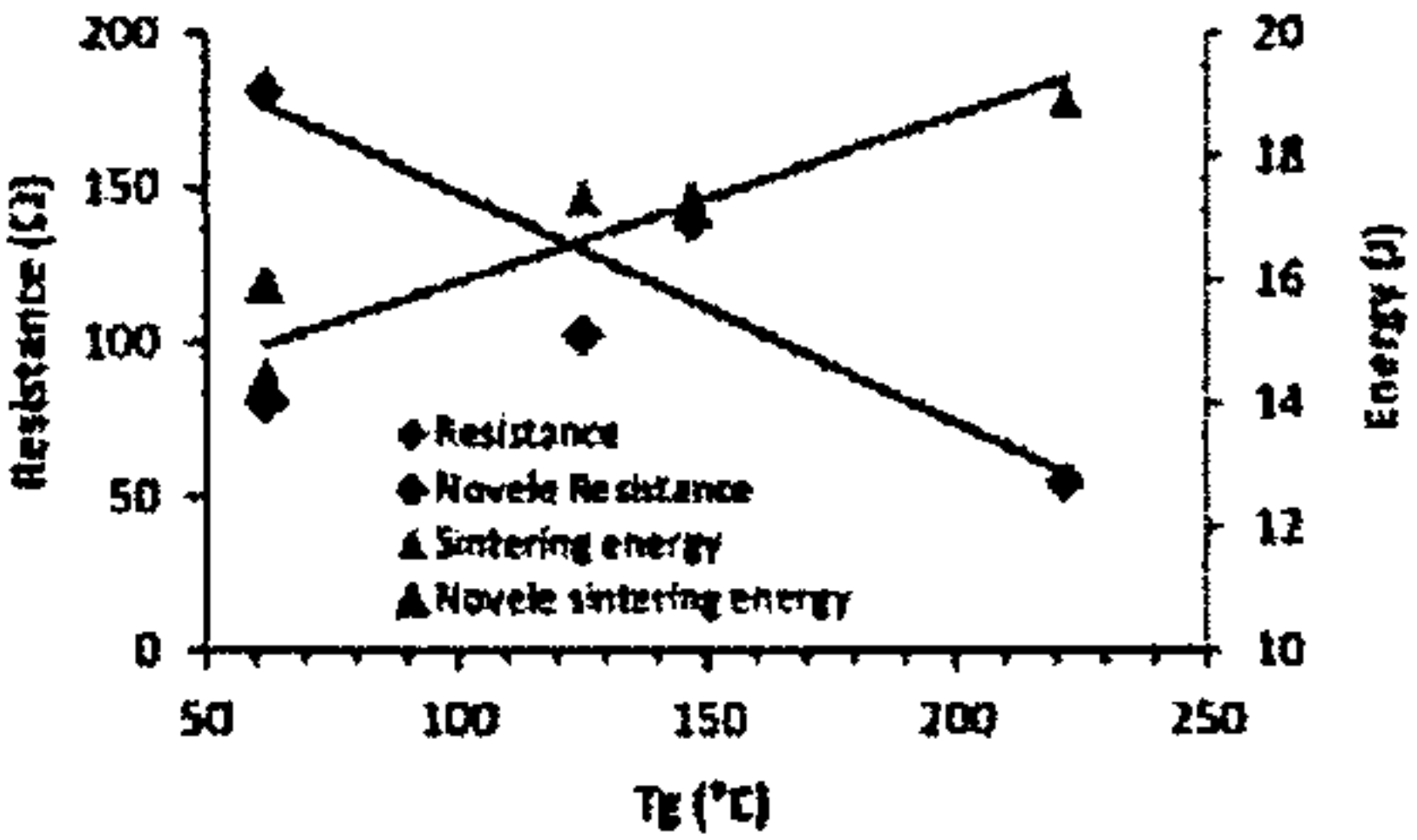
FIGURE 2

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Photonic Sintering on Various Substrates

Substrate	Optimal sintering energy, J/cm ²	T _g , °C	Resistance of 550 μm line, Ω
Mylar®	19.47	82	161 $\pm$ 6
Novolex	22.41	82	81 $\pm$ 3
Tecovest	29.38	125	100
Lexan®	35.38	147	138
Kapton®	38.96	222	55 $\pm$ 0.1



Current Carrying Capacity

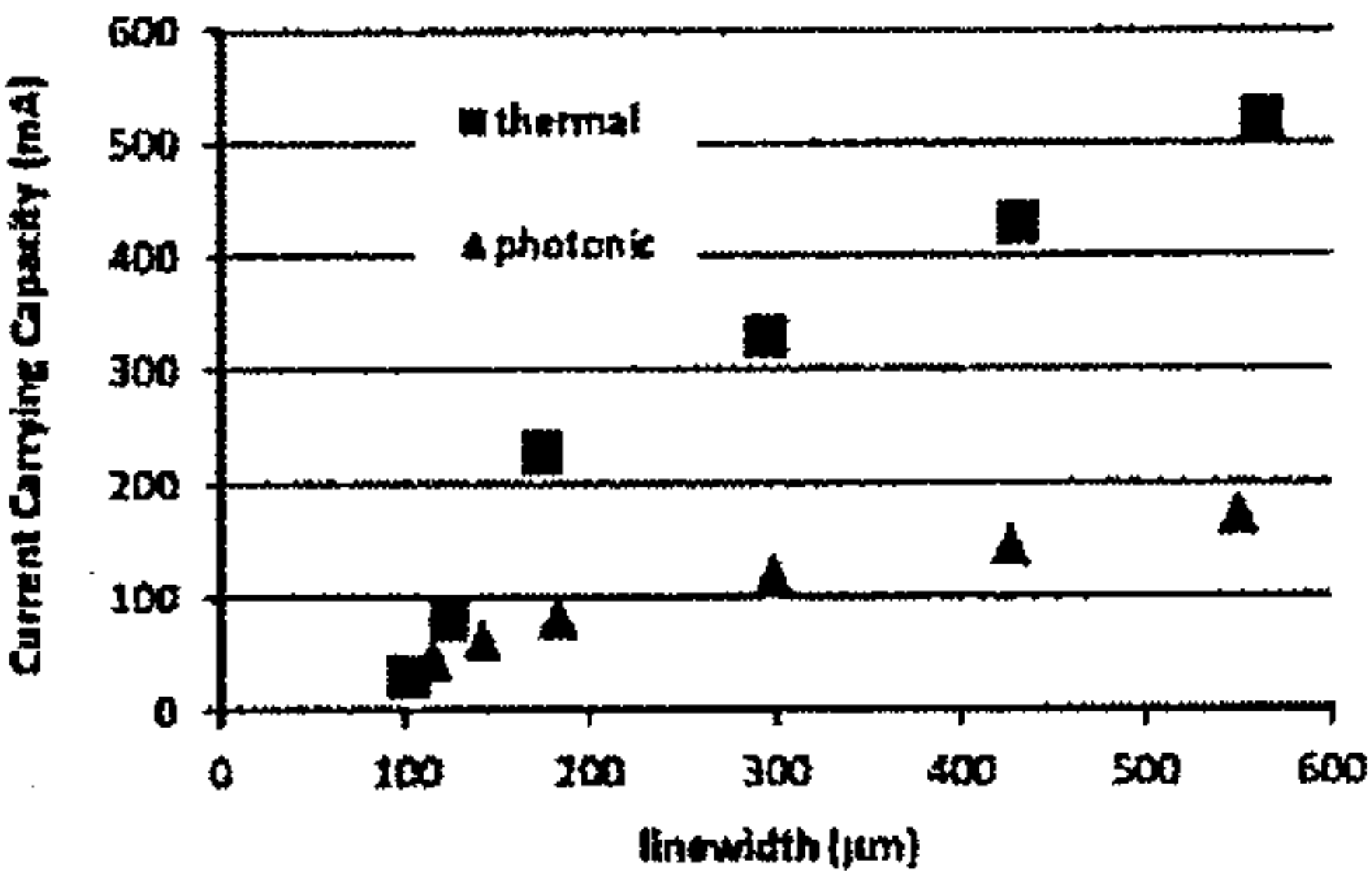


FIGURE 3

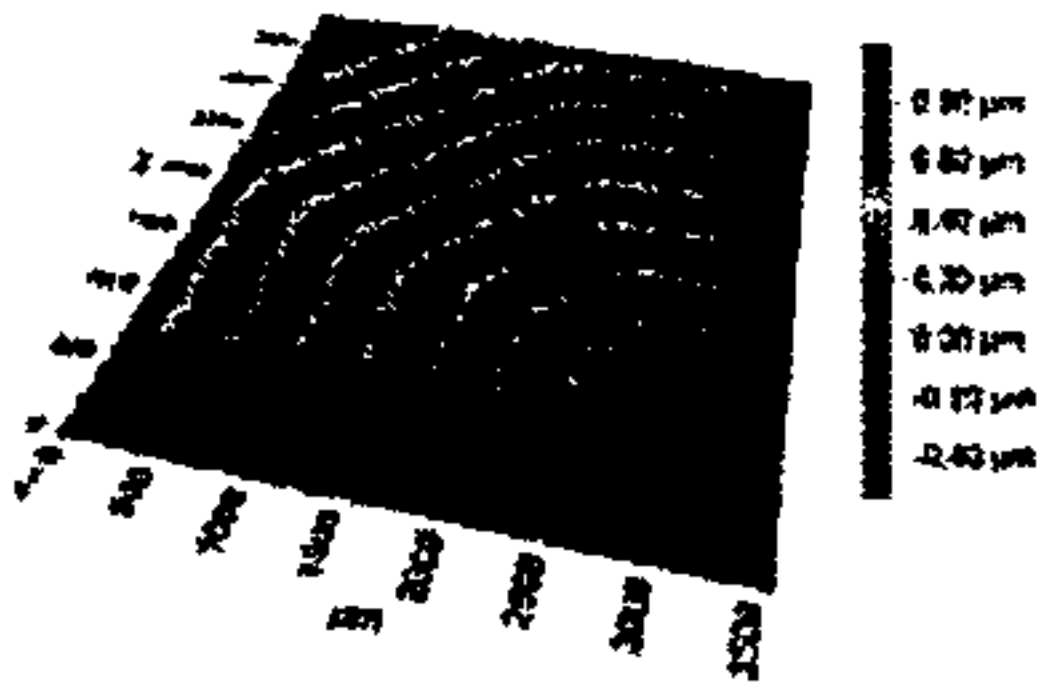
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IPC Shear Force Test (No encapsulation on LED)

RDRD-13063 Shear Test on LED (with silver epoxy-based adhesive)					
	Flake Ink on PET (lbs)	Molecular Ink Photosintered on PET (lbs)	Flake Ink on Polyimide (lbs)	Molecular Ink Thermosintered on Polyimide (lbs)	Molecular Ink Photosintered on Polyimide (lbs)
LED 1	1.90	3.70	3.85	5.30	4.95
LED 2	3.20	3.60	3.85	4.80	4.10
LED 3	1.85	4.60	3.85	4.30	4.05
LED 4	2.40		3.55	4.75	4.90
LED 5					5.15
Minimum	1.85	3.60	3.55	4.30	4.05
Average	2.09	3.97	3.73	4.78	4.63
Maximum	2.40	4.60	3.85	5.30	5.15



Adhesive force is higher on Molecular Silver Ink



Silver Migration (ASTM F1996-01 Silver migration test)

- 5 and 10 mil traces held at 40°C/95% RH for 10 days, 20V DC
- No migration

FIGURE 4

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Metal-Insulator-Metal (MIM Devices)

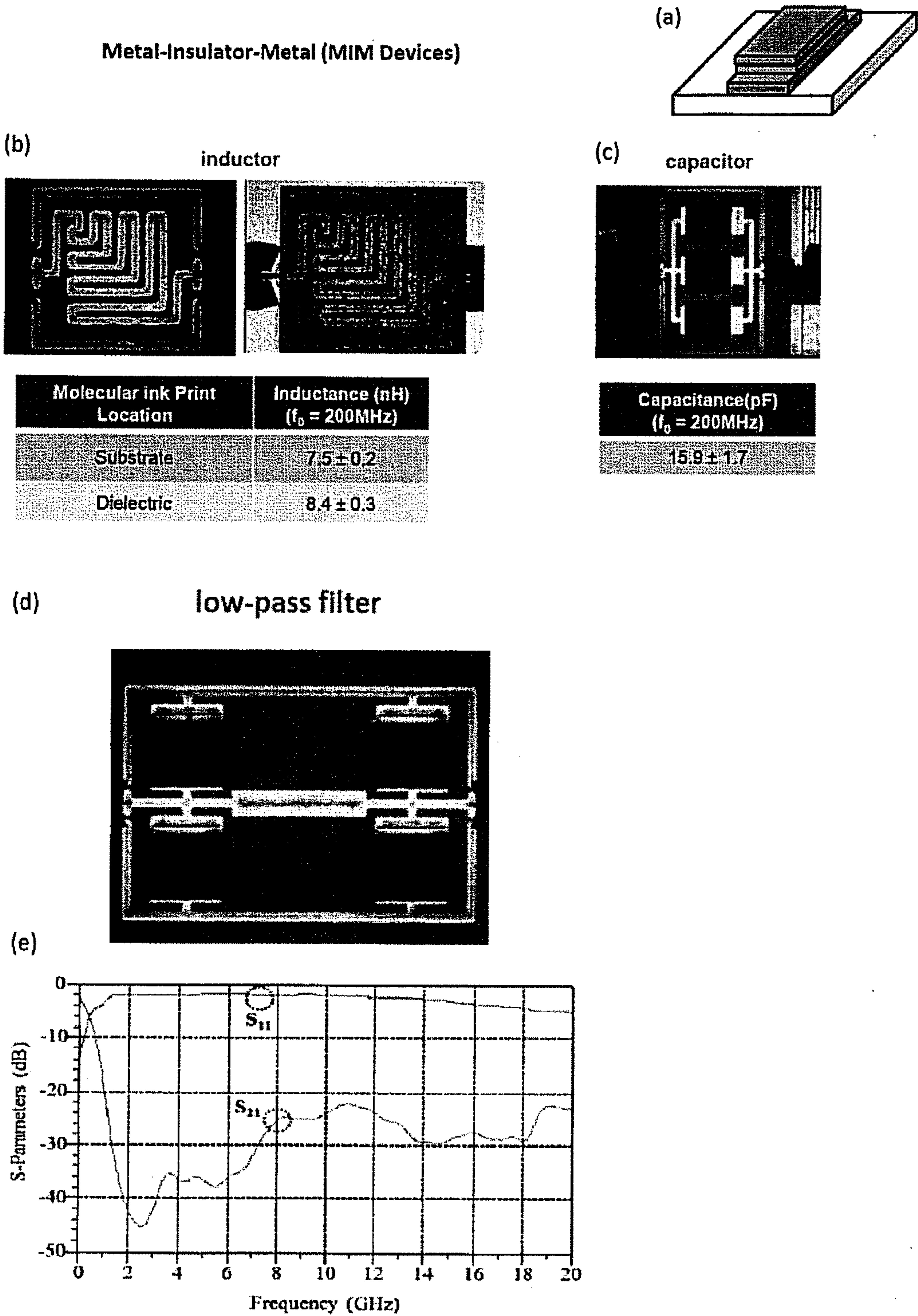


FIGURE 5

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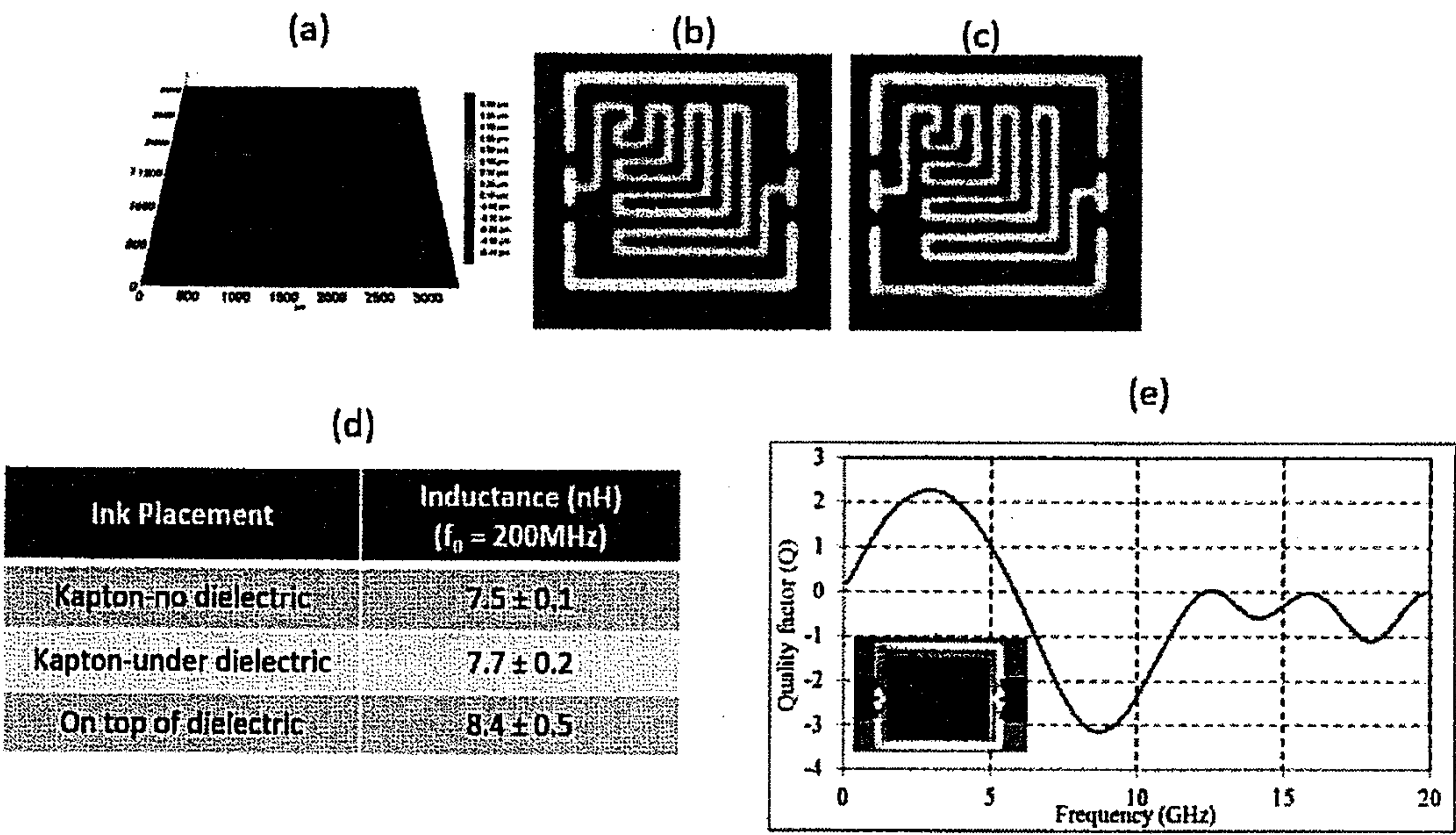


FIGURE 6

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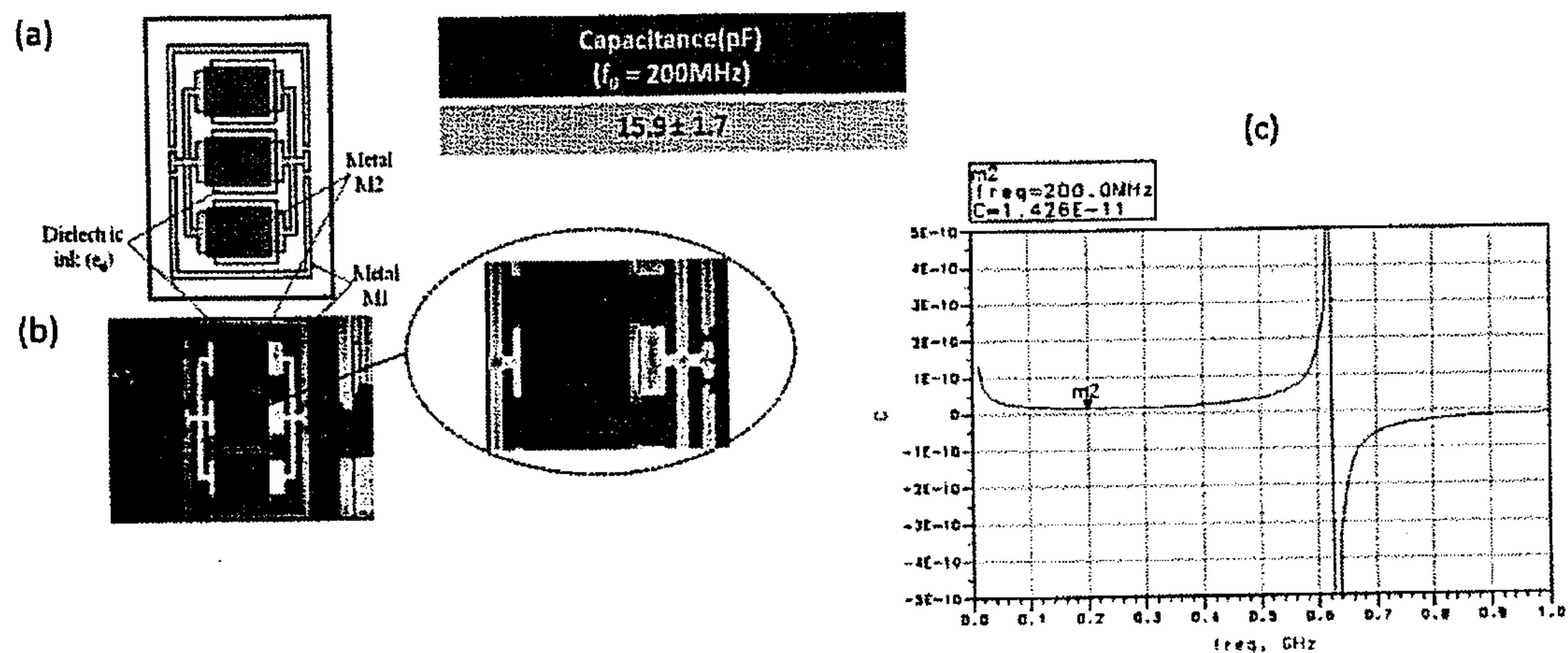


FIGURE 7

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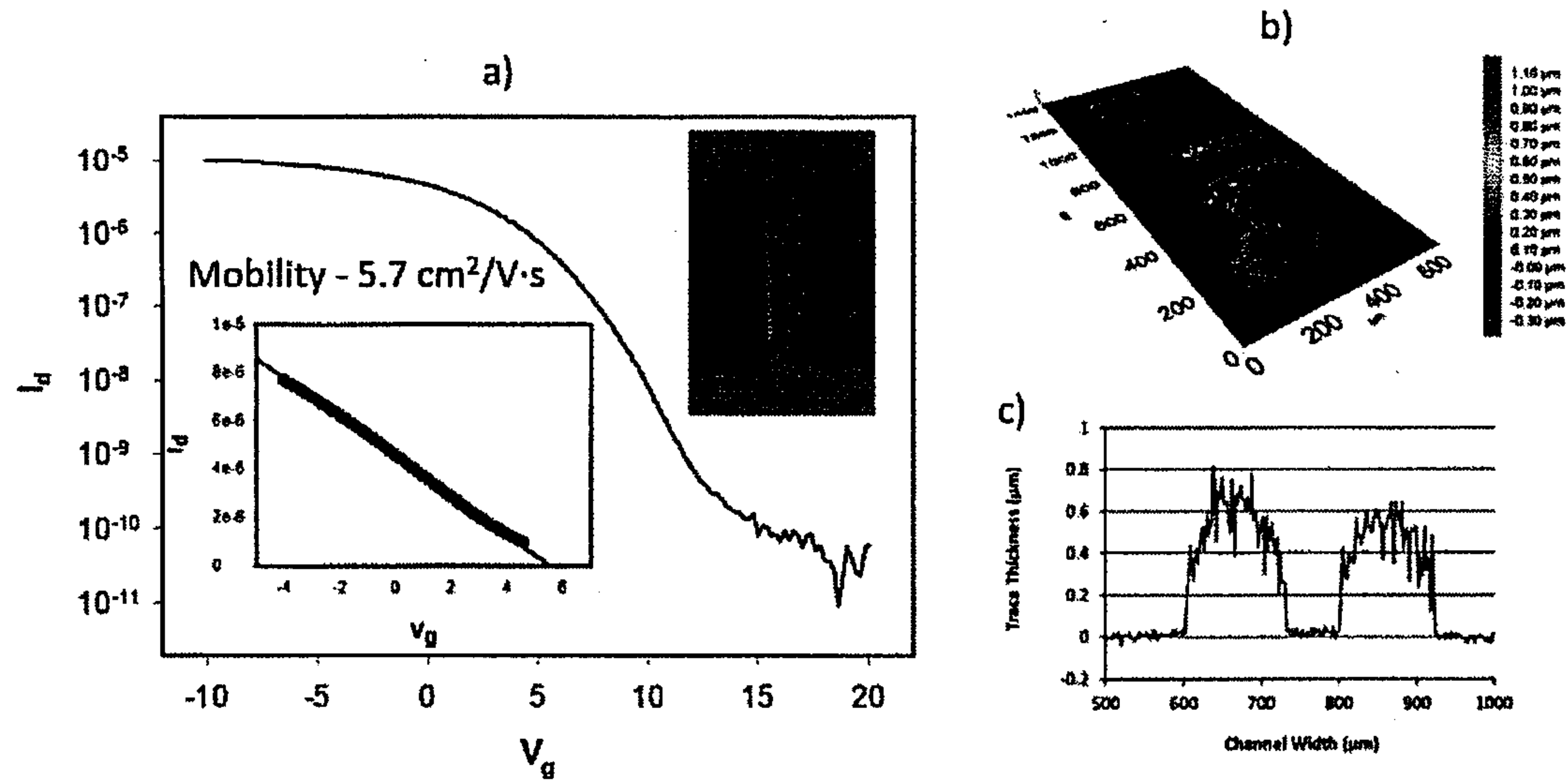


FIGURE 8

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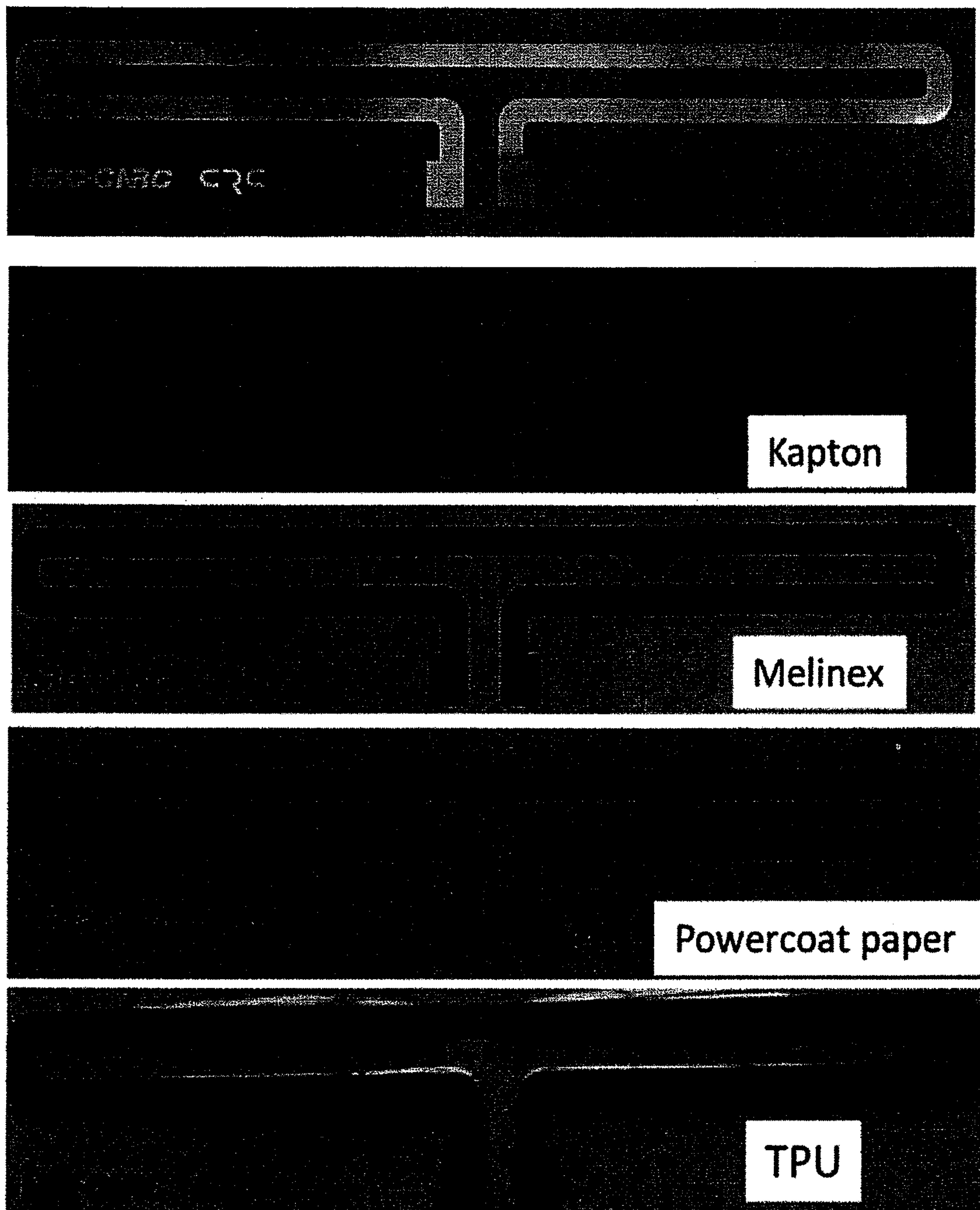
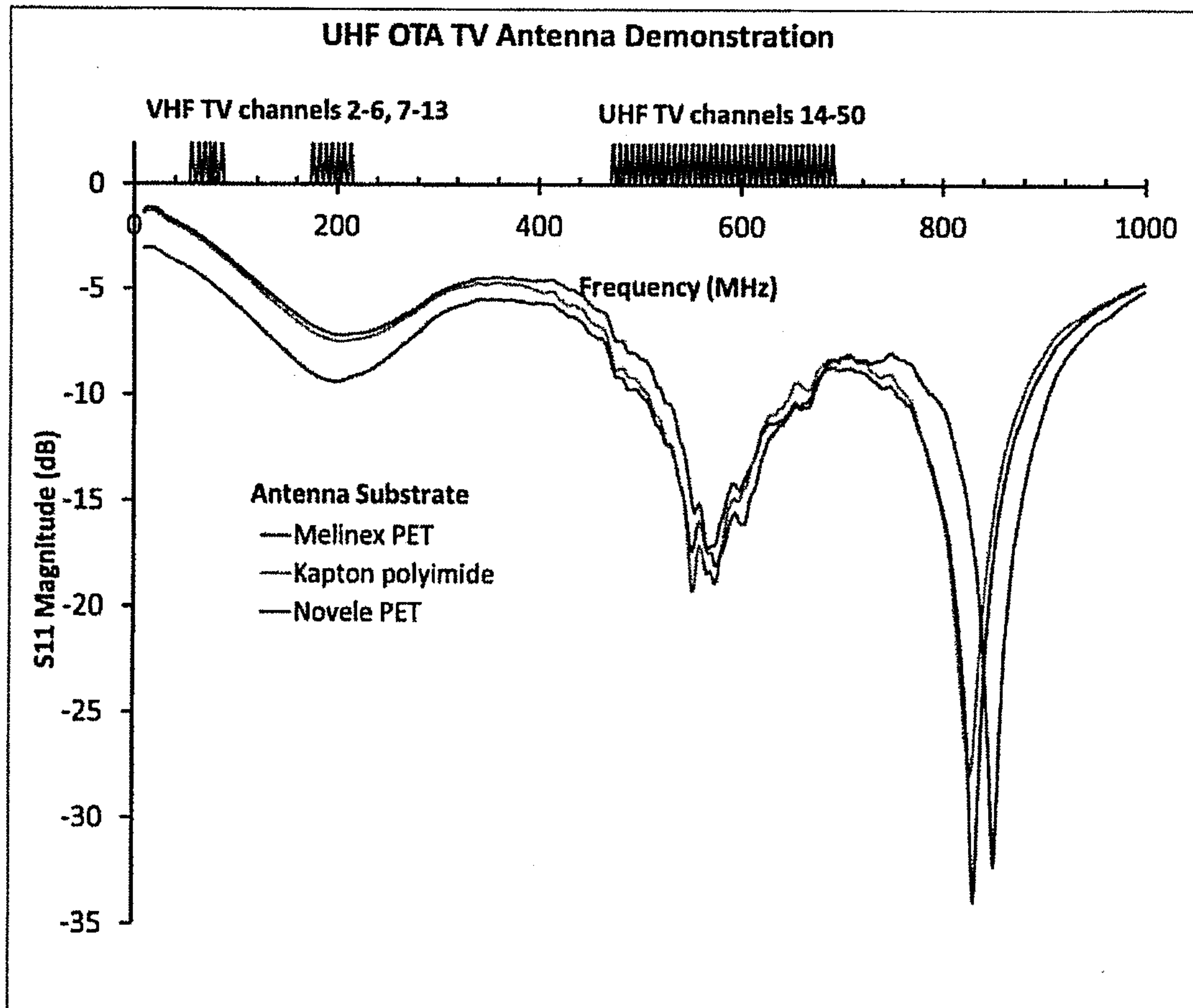


FIGURE 9

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**FIGURE 10**

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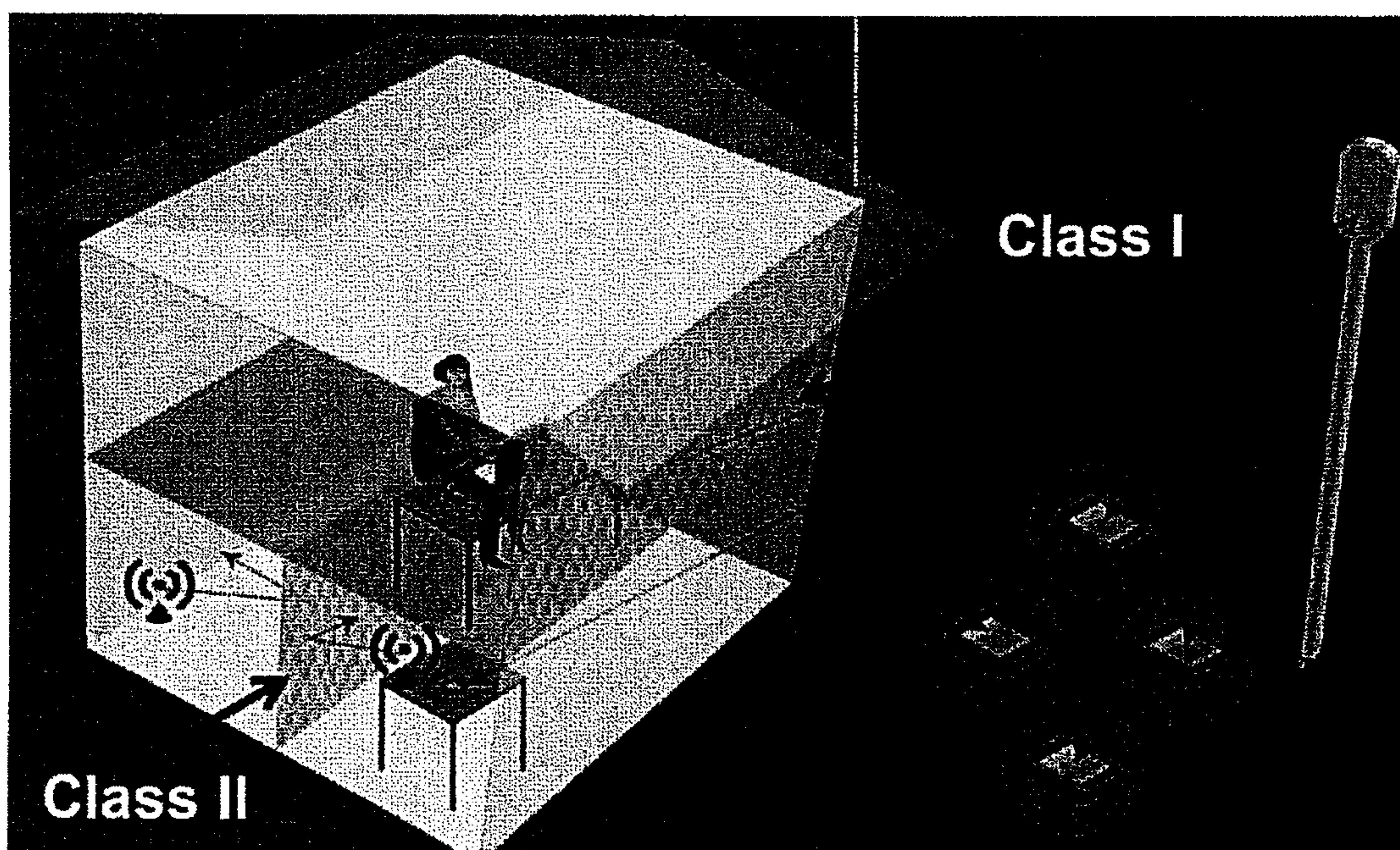
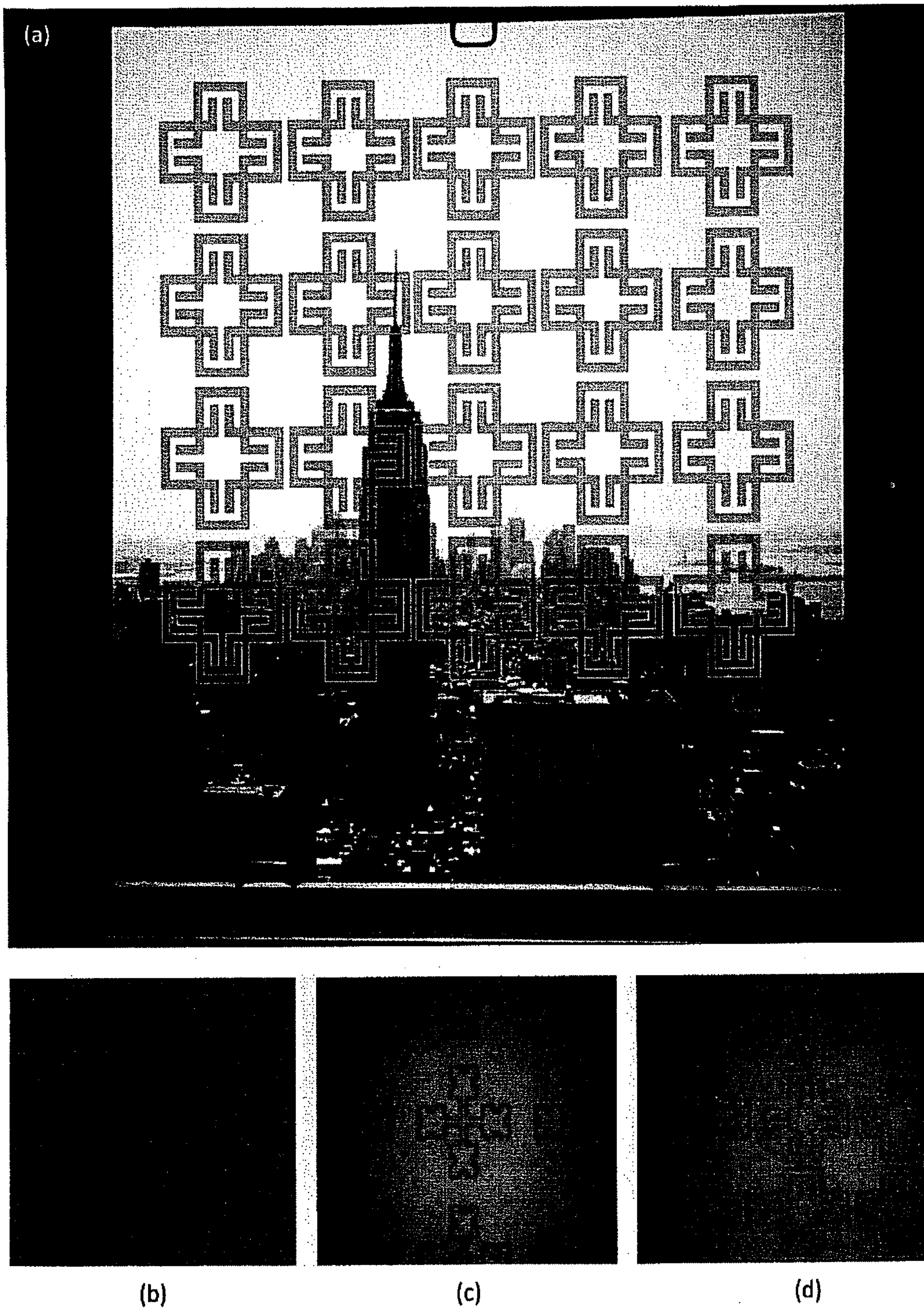


FIGURE 11

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**FIGURE 12**

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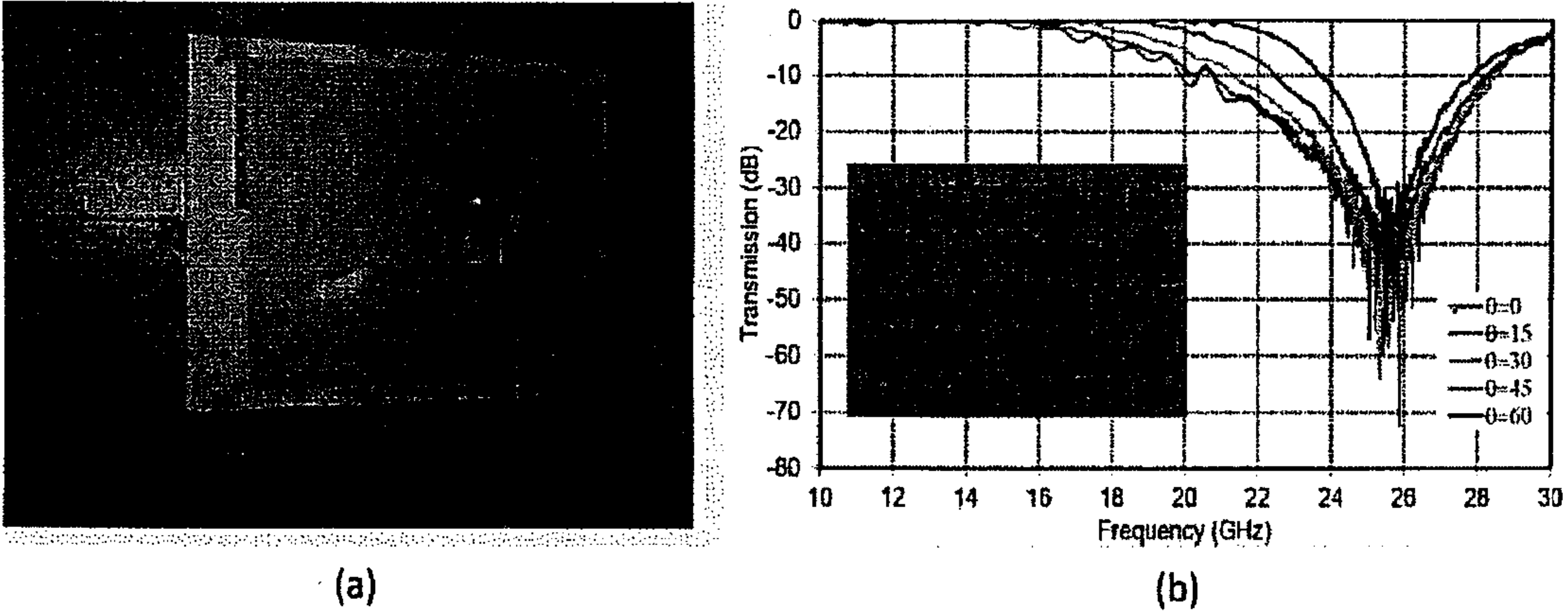
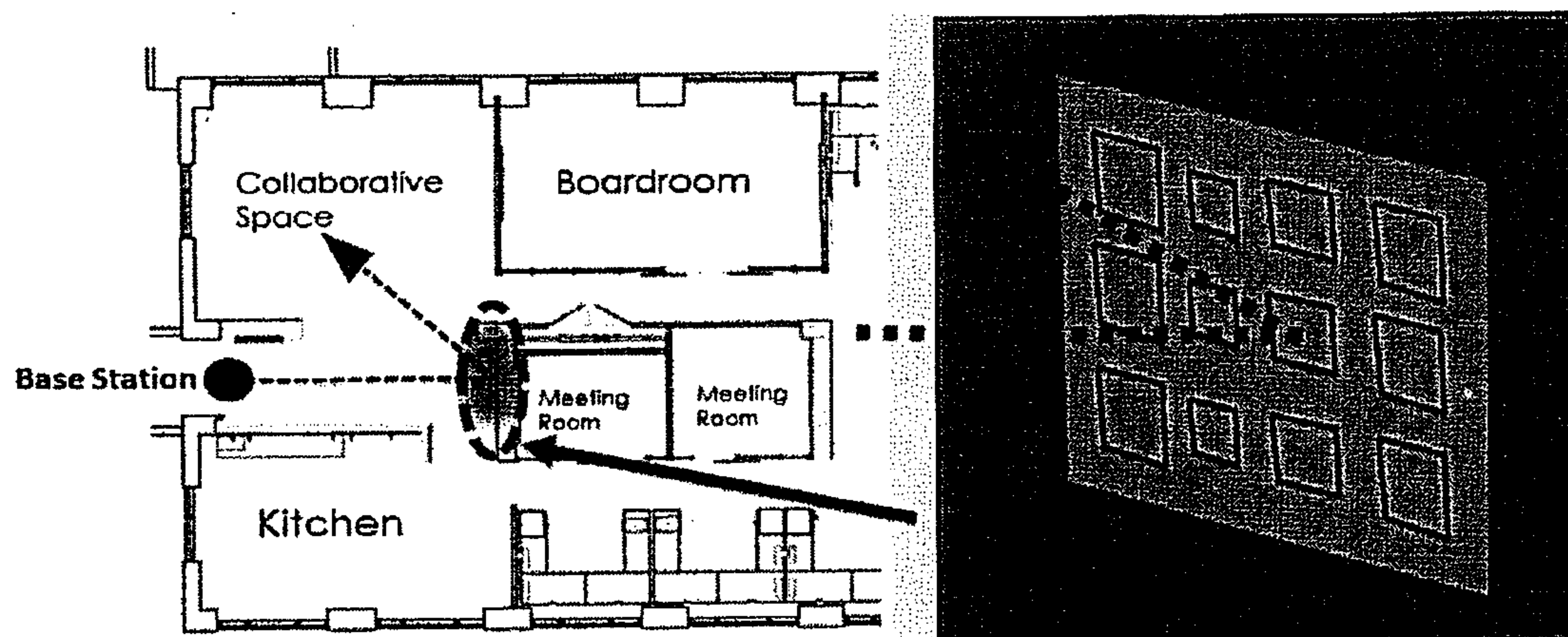
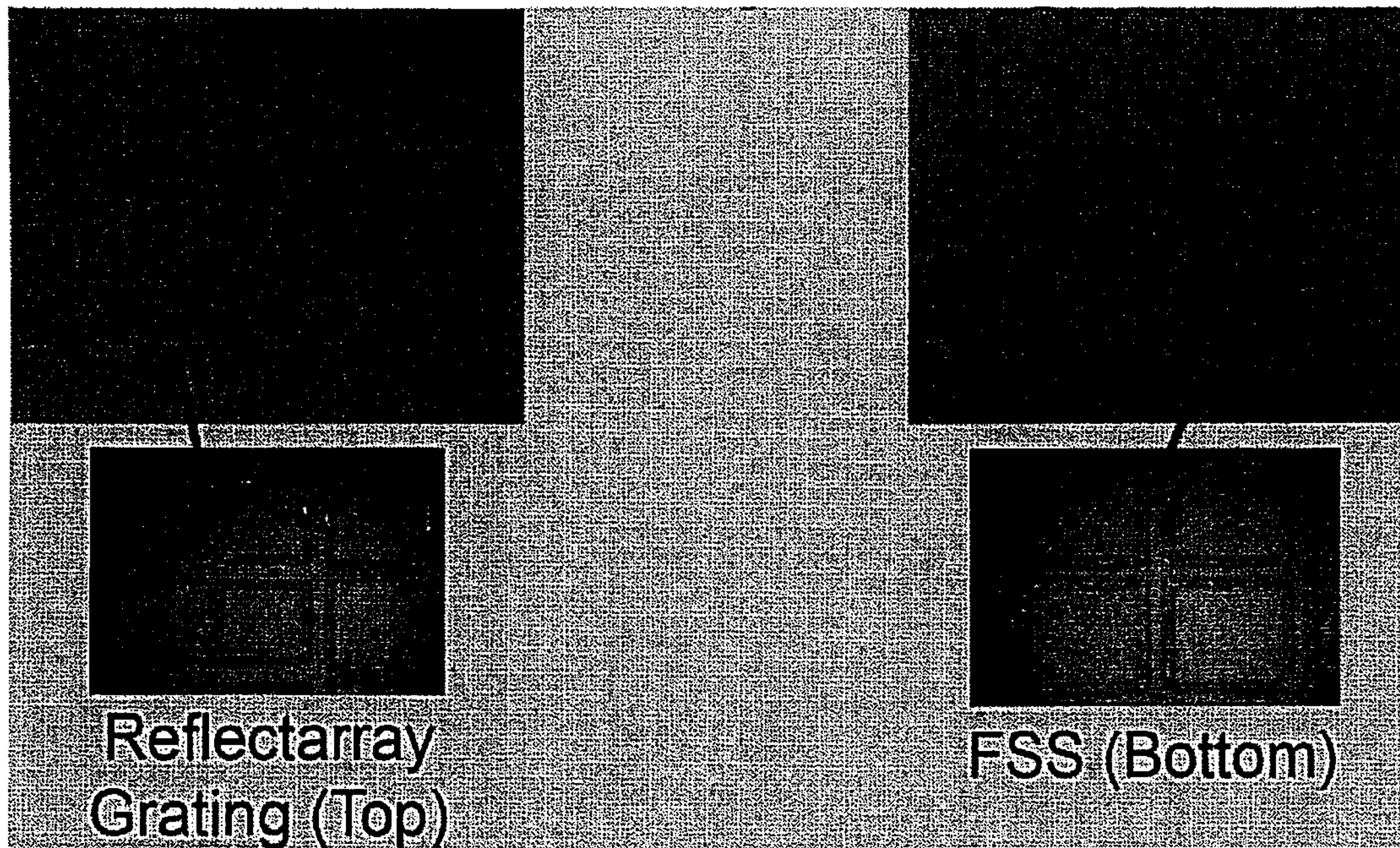


FIGURE 13

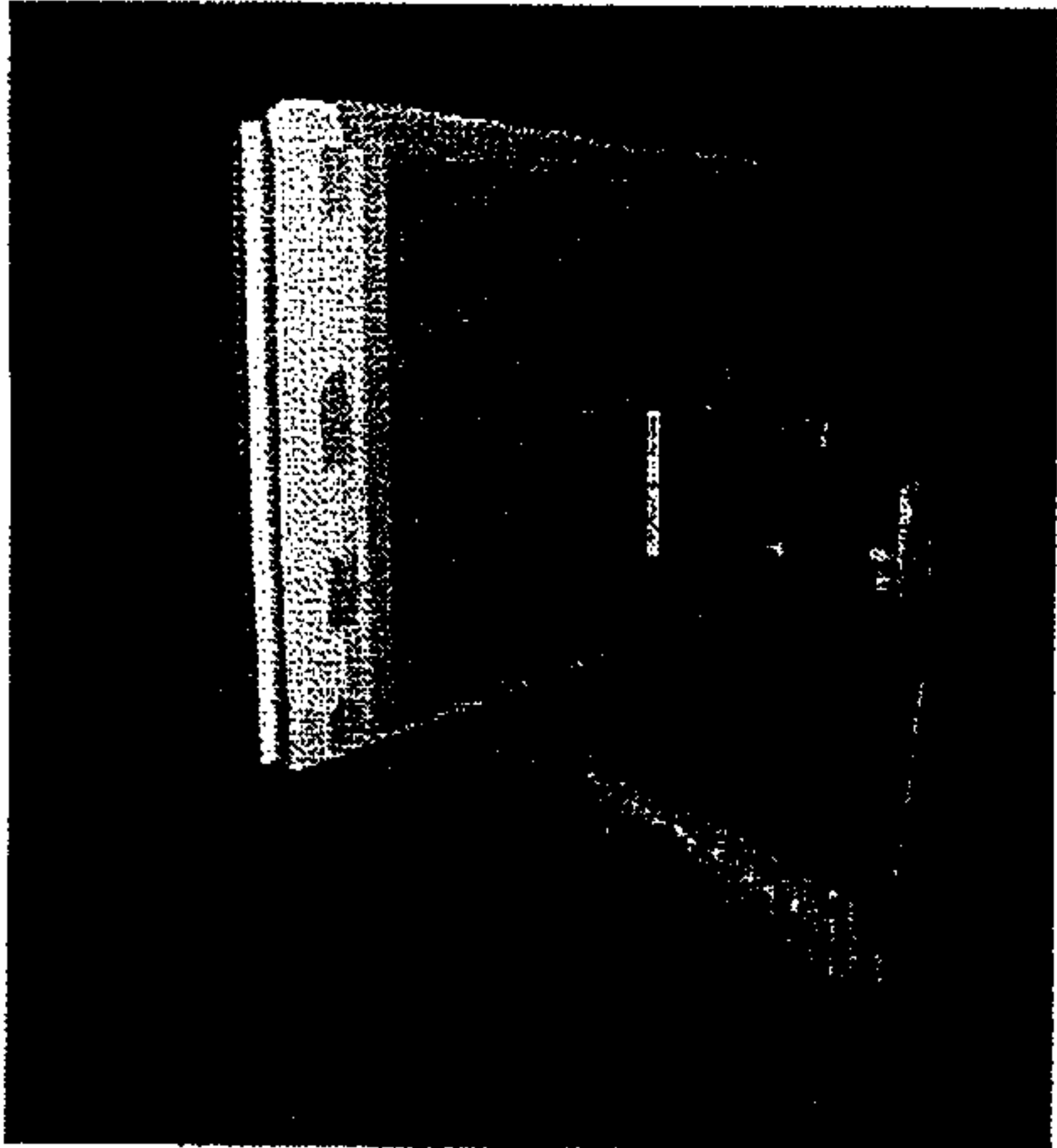
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**FIGURE 14**

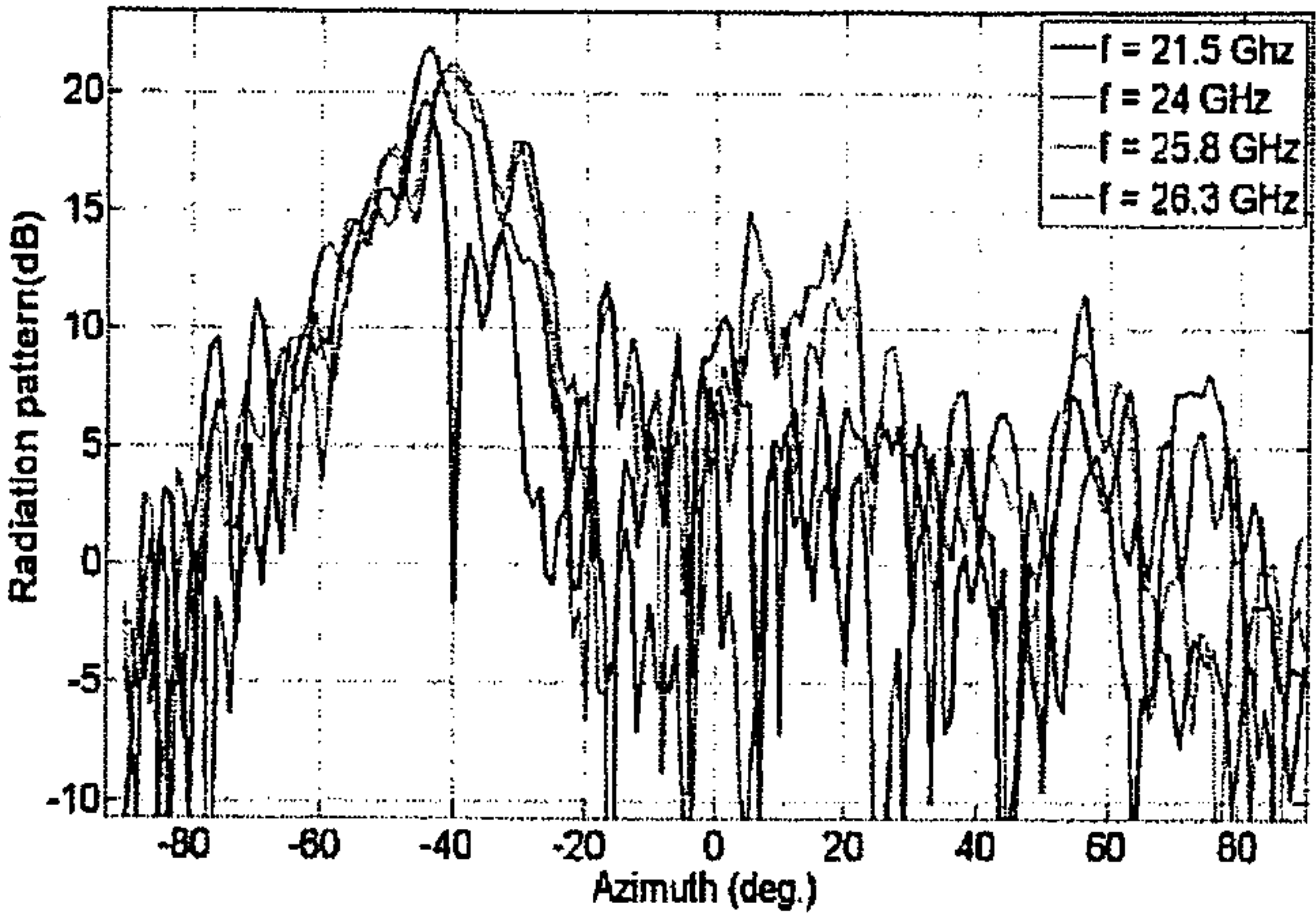
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**FIGURE 15**

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(a)

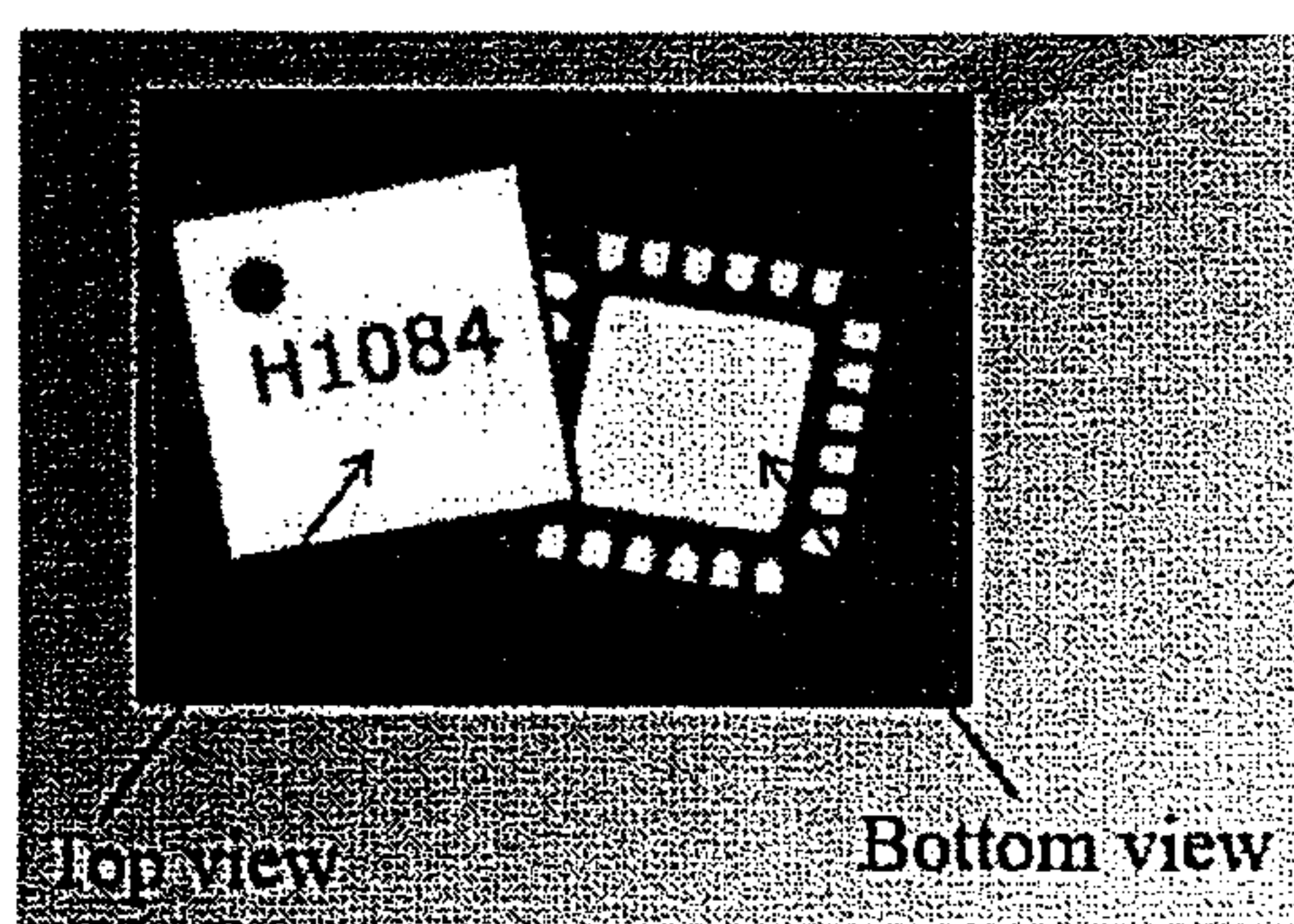


(b)

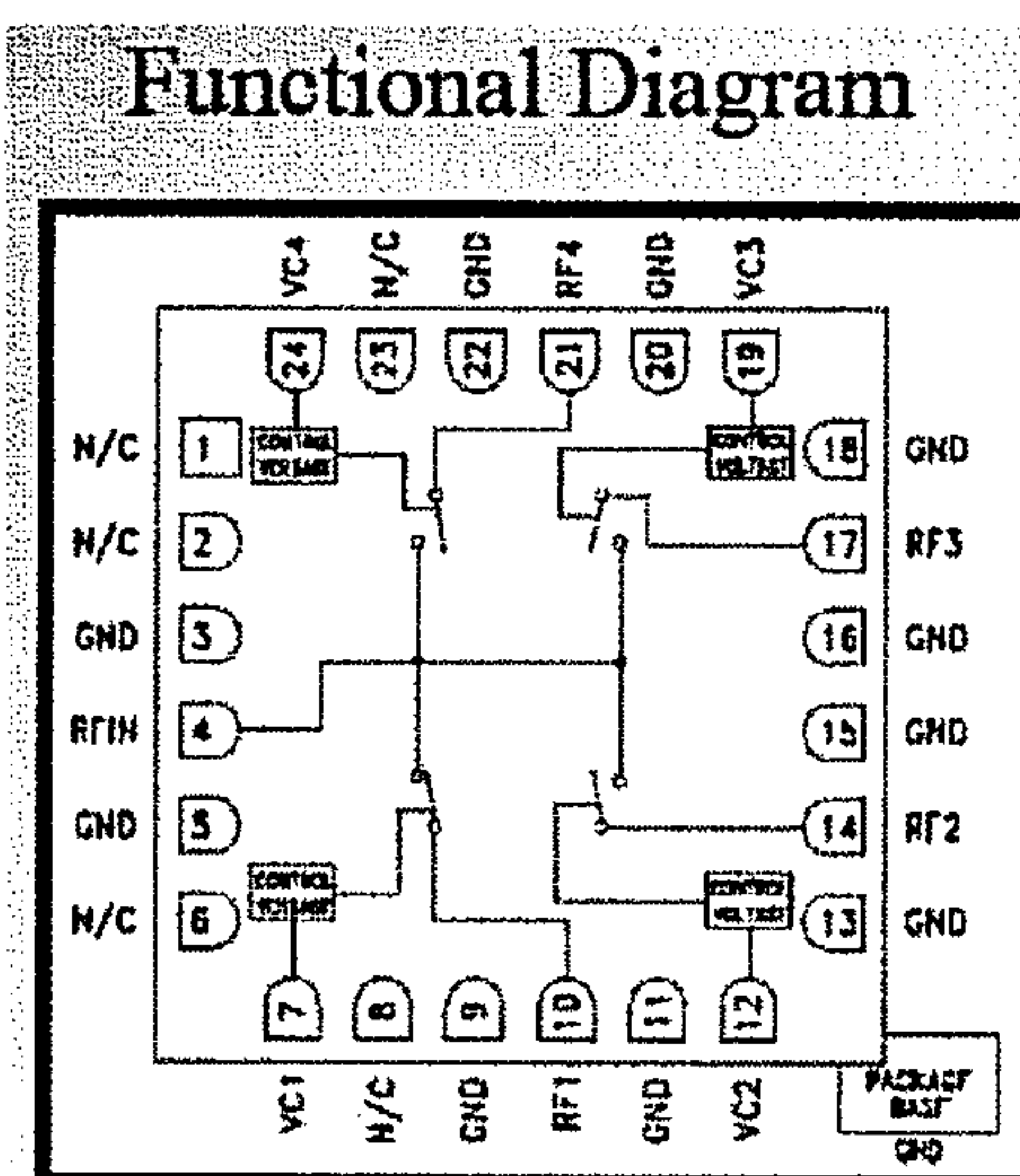
FIGURE 16

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(a)



(b)



Outline Drawing

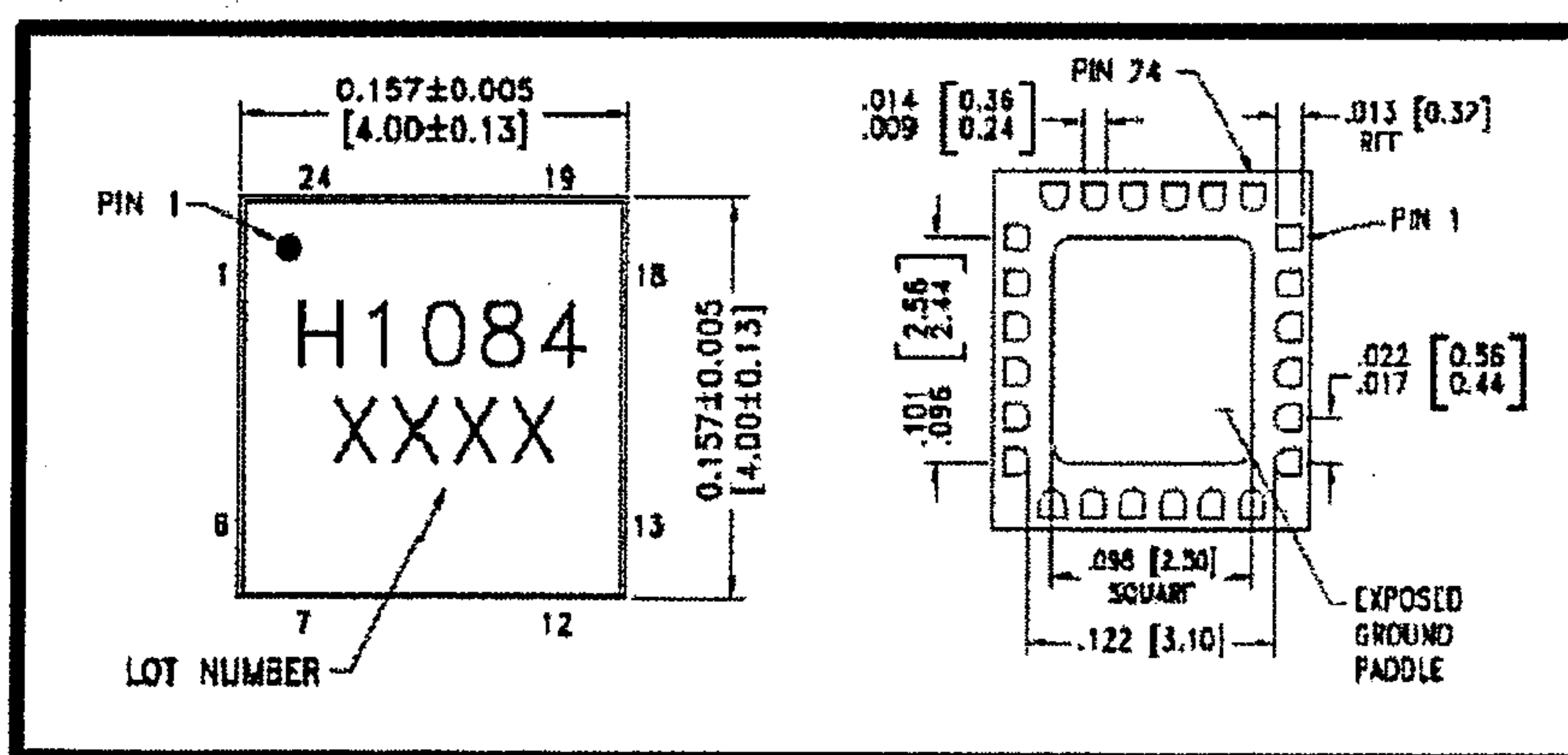
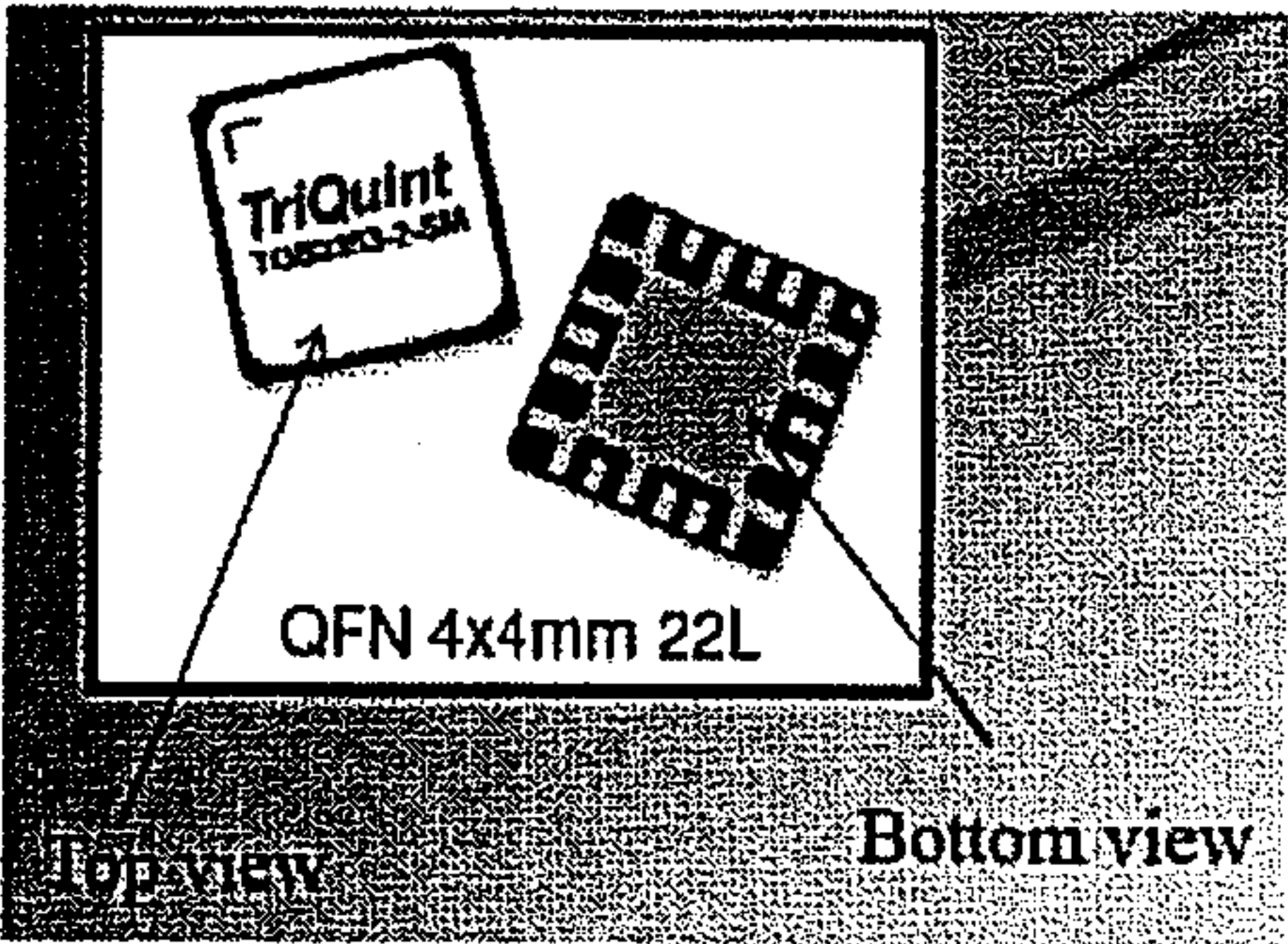


FIGURE 17

(a)



(b)

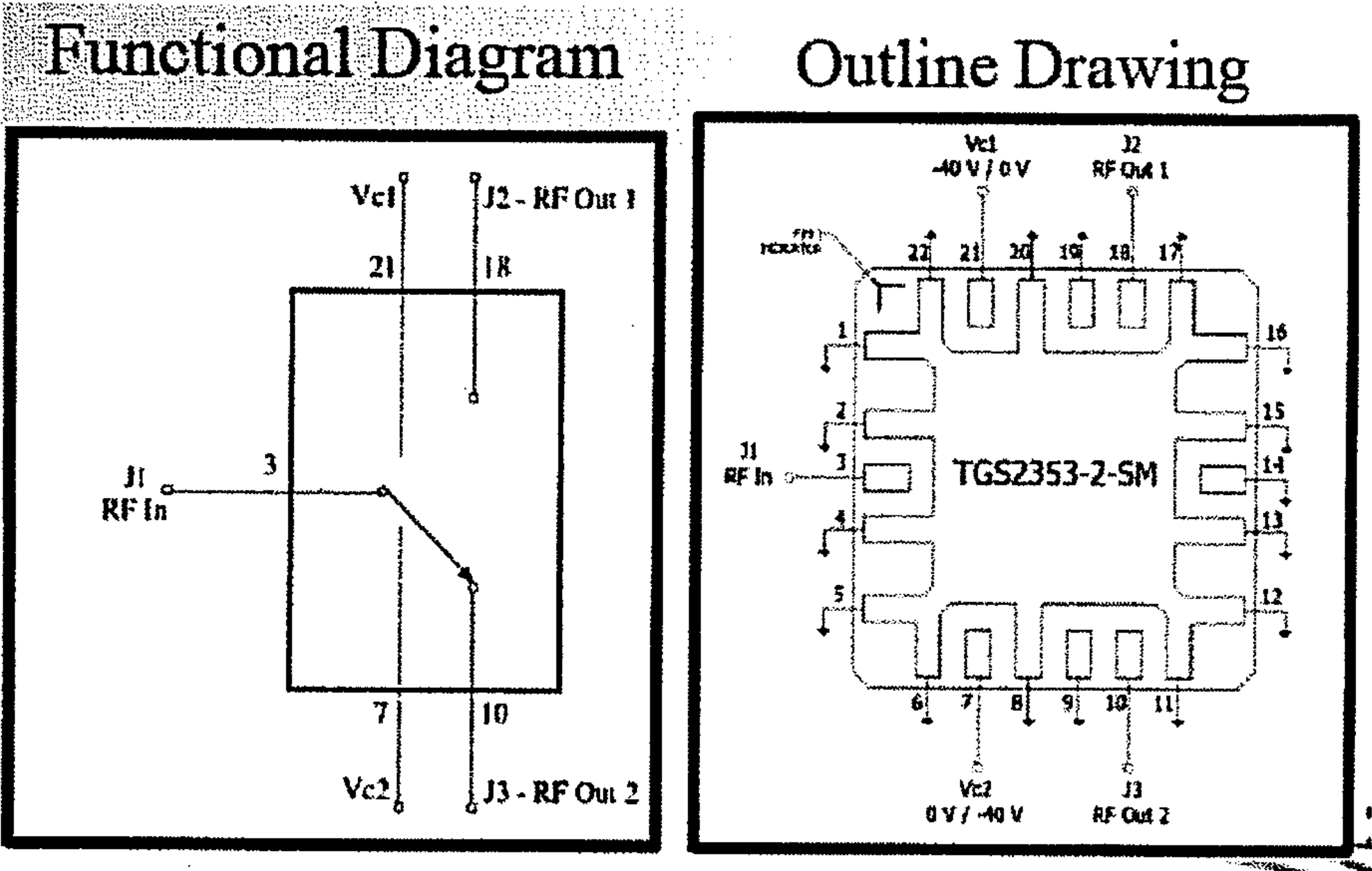
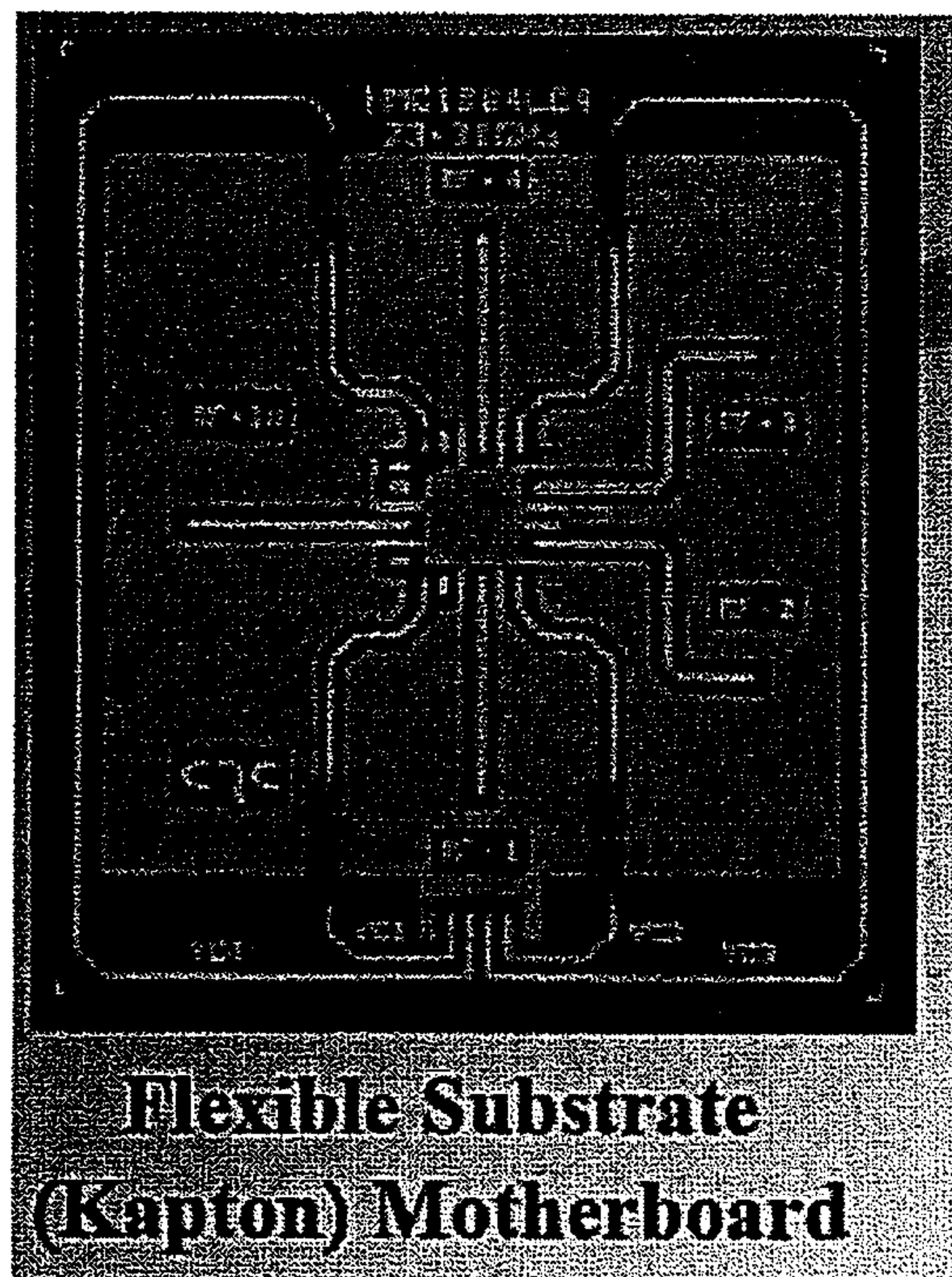


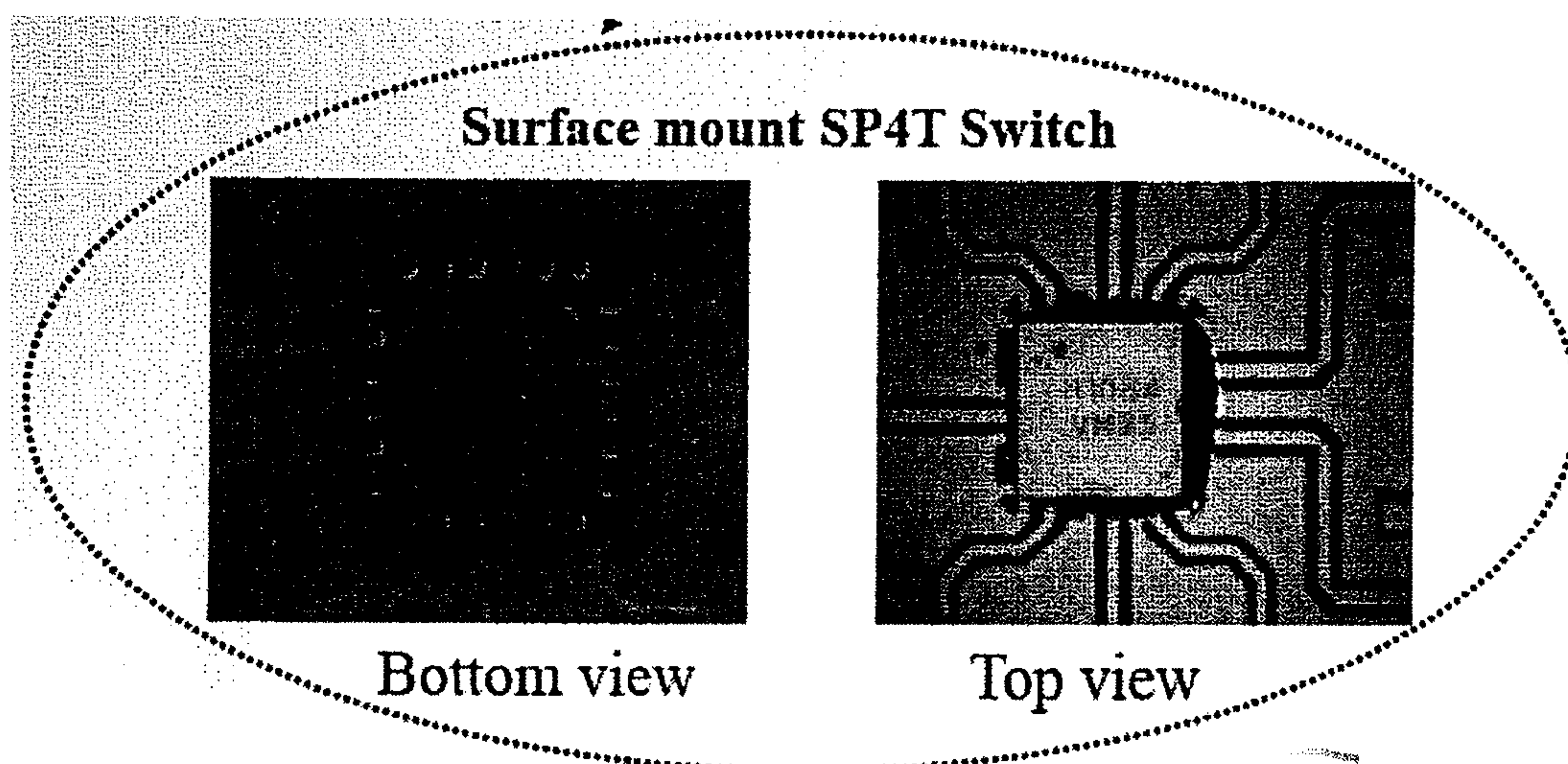
FIGURE 18

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(a)

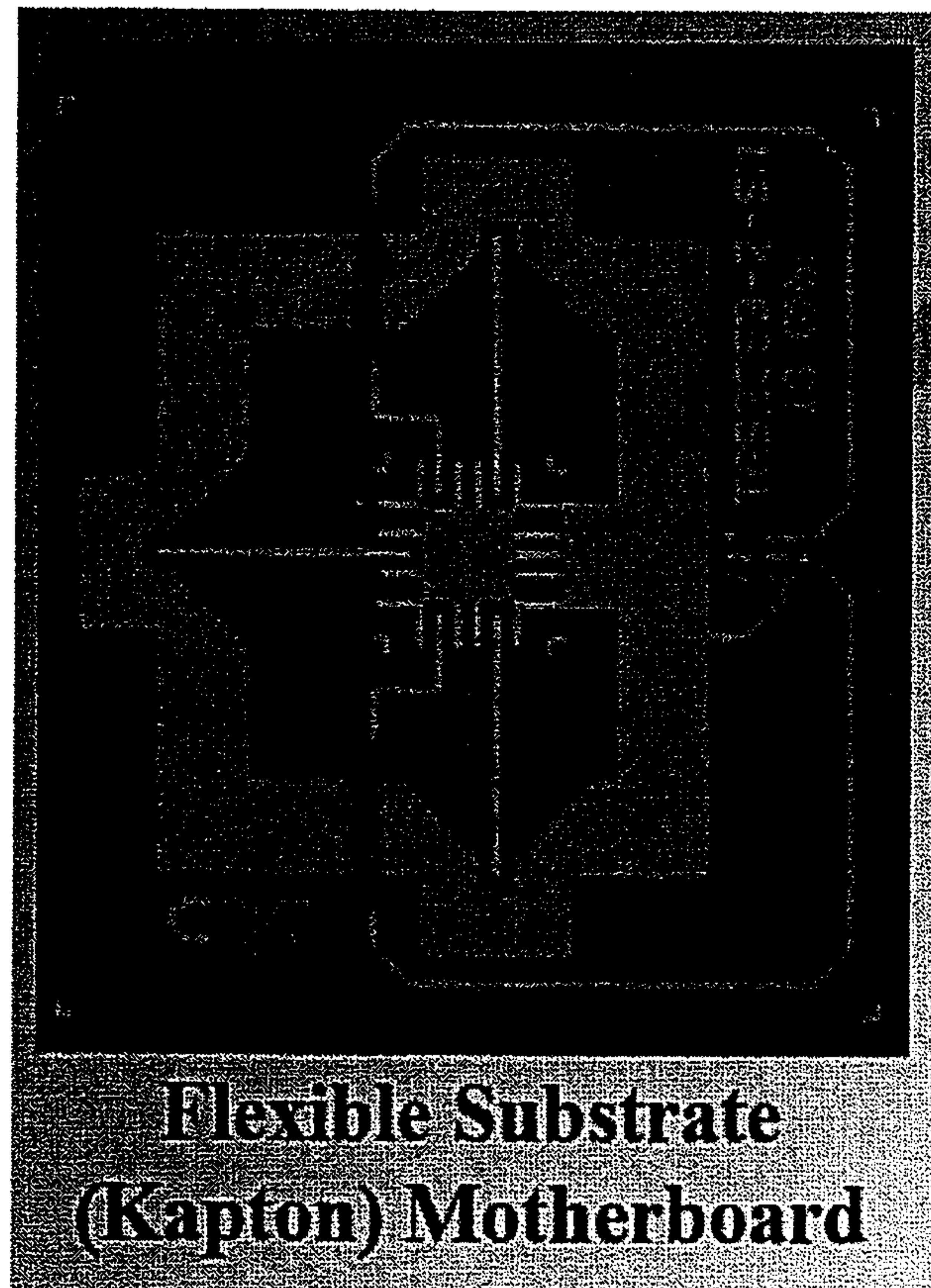


(b)

**FIGURE 19**

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(a)



(b)

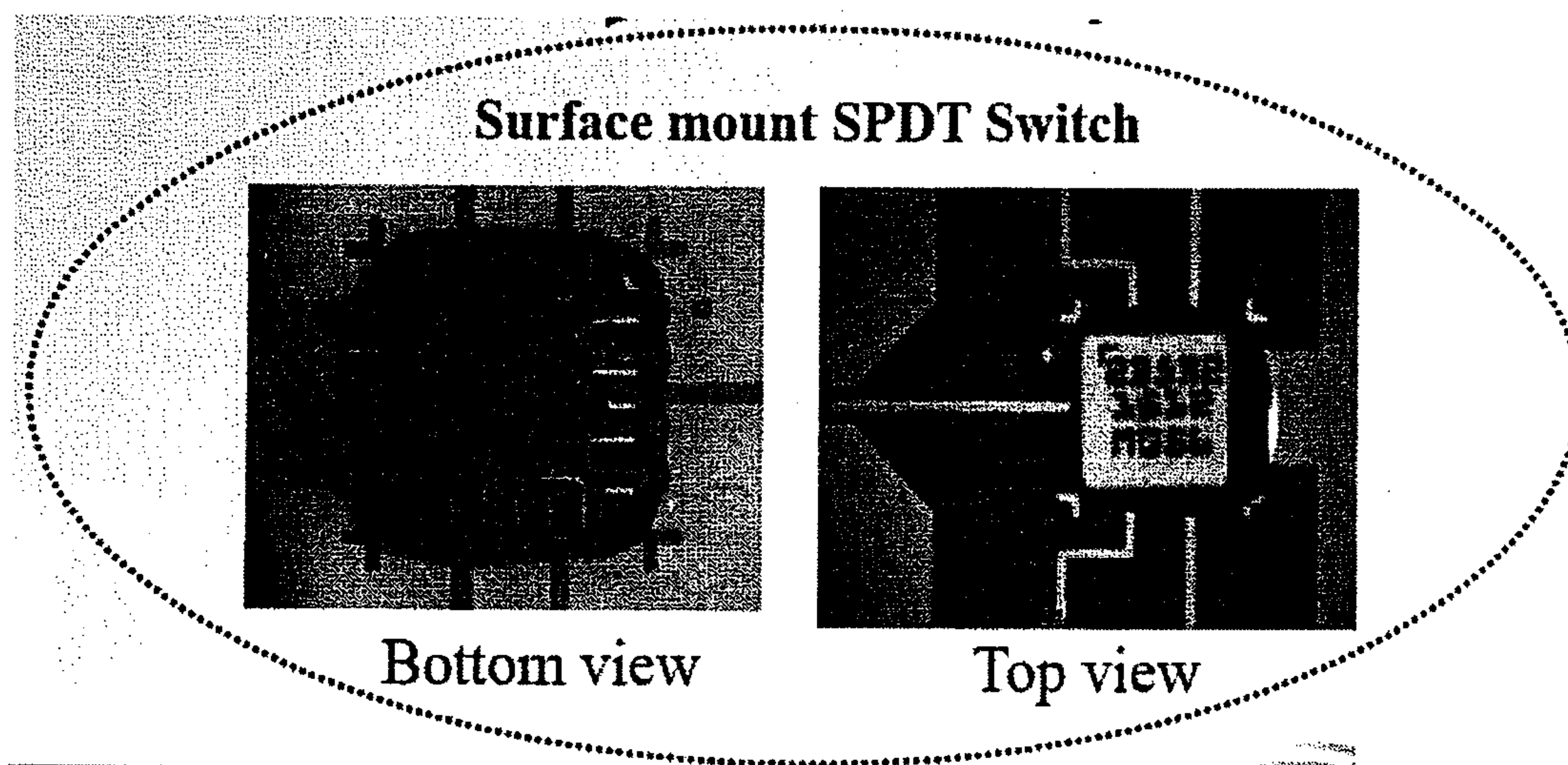
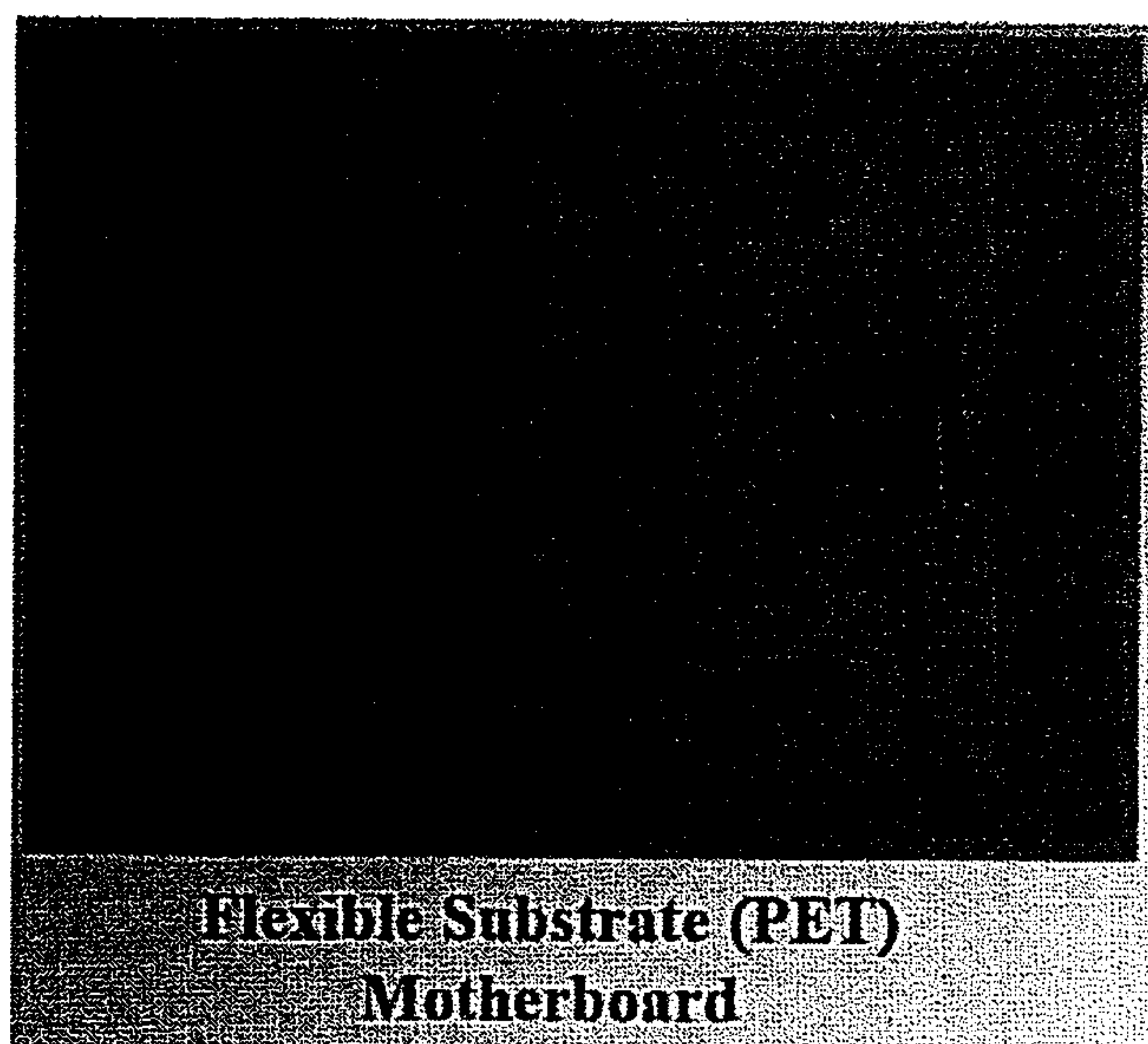


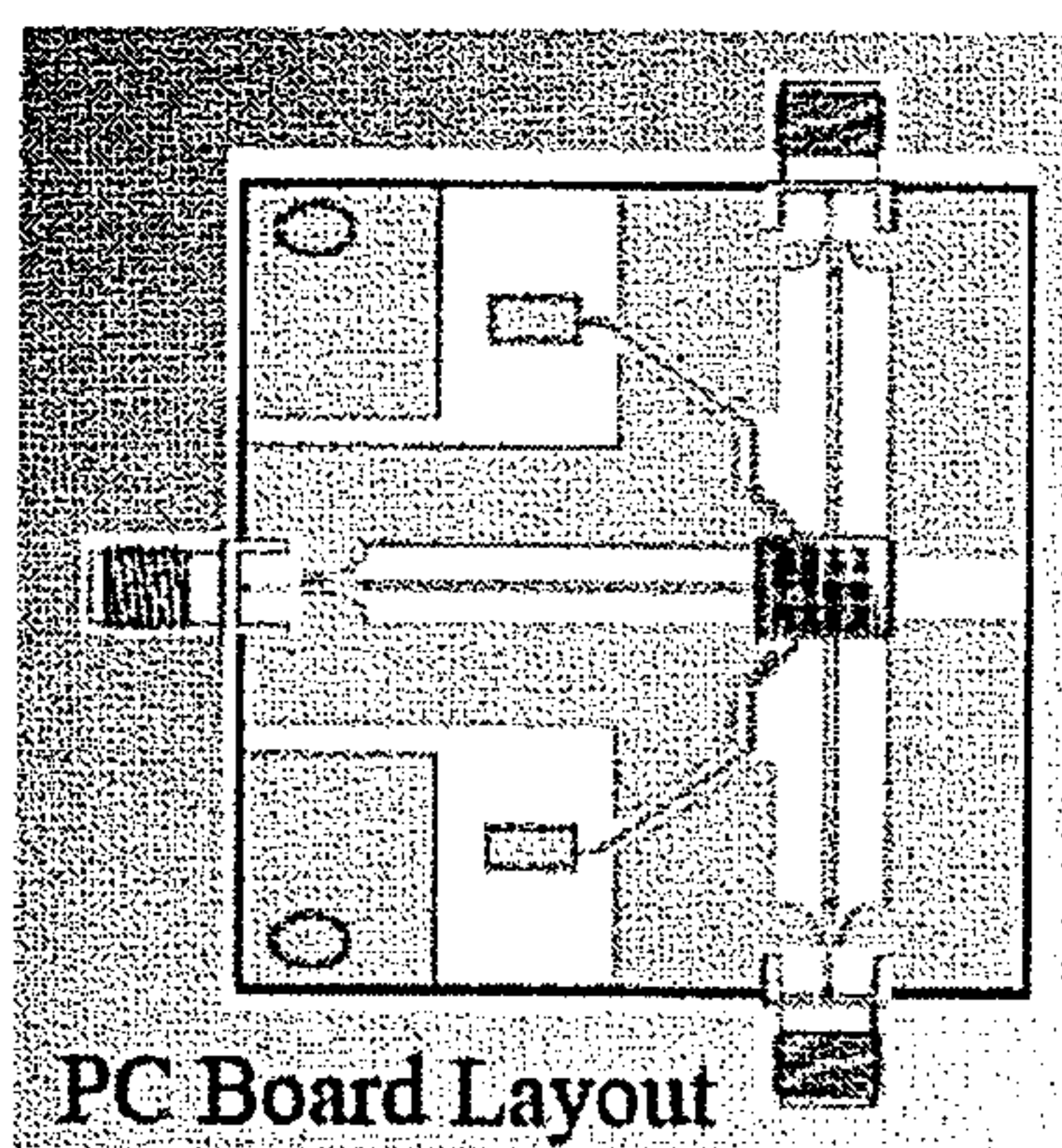
FIGURE 20

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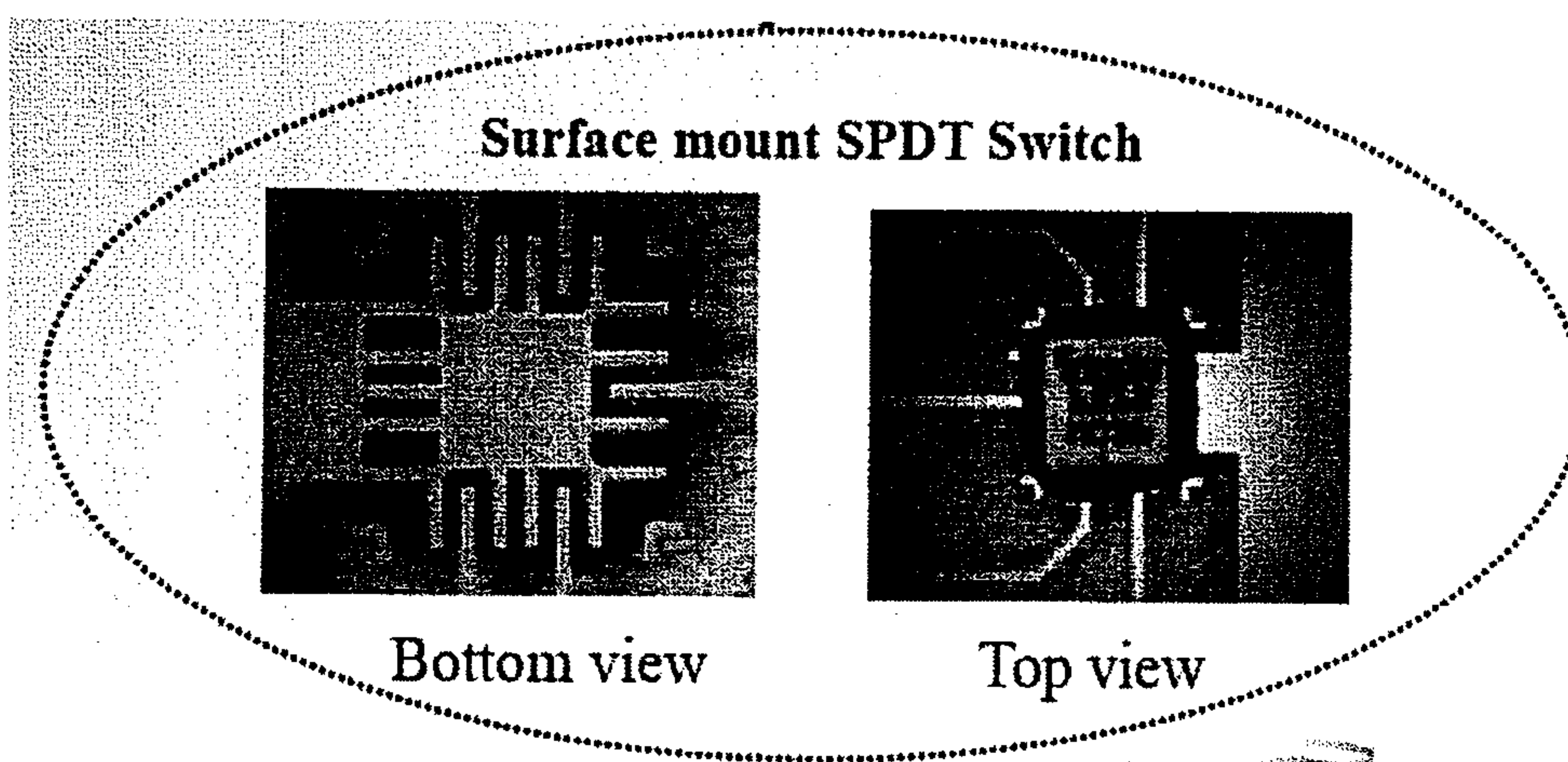
(a)

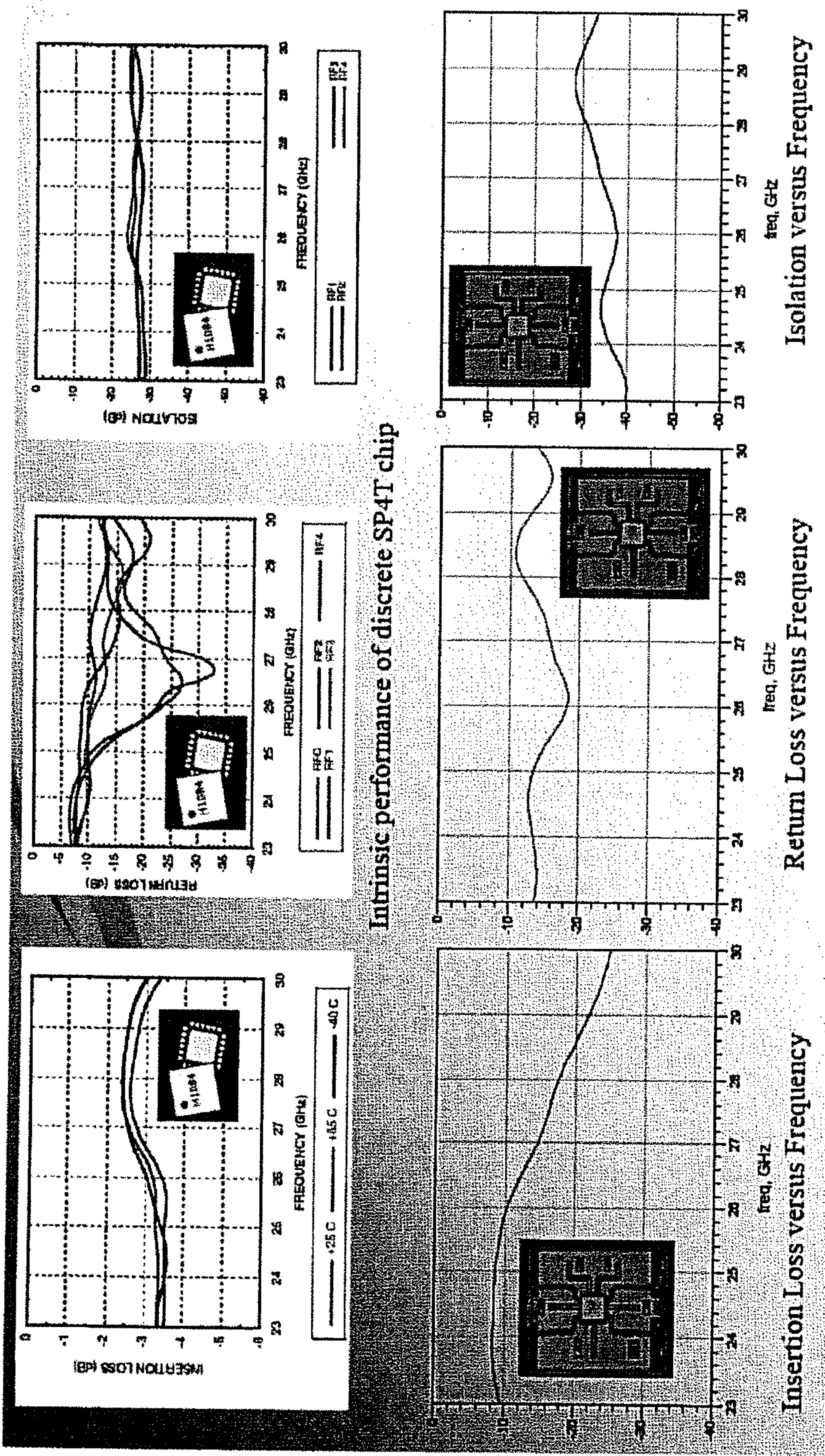


(b)



(c)

**FIGURE 21**



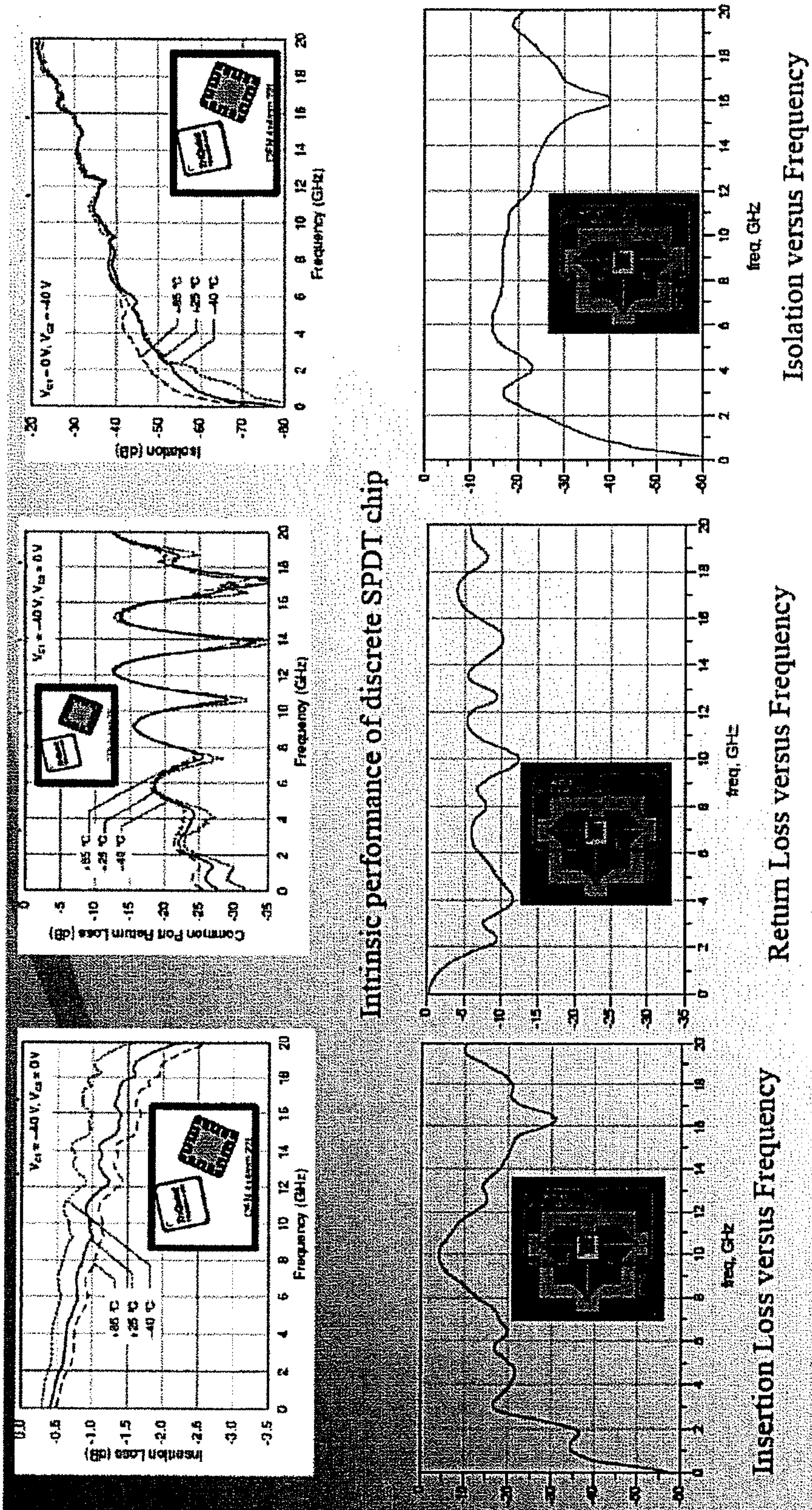


FIGURE 23

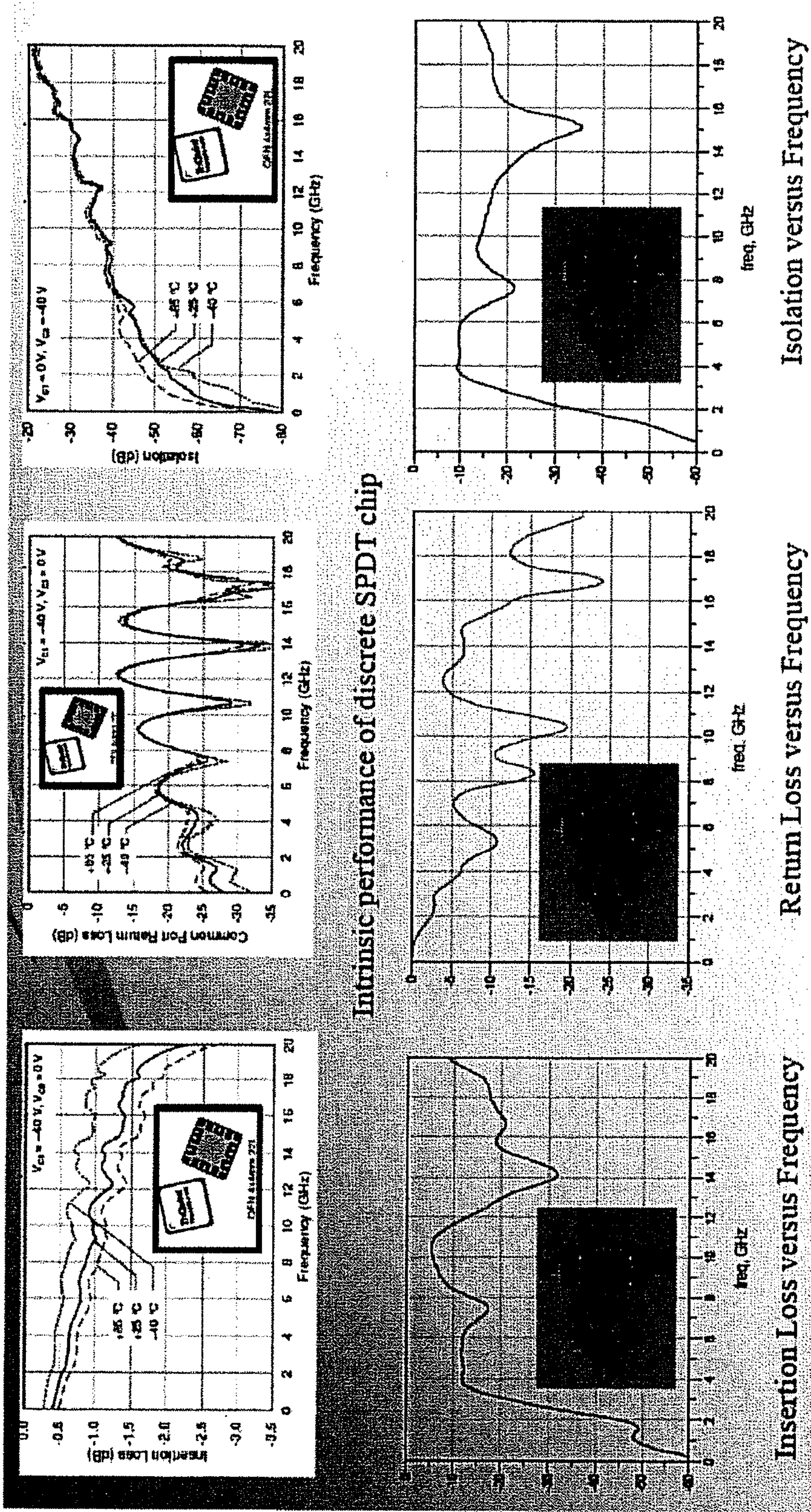
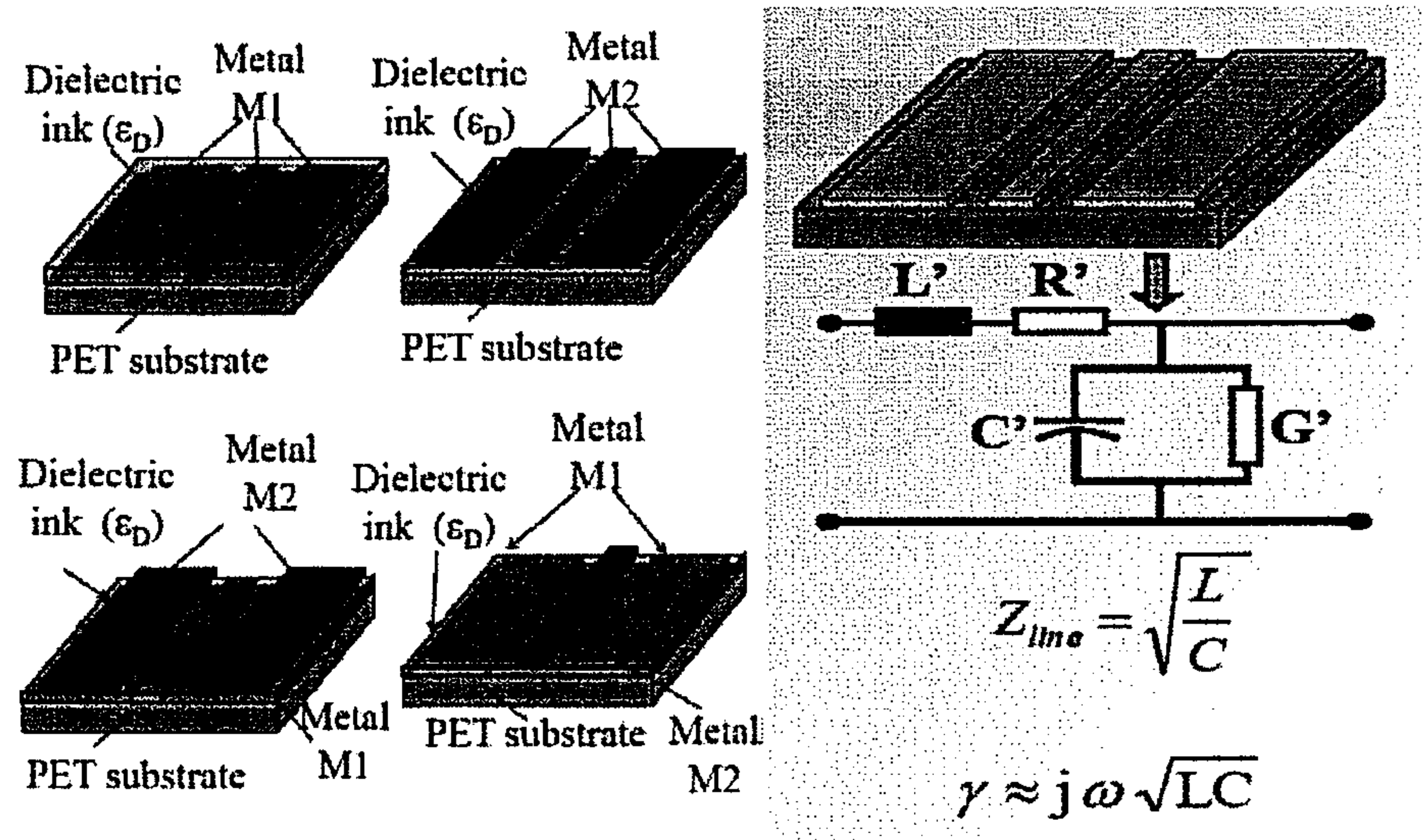


FIGURE 24

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(a)



(b)

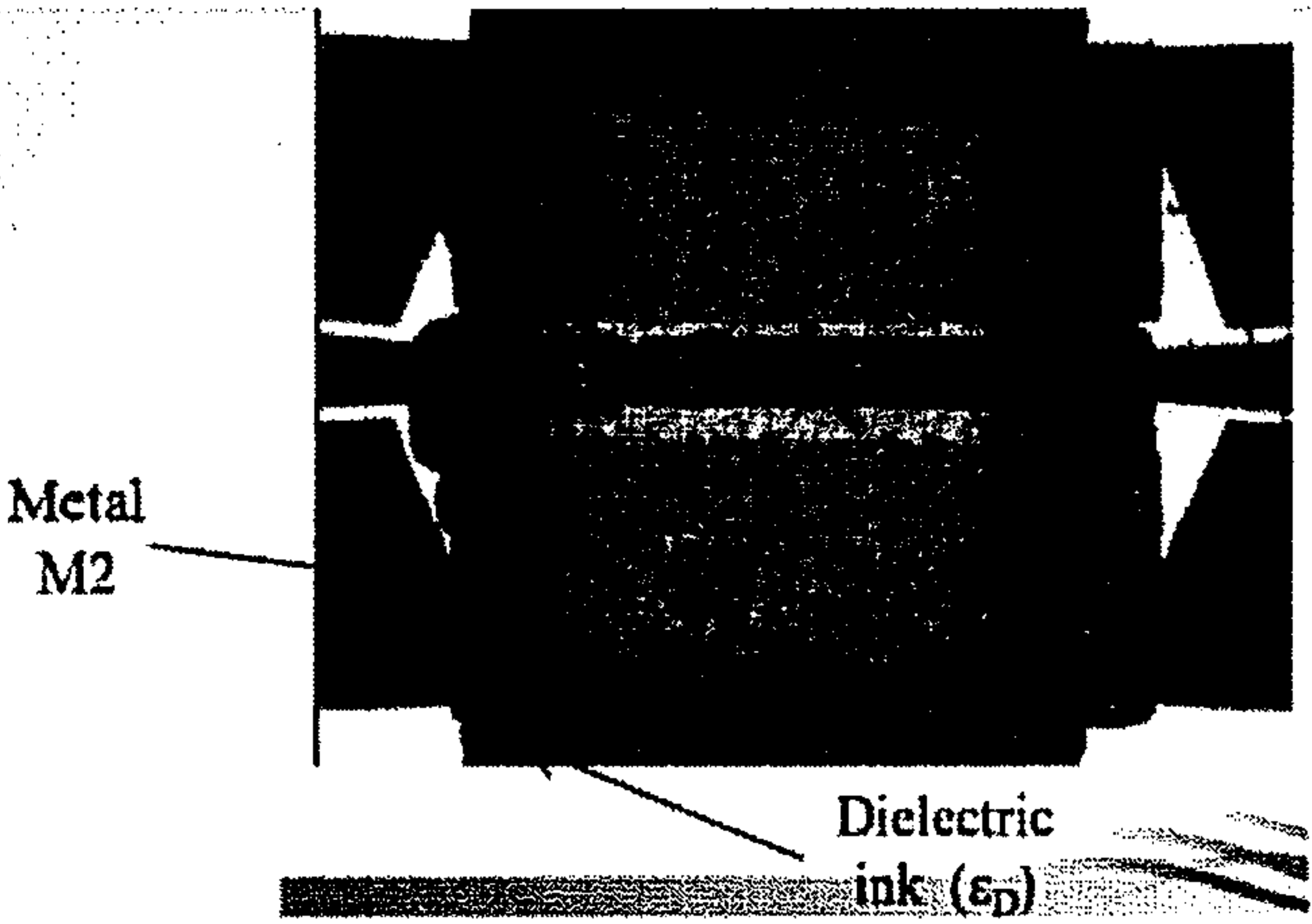


FIGURE 25

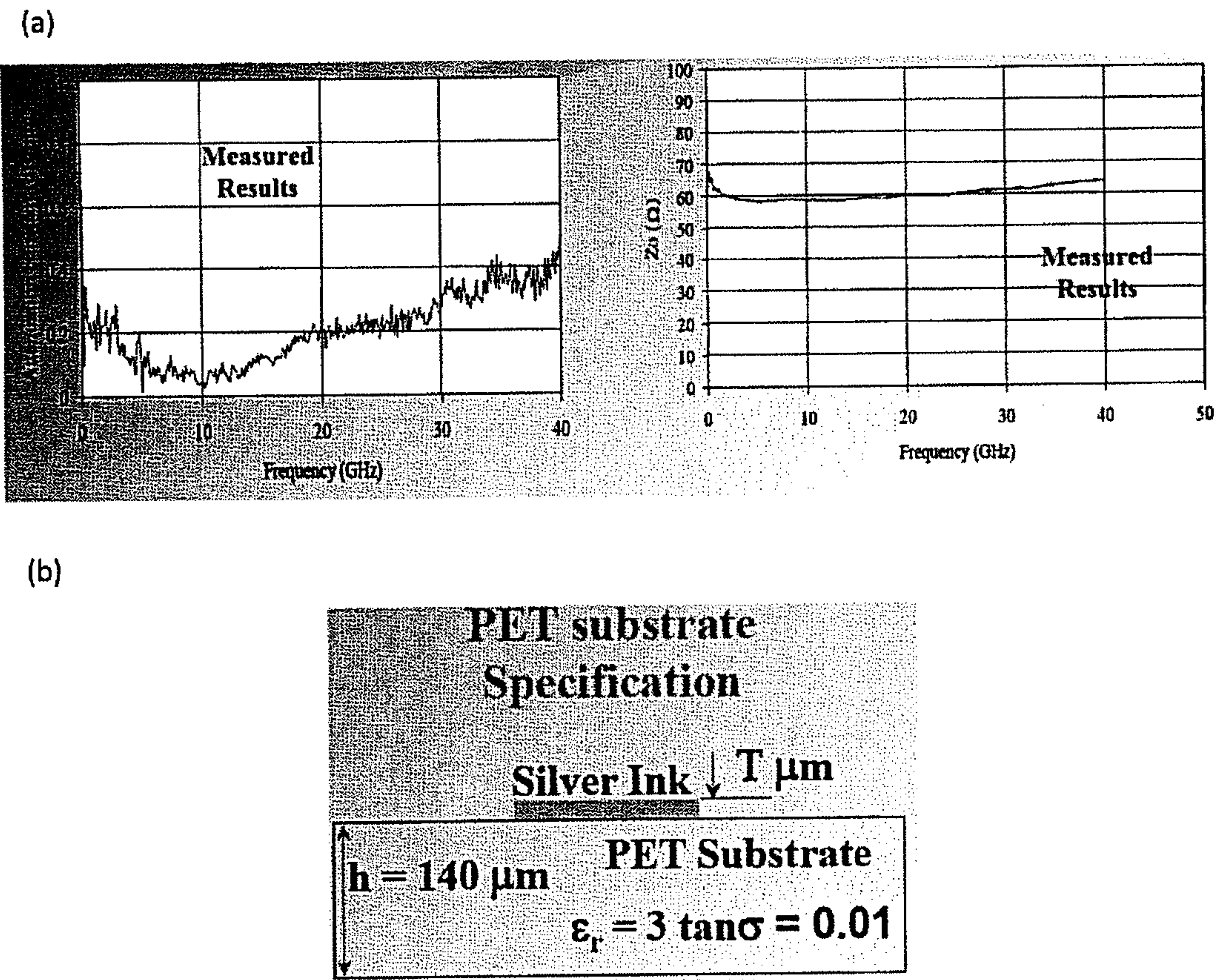


FIGURE 26

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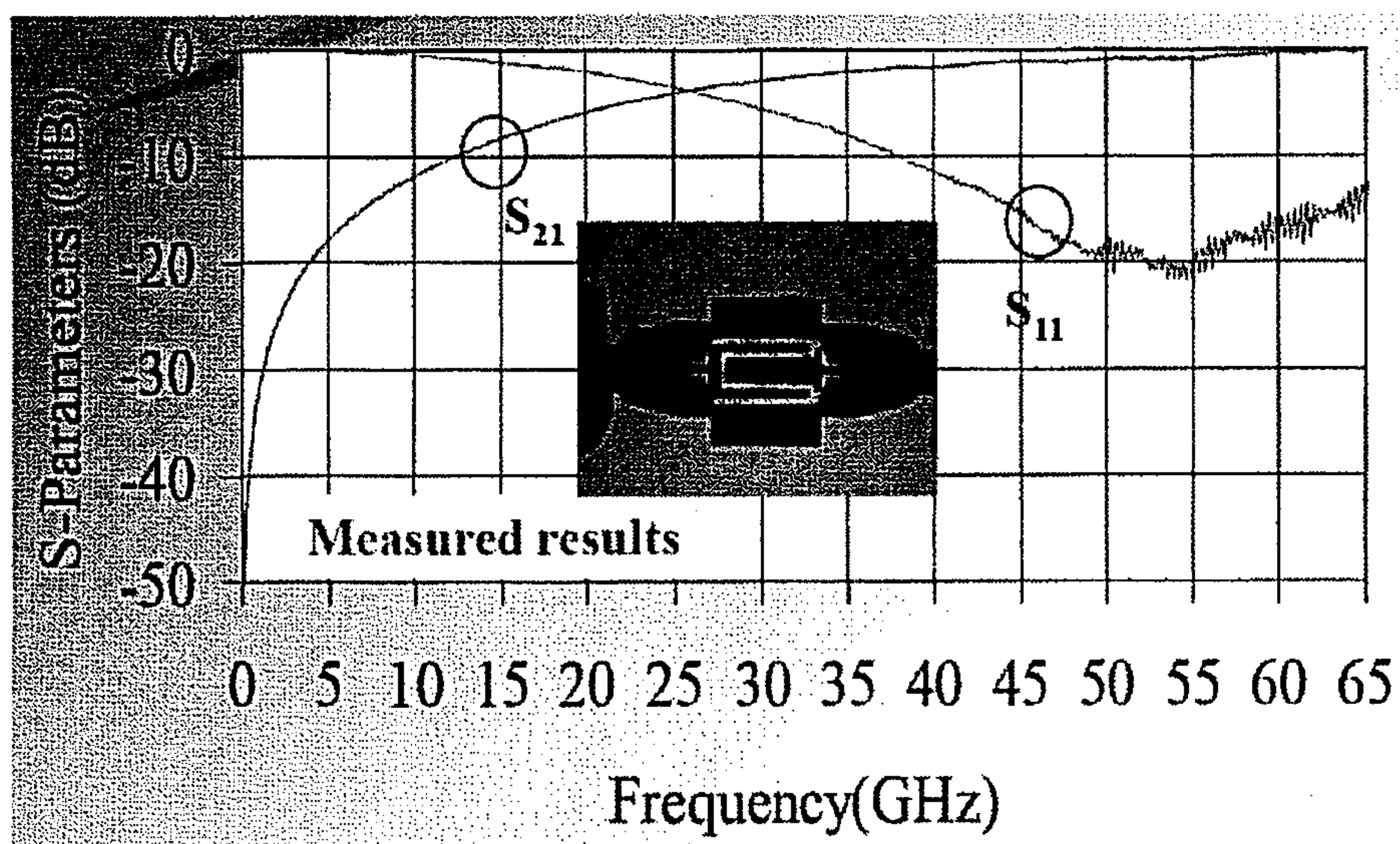


FIGURE 27

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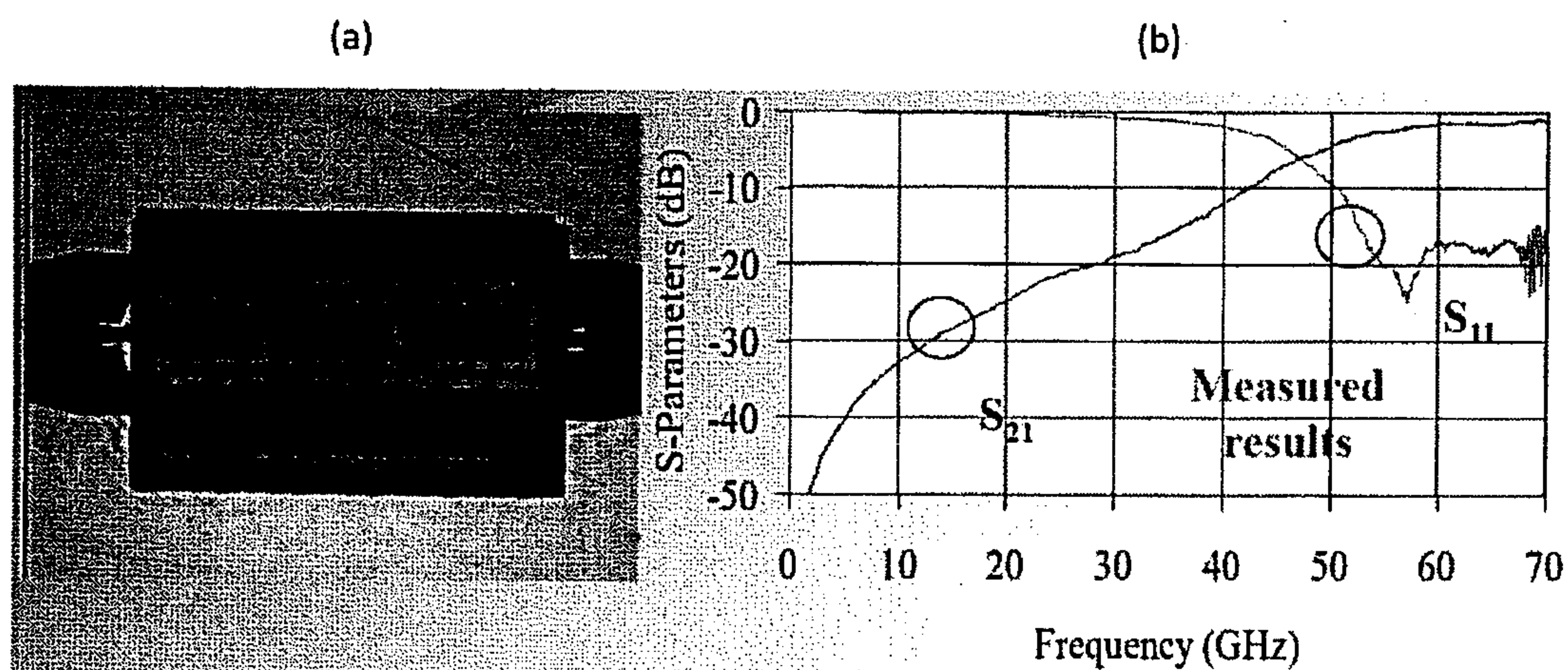
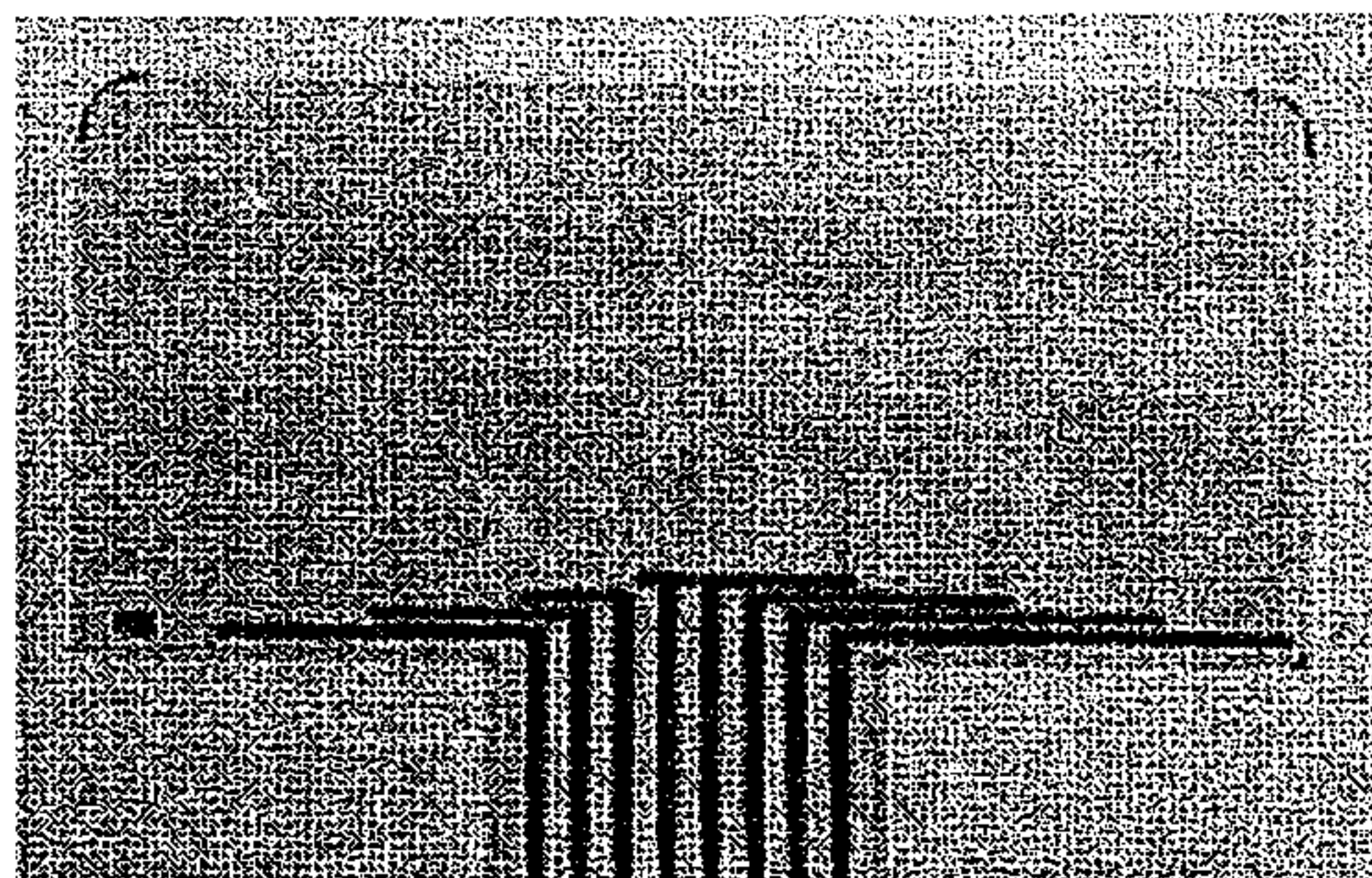
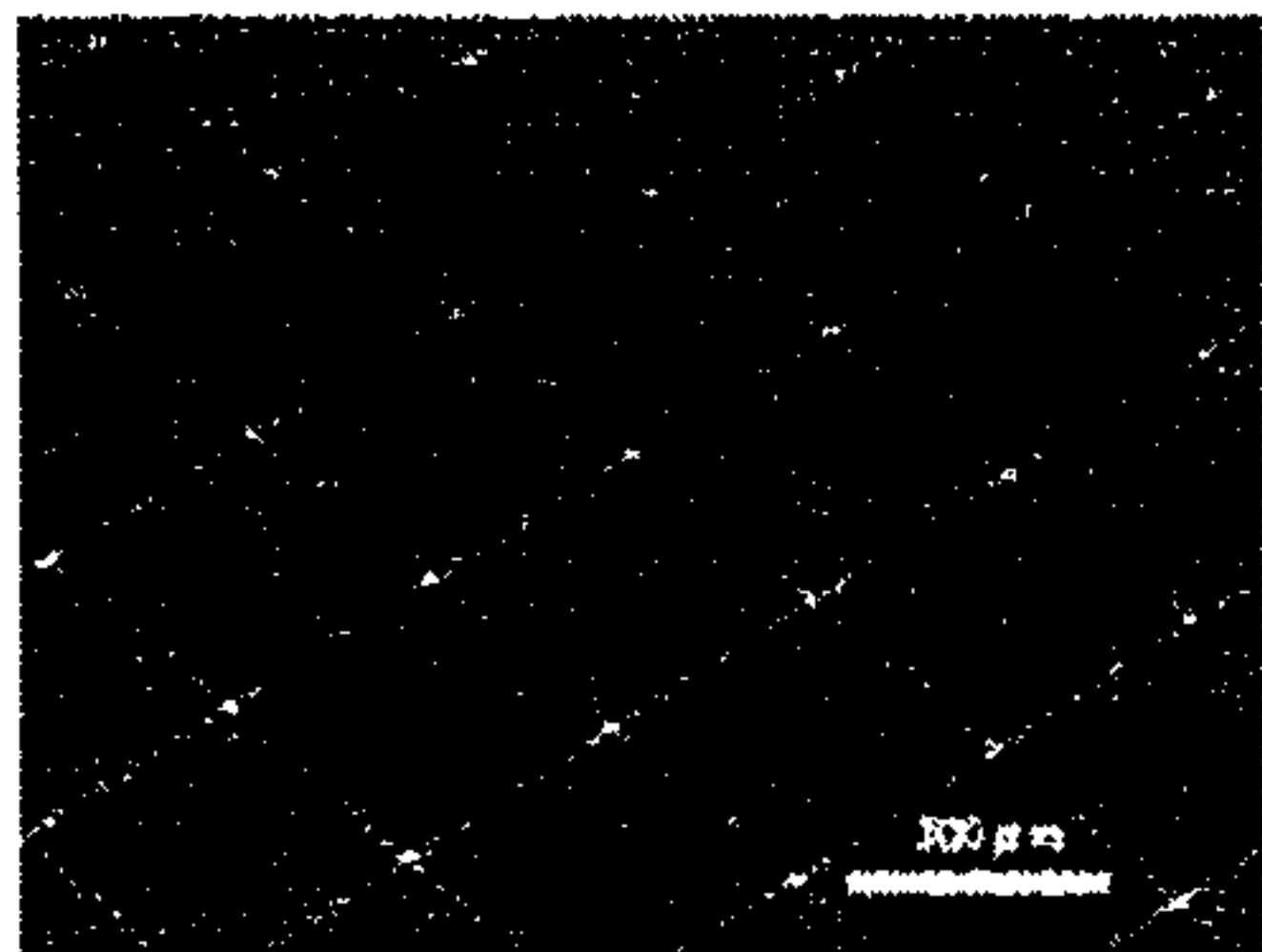


FIGURE 28

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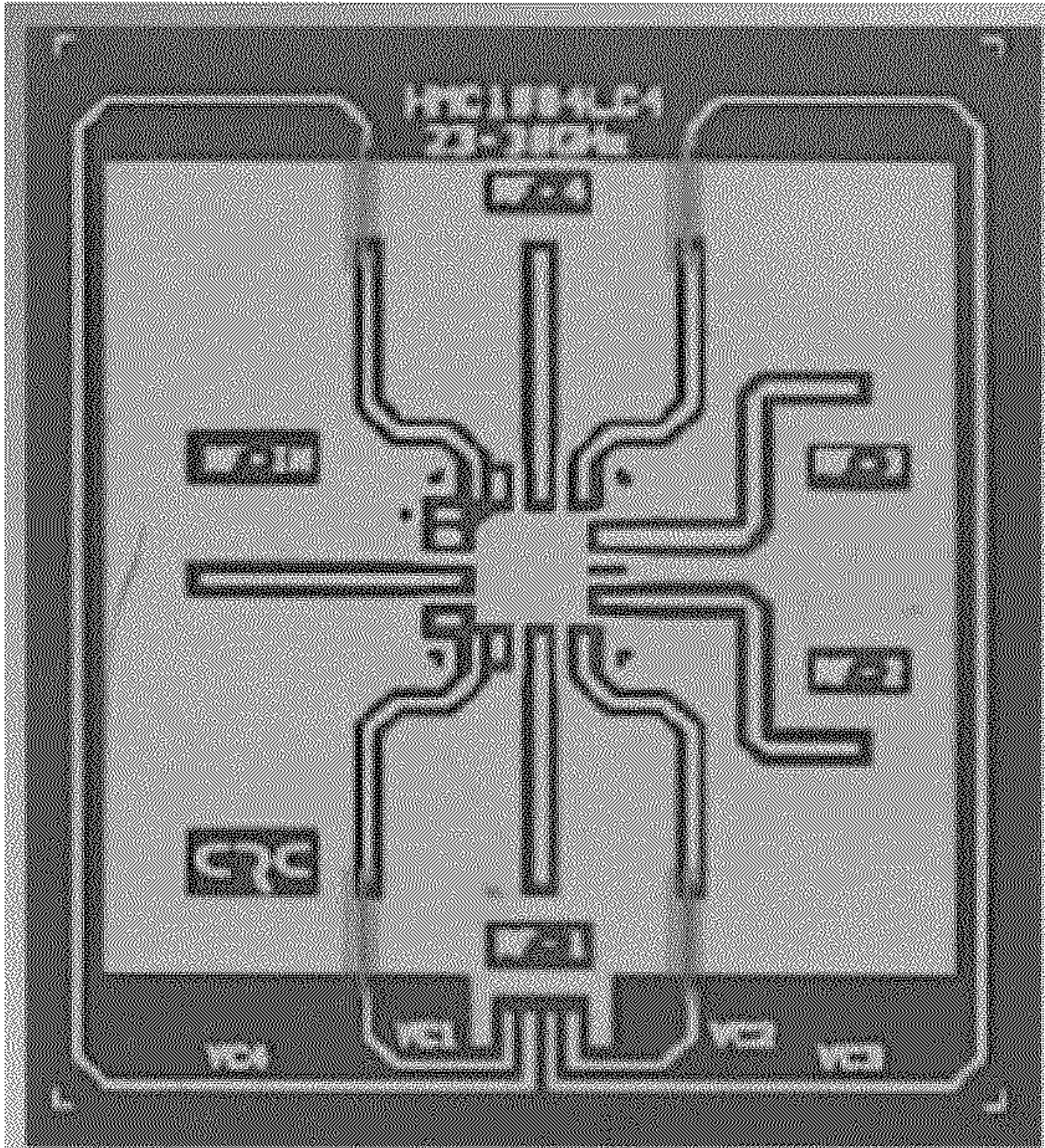
Traces Ag w= 1,0 -1,4 mils (25-35 uM)



Micro-wires Touch interface

FIGURE 29

FIGURE 19A



**Flexible Substrate
(Kapton) Motherboard**