



(51) International Patent Classification:

H02M 1/32 (2007.01) H02M 3/335 (2006.01)

(21) International Application Number:

PCT/EP2018/065940

(22) International Filing Date:

15 June 2018 (15.06.2018)

(25) Filing Language:

English

(26) Publication Language:

English

(71) Applicant: ABB SCHWEIZ AG [CH/CH]; Brown Boveri Strasse 6, 5400 Baden (CH).

(72) Inventors: E, Amarendra; D.No: 12-8, Sitanagaram, East Godavari District, Andhra Pradesh, Jaggampeta 533435 (IN). KUMAR-NAYAK, Khirod; Banka Bihari Nagar, Gate Bazar, Odisha, Brahmapur 760001 (IN).

KASAL, Gaurav-Kumar; EF2, Navin Jayaram Garden-Manapakkam, Chennai, Tamilnadu 60016 (IN). SUBRAMANIAN, Sasitharan; 32, Periyur main st., Kolanalli (po), Erode DT, Tamilnadu, Chennai, Tamilnadu 638154 (IN).

(74) Agent: KRANSELL & WENNBORG KB; P.O. Box 27834, 115 93 Stockholm (SE).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JO, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA,

(54) Title: SERIES MODULAR DC TO DC CONVERTER

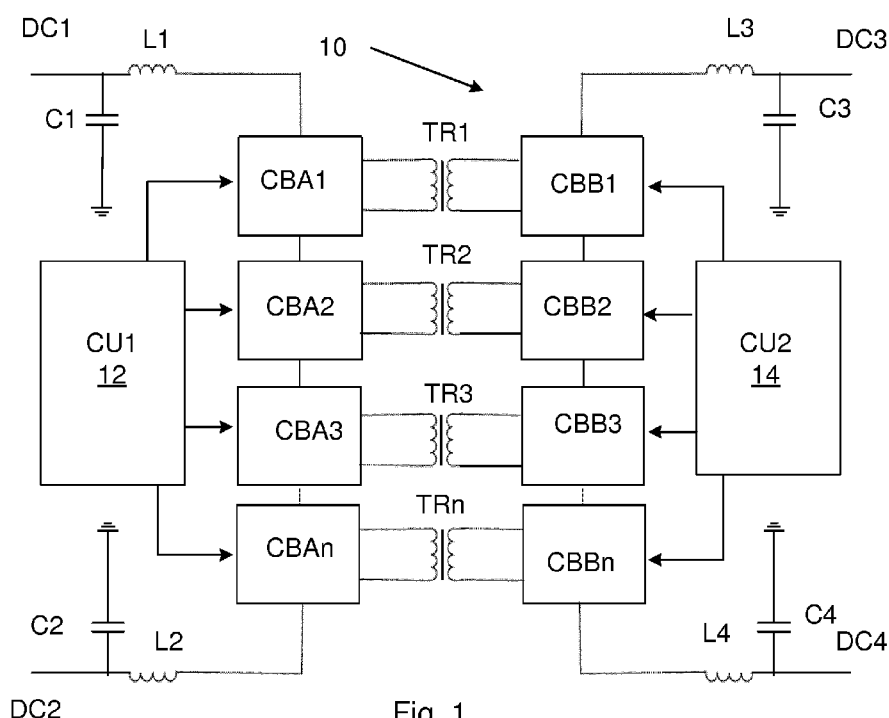


Fig. 1

(57) **Abstract:** A DC to DC converter (10) comprises n converter blocks (CBA1, CBA2, CBA3,... CBAn), each corresponding to an AC phase, connected in series in a first chain between a first and a second DC terminal DC1, DC2, n converter blocks CBB1, CBB2, CBB3, CBBn), each corresponding to an AC phase, connected in series in a second chain between a third and a fourth DC terminal (DC3, DC4) and n AC connections, each comprising a transformer (TR1, TR2, TR3, TRn) interconnecting two converter blocks, where $n \geq 3$. Each converter block comprises a wave-shaper branch connected in parallel with a switching arrangement. In each converter block of a chain the wave-shaper branch generates rectified wave shapes and the switching arrangement controls the application of rectified wave shapes across a winding of a corresponding transformer for generating an AC phase wave shape, while the sum of the rectified wave shapes of the chain contribute to the DC voltage between the two DC terminals.



SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN,
TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Declarations under Rule 4.17:

— *of inventorship (Rule 4.17(iv))*

Published:

— *with international search report (Art. 21(3))*

SERIES MODULAR DC TO DC CONVERTER

FIELD OF INVENTION

- 5 The present invention generally relates to voltage source converters. More particularly the present invention relates to a Direct Current to Direct Current converter.

BACKGROUND

10

The Modular multilevel converter (MMC) has become of interest to use in a number of different areas and topologies. One area of interest is the Direct Current - Direct Current (DC - DC) converter. One example of this is WO 2013/017160 where three parallel phase legs of one conversion unit
15 are coupled to three parallel phase legs of a second conversion unit. In this topology each arm of the MMC has to be rated for pole to pole voltage and thus the overall submodule rating becomes large.

20

One of the research trends has been to propose new topologies to minimize the overall submodule rating and thus, significantly reduce the overall cost, losses, and converter footprint. The series connection of phases has been identified as one of the best solution to achieve this objective.

25

Series connected topologies have been proposed for DC/DC conversion; see for instance WO 2013/004282. However, these converters typically require bulky capacitors in addition to submodules to support the DC-link voltage.

30

There is in this regard room for improvement especially if using this topology in DC/DC conversion and with a view of making the converter less bulky along with a reduction of overall cost and losses.

SUMMARY OF THE INVENTION

The present invention is directed towards obtaining a DC - DC converter
5 with reduced cost, size and losses.

This object is according to a first aspect of the present invention achieved through a Direct Current to Direct Current, DC to DC, converter comprising a first number n of converter blocks, each corresponding to an
10 alternating current, AC, phase and connected in series in a first chain between a first and a second DC terminal forming a first DC link, a second number n of converter blocks, each corresponding to an alternating current, AC, phase and connected in series in a second chain between a third and a fourth DC terminal forming a second DC link and a number n
15 of AC connections, each comprising a transformer interconnecting a converter block of the first chain with a corresponding converter block of the second chain, where $n \geq 3$, each converter block comprising: a wave-shaper branch connected in parallel with a switching arrangement, the wave-shaper branch comprising a first wave-shaper arm formed as a
20 chain-link of submodules and the switching arrangement comprising a first string of series-connected switches in parallel with the wave-shaper branch,
where in each converter block of a chain, the wave-shaper branch is configured to generate at least one rectified wave shape and the switching
25 arrangement is configured to control the application of said at least one rectified wave shape across a winding of a corresponding transformer for generating an AC phase wave shape and the sum of the rectified wave shapes of the chain contribute to the DC voltage between the two DC terminals.

30

It is possible that the converter comprises at least one control unit configured to control each converter block. Such control of a converter block may comprise controlling all wave-shaper arms of the wave-shaper

branch to generate rectified wave shapes and controlling the switching arrangement to apply rectified wave shapes across the winding of the corresponding transformer.

- 5 The AC wave shape is generated with a fundamental frequency. It is possible that this frequency is higher than 50/60 HZ, for instance higher than 100 Hz.

10 The present invention has a number of advantages. It has a low cost, low losses and a low rating. Furthermore the converter rating and the number of cells is lowered compared with previous converters of the same type.

BRIEF DESCRIPTION OF THE DRAWINGS

- 15 The present invention will in the following be described with reference being made to the accompanying drawings, where

fig. 1 schematically shows one general series modular multilevel converter realization of a DC/DC converter with a first and second chain of converter
20 blocks coupled to each other via transformers,

fig. 2 schematically shows a first realization of a converter block connected to one winding of a transformer,

fig. 3 schematically shows the generation of phase voltages by a chain of converter blocks in the first converter realization for forming sinusoidal
25 AC wave shapes,

fig. 4 schematically shows pole-to-pole DC voltages provided by the converter with different numbers of converter blocks in the chains,

fig. 5 schematically shows the generation of voltages of a chain of converter blocks in the first converter realization for forming trapezoidal AC wave
30 shapes,

fig. 6 schematically shows the first converter realization when there is a fault on an AC link between the two uppermost converter blocks of the two chains,

fig. 7 schematically shows a variation of the first converter realization for handling the fault on the AC link,

fig. 8 schematically shows a second realization of a converter block connected to one winding of a transformer,

5 fig. 9 schematically shows the generation of voltages of a chain of converter blocks in the second converter realization for forming sinusoidal AC wave shapes,

fig. 10 schematically shows the generation of voltages of a chain of converter blocks in the second converter realization for forming

10 trapezoidal AC wave shapes, and

fig. 11 schematically shows a variation of the second converter realization for handling a fault on the AC link between the two uppermost converter blocks of the two chains.

15 DETAILED DESCRIPTION OF THE INVENTION

In the following, a detailed description of preferred embodiments of the invention will be given.

20 Fig. 1 shows one realization of a series modular multilevel converter (SMMC) 10 for converting between Direct Current (DC) and Direct Current (DC). Such a converter may find use in a number of different networks, for instance in a High Voltage Direct Current (HVDC) network, where the HVDC network may operate at voltage levels of 400 kV and
25 above.

A first DC side of the converter 10 comprises a first and a second DC terminal DC1 and DC 2 and a second DC side of the converter comprises a third and a fourth DC terminal DC3 and DC4. The first and second DC
30 terminals DC1 and DC2 may here form a first DC link, while the third and fourth DC terminals may form a second DC link. Here it may be mentioned that the first DC terminal DC1 typically has the highest electric potential of

the first DC link, while the third DC terminal DC₃ has the highest electric potential of the second DC link.

The first DC terminal DC₁ is connected to a first inductor L₁ and the
5 second DC terminal DC₂ is connected to a second inductor L₂. A first capacitor C₁ is connected between ground and a junction between the first DC terminal DC₁ and the first inductor L₁. A second capacitor C₂ is connected between ground and a junction between the second DC terminal DC₂ and the second inductor L₂. There is also a first chain with a first
10 number n of converter blocks connected in series between the first and second inductor L₁ and L₂. The first chain of converter blocks, which chain forms a first conversion unit, is thereby connected between the first and second DC terminals DC₁ and DC₂. The number of converter blocks n connected in series in this way correspond to a number of Alternative
15 Current (AC) phase wave shapes that are to be generated by the converter blocks. As can be seen there is therefore a first converter block CBA₁, a second converter block CBA₂, a third converter block CBA₃ and an nth converter block CBA_n in the first chain.

20 In a similar manner the third DC terminal DC₃ is connected to a third inductor L₃ and the fourth DC terminal DC₄ is connected to a fourth inductor L₄. A third capacitor C₃ is connected between ground and a junction between the third DC terminal DC₃ and the third inductor L₃. A fourth capacitor C₄ is connected between ground and a junction between
25 the fourth DC terminal DC₄ and the fourth inductor L₄. There is also a second chain with a second number n of converter blocks connected in series between the third and fourth inductor L₃ and L₄. The second chain of converter blocks, which chain forms a second conversion unit, is thereby connected between the third and fourth DC terminals DC₃ and DC₄. The
30 number of converter blocks n connected in series in the second chain is the same as the number of AC phase wave shapes and the number of converter blocks in the first chain, which phases are to be generated by the converter blocks. Therefore, as can be seen in fig. 1 there is a first converter block

CBB₁, a second converter block CBB₂, a third converter block CBB₃ and an nth converter block CBB_n in the second chain. In many converter realizations $n = 3$. However, as can be seen in fig. 1, it is also possible that $n > 3$.

5

The converter blocks in the first chain are coupled to the converter blocks in the second chain through AC connections, each comprising a transformer interconnecting a converter block of the first chain with a corresponding converter block of the second chain. There are more particularly n transformers in the converter 10. The first converter block CBA₁ in the first chain is connected to a first winding, such as a primary winding, of a first transformer TR₁, while a first converter block CBB₁ of the second chain is connected to a second winding, such as a secondary winding, of the first transformer TR₁. The second converter block CBA₂ in the first chain is connected to a first winding of a second transformer TR₂, while a second converter block CBB₂ of the second chain is connected to a second winding of the second transformer TR₂. The third converter block CBA₃ in the first chain is connected to a first winding of a third transformer TR₃, while a third converter block CBB₃ of the second chain is connected to a second winding of the third transformer TR₃. Finally, the nth converter block CBA_n in the first chain is connected to a first winding of an nth transformer TR_n, while an nth converter block CBB_n of the second chain is connected to a second winding of the nth transformer TR_n.

Each converter block forms an Alternating Current (AC) wave shape across the corresponding transformer winding. Thereby a pair of converter blocks in the first and second chains corresponds to an AC phase.

In order to form the different wave shapes there is a first control unit CU₁ 12 controlling the converter blocks in the first chain and a second control unit CU₂ 14 controlling the converter blocks in the second chain. The chains of converter blocks may be provided at the same location and

30

therefore the first and second control units 12 and 14 may with advantage be combined into one control unit.

A control unit may be realized in the form of discrete components, such as one or more Field Programmable Gate Arrays (FPGAs), Application Specific Integrated Circuits (ASICs) or Digital Signal Processors (DSPs). However, it may also be implemented in the form of a processor with accompanying program memory comprising computer program code that performs the desired control functionality when being run on the processor.

As has been indicated earlier, it is possible that the converter 10 in fig. 1 may only comprise 3 phases. It should also be realized that it is possible to omit one or more of the first, second, third and fourth capacitors C1, C2, C3 and C4 as well as to remove one or more of the first, second, third and fourth inductors L1, L2, L3 and L4.

A converter block typically comprises a wave-shaper branch generating at least one rectified wave shape and comprising at least one wave-shaper arm, where the branch is connected in parallel with a switching arrangement comprising at least two switches, which two switches are placed in a first string of series-connected switches. The first string of series-connected switches is thereby also connected in parallel with the wave-shaper branch. A converter block is more particularly configured to generate an AC wave shape based on the at least one rectified wave shape, which AC wave shape forms an AC phase. On the DC side the voltages of the rectified wave shapes generated by the wave-shaper arms of the converter blocks are summed up to form the DC voltage between the two DC terminals between which the converter modules are connected. The DC terminals associated with a chain of converter blocks are often connected to two poles of a DC system and therefore the voltage between these two DC terminals is often called a pole-to-pole voltage or DC link voltage.

There are a number of ways in which the converter blocks may be formed.

A first variation of a converter block is shown in fig. 2. The converter block comprises a single wave-shaper arm. The wave-shaper branch thus
5 comprises a single wave-shaper arm, in the figure shown as an AC voltage source 16, connected in parallel with the switching arrangement SA1, which switching arrangement SA1 comprises the previously mentioned first string of switches S1 and S3 and a second string of switches S4 and S2 in parallel with the first string, which two strings of switches are both
10 connected in parallel with the voltage source 16. Thereby the switching arrangement SA1 is an H bridge switching arrangement. The transformer winding connected to the converter block has one end connected to a junction between the first and the third switch S1 and S3 in the first string of switches and the other connected to a junction between the fourth and
15 the second switch S4 and S2 of the second string of switches. The voltage source 16 may here be formed as a chain-link of submodules with bipolar voltage contribution capability, such as full-bridge submodules SMA1 and SMA2, or as a chain-link of submodules with unipolar voltage contribution capability, such as half-bridge submodules SMB1 and SMB2. The switches
20 may in turn be realized as a string of transistors, like Insulated Gate Bipolar Transistors (IGBTs), with anti-parallel diodes.

In fig. 2 the AC voltage V_{ac} across the transformer winding is also shown as well as the rectified phase voltage V_{dc_ph} generated by the wave-shaper
25 arm 16.

It should here be realized that a wave-shaper arm may also be made up of a mixture of submodules with unipolar and bipolar voltage contribution capabilities.
30

The wave-shaper arm thus has series-connected half-bridge or full-bridge submodules, and an H-bridge switching arrangement connected in parallel with the wave-shaper arm. Each string in the H-bridge arrangement may

comprise two series connected switches. In each converter block of a chain, the wave-shaper arm 16 generates at least one rectified wave shape, here in the form of a single rectified AC voltage, and the switches of the switching arrangement SA1 operate at a fundamental switching frequency to

5 generate an AC voltage V_{ac} across the transformer winding, which fundamental frequencies may as an example be higher than 50/60 Hz, such as above 100 Hz. The switching arrangement thereby controls the application of the at least one wave shape across a winding of the corresponding transformer for generating an AC phase wave shape. The

10 sum of the rectified wave shapes in a chain also contributes to the DC voltage between the DC terminals, i.e. contributes to the DC voltage on the DC link.

As a control unit is used to control a converter block, it is clear that a

15 control unit controls the turning on and off of the switches in the submodules as well as the switches in the switching arrangement in order to perform the DC/DC conversion.

Fig. 3 shows one way of operating a chain of converter blocks in the first

20 realization of the converter in order to form three rectified wave shapes or rectified phase voltages for forming three sinusoidal AC wave shapes as well as for forming a DC voltage.

Each wave-shaper arm 16 of a converter block forms a rectified AC wave

25 shape, here in the form of a rectified sine wave, where three rectified sine waves V_{dc_p1} , V_{dc_p2} and V_{dc_p3} are separated or phase shifted from each other by 60 degrees, which through the control of the switching arrangement SA1 results in non-rectified phase voltages phase shifted from each other by 120 degrees. Here the first and the second switch S1

30 and S2 are jointly operated for applying one polarity of the rectified wave shape V_{dc_ph} across the corresponding transformer winding and the third and fourth switches S3 and S4 are operated for changing the polarity of the rectified wave shape across the transformer winding and thereby an AC

wave shape is obtained. It can be seen that every other rectified wave shape has its polarity reversed.

However, for forming the DC voltage, the rectified wave shapes V_{dc_ph1} ,
5 V_{dc_ph2} and V_{dc_ph3} of the phases are added to each other and thereby a pole-to-pole DC voltage V_{pp} or DC link voltage with ripple as shown in fig. 3 is obtained, which ripple is smoothed through suitable filtering, such as through using the capacitors and inductors connected to the DC terminals.

10

In the 3-phase operation shown in fig. 3, the pole to pole voltage will be equal to the sum of three rectified AC voltages having 60° phase shift, which will have $6N$ harmonic ripple, i.e. the ripple has sixth order harmonics.

15 This ripple can be significantly reduced through the addition of further phases. This is done through adding converter blocks in the chains coupled to each other via additional transformers. The reduction of ripple can be observed in fig. 4, which shows the pole-to-pole DC voltage or DC link voltage for three phases V_{pp3} , for 5 phases V_{pp5} , for nine phases V_{pp9}
20 and fifteen phases V_{pp15} , where each converter block is rated 1 per unit (p.u.). As can be clearly seen in fig. 4, the ripple decreases the more phases that are used and thereby the filtering requirements are relaxed the more phases that are used.

25 To reduce the ripple in the DC link, the number of phases may thus be increased. The total number of phases could as an example be an odd number to achieve minimum possible ripple. Thereby the number of converter blocks will also be odd. Utilizing higher number of phases is useful for very high-power applications. When the number of phases are
30 increased, the rating of each transformer used can be limited. The volume occupied by a transformer is thereby limited and it is therefore also more easily transported.

The comparison of voltage ripple with different number of phases is shown in Table 1. It can be observed that the voltage ripple reduces significantly as the number of phases increase.

	Min(multiple of AC peak)	Max(multiple of AC peak)	Ripple (pu)
3 phases	1.7320508	2	0.26794919
5 phases	3.0776835	3.236068	0.15838444
9 phases	5.6712818	5.7587705	0.08748866
15 phases	9.5143645	9.5667722	0.05240778

TABLE I

5

For DC to DC converter applications, the voltage wave shapes across transformer need not be sinusoidal. It is thus possible to use other wave shapes in order to reduce the ripple.

- 10 This is exemplified in fig. 5, which shows three phases Vdc_ph1, Vdc_ph2 and Vdc_ph3 of a rectified wave shape separated by sixty degrees from each other and obtained via the first converter realization. In this case the wave shape is a trapezoid wave shape having a cycle divided into six time intervals. In a first time interval, the voltage of the shape rises to a
- 15 maximum level Vdc, which maximum level is then kept for a number of time intervals, in this example three. The maximum level is thereby kept during a second, third and fourth time interval. Then the voltage decreases to zero during a fifth time interval and remains at zero during a sixth time interval. The wave shape generation is then repeated. The rectified wave
- 20 shape thus has a cycle where the voltage rises from zero to a maximum value during a first time interval, decreases from the maximum value to zero during a last time interval and remains at the maximum value during intermediate time intervals. It can be seen that through this type of rectified AC curve generation, a smooth DC link voltage Vpp is obtained
- 25 requiring a minimum amount of filtering.

The ripple in the pole-to-pole voltage can thus be made almost zero with the trapezoidal modulation scheme. The basic idea is that one of the arm voltages should be ramped up while another is ramped down so that the pole-to-pole voltage is always maintained equal to $2V_{dc}$ (considering
5 ripple in submodule capacitor voltages is negligible).

In the converter according to the first realization a fault may occur in the uppermost AC connection, i.e. in the connection between the first converter block CBA1 of the first chain and the first converter block CBB1
10 of the second chain. This connection may be termed an internal converter bus. An example of such a fault occurring in the first converter block CBA1 of the first chain adjacent the first transformer TR1 is schematically shown in fig. 6. It can be seen that the fault will lead to a fault current running from the first DC terminal DC1 through the first converter block CBA1 of
15 the first chain to the fault on the AC connection. In the first converter block CBA1, the fault current more particularly runs through the first wave-shaper arm and the switching arrangement. One issue with series connected topologies is submodule capacitors of the upper-most arms in the uppermost converter blocks get over-charged during such an internal
20 converter bus fault.

Consider a 3-phase system, if there is converter bus fault between the first converter blocks of the first and second chains, then the pole-to-pole voltage will appear across the wave-shaper arm of the first converter block
25 CBA1 in the first chain, as shown in Figure 6. Even if the converter is blocked, the sum of the submodule voltages V_{dc} of the upper-arm V_{dc} is less than pole to pole voltage and thus, the submodule capacitors get charged via diodes up to $2V_{dc}$.

30 In order to handle such a fault and protect the submodules, it may be necessary to bypass one or more wave-shaper arms. One variation of the first converter realization of the converter that achieves this is schematically shown in fig. 7. As can be seen in this figure there is a first

string of bypass switches BPSS1 connected in parallel with the first converter block of the first chain and a second string of bypass switches BPSS2 connected in parallel with the first converter block of the second chain, where such a string of bypass switches is connected in parallel with the wave-shaper arm of the corresponding converter block. The string of bypass switches is thus connected in parallel with the wave-shaper arm of the converter block that is closest to the DC terminal of the DC link having the highest electric potential. The bypass switches may in this case be realized as thyristors, which are jointly being turned on in the case of detection of fault on the AC connection. The thyristors may be oriented downwards. Their direction of conductivity may thus be from the first and third DC terminals DC1 and DC3 towards the second and fourth DC terminals DC2 and DC3, respectively.

During converter bus fault at the top-most phase, the thyristors should thus be fired so that a fault current will be bypassed from the submodules of the top-most wave-shaper arm. It should be noted that, more thyristor bypass strings may be required if the number of phases increase. For example, consider a 9-phase system with each wave-shaper arm rated for $V_{dc}/3$ such that the pole-to-pole voltage obtained is $2V_{dc}$. If it is assumed that a sinusoidal modulation scheme is utilized, then the bypass thyristors are required for all those top-most arms whose sum of submodule voltages reach up to $2U_d$ to prevent the overcharging of submodule capacitors due to the fault current. The faulty AC connection will block up to $V_{dc}/3$ and hence it is necessary to bypass $2V_{dc} - (V_{dc}/3)$ submodules that is equal to $5V_{dc}/3$, i.e., top five phase valve arms need to be bypassed. It can thereby be seen that the number of wave-shaper arms being bypassed is a number that together have a rating that is the difference between the DC link voltage and the rating of one wave-shaper arm.

A second variation of a converter block is shown in fig. 8. The converter block comprises two wave-shaper arms. The wave-shaper branch thus comprises two wave-shaper arms, a first and a second wave-shaper arm, in

the figure shown as a first and second AC voltage source 16A and 16B, with the branch being connected in parallel with a switching arrangement SA2, which switching arrangement SA2 only comprises a first string of switches S1, comprising a first and a second switch S1 and S2. Thereby the string of switches S1 and S2 is also connected in parallel with the two voltage sources 16A and 16B. The transformer winding connected to the converter block has one end connected to a junction between the first and the second switches S1 and S2 in the first string of switches and the other connected to a junction between the two voltage sources 16A and 16B. Also in this case the voltage sources 16A and 16B may be formed as chain-links made up of submodules with bipolar voltage contribution capability, such as full-bridge submodules SMA1 and SMA2 or as a chain-link of submodules with unipolar voltage contribution capability, such as half-bridge submodules SMB1 and SMB2. The switches may in turn be realized as a string of transistors, like anti-parallel thyristors or IGBTs, with anti-parallel diodes or as a string of anti-parallel thyristors. Also in this case a wave-shaper arm may be made up of a mixture of submodules with unipolar and bipolar voltage contribution capabilities. The first voltage source 16A in this case generates a first voltage or rectified wave shape V_{dc_phU} for an upper or positive half of an AC wave shape and the second voltage source 16B generates a second voltage or rectified wave shape V_{dc_phL} for a lower or negative half of the AC wave shape.

The converter block may thus comprise two wave-shaper valve arms which comprise series-connected half-bridge or full-bridge submodules, and a switching arrangement or two director valve arms that comprise series-connected switches that may be IGBTs with anti-parallel diodes or series-connected anti-parallel thyristors. If anti-parallel thyristors are utilized in the switching arrangement SA2, the wave-shaper arm requires 100% full-bridge submodules in order to provide commutation voltage and most importantly to sustain transient operating conditions. On the other hand, the state-of-the-art DC/DC converters utilize higher fundamental frequency to reduce the transformer size and cost. However, thyristor

commutation will be difficult at higher fundamental frequency due to time limitations in each fundamental cycle. The switching arrangement (director valve arm) may thereby need to be realized as series connected IGBTs for higher fundamental switching frequency operation.

5

The operation may generally be performed in the following way.

In a converter block, one of the wave-shaper arms, denoted an active or AC arm, generates the rectified AC voltage and the corresponding director
10 valve or switch of the switching arrangement connects this active arm and applies the generated rectified wave shape to the transformer. At any instance in time, three out of six wave-shaper arms are utilized for generating AC voltages and the other three arms, which may be denoted DC arms or inactive, are available for compensating the voltage ripple in
15 the pole-to-pole DC voltage. For example, during a positive half-cycle, the upper arm of each phase will be connected to the transformer winding of the AC connection, while the lower arm compensates the DC voltage ripple. The AC voltage wave shape across the transformer can be trapezoidal or sinusoidal.

20

The first wave-shaper arm 16A thus generates a first rectified wave shape V_{dc_phU} and the second wave-shaper arm 16B generates a second rectified wave shape V_{dc_phL} . Moreover, the first switch S1 of the first string in the switching arrangement SA2 applies the wave shape V_{dc_phU}
25 of the first wave-shaper arm 16A across the connected transformer winding for generating a positive half cycle of the AC wave shape V_{ac_ph} and the second switch S2 of the first string in the switching arrangement SA2 applies the wave shape V_{dc_phL} of the second wave-shaper arm 16B across the connected transformer winding for generating the negative half
30 cycle of the AC wave shape V_{ac_ph} .

Thereby a wave-shaper arm has a half cycle when it is active in contributing to forming of an AC wave shape and a half cycle when it is

inactive in such forming. An inactive wave-shaper arm of at least one converter block in a chain may then be used for countering voltage ripple in the DC link voltage caused by the rectified wave shapes of the active wave-shaper arms.

5

Fig. 9 shows one way of operating a chain of three converter blocks in the second realization of the converter in order to form a number of phase voltages that are to be used for forming three sinusoidal AC wave shapes as well as for forming a DC voltage.

10

Each wave-shaper arm of a converter block forms one half cycle of the AC wave shape of the phase. Thereby, in the first or upper wave-shaper arms 16A of the converter blocks are used for forming the voltages V_{dc_phU1} , V_{dc_phU2} and V_{dc_phU3} of the positive half cycle and the second or lower wave-shaper arms 16B of the converter blocks are used for forming the voltages V_{dc_phL1} , V_{dc_phL2} and V_{dc_phL3} of the negative half cycle. However, as can be seen both wave-shaper arms generate positive wave shapes. The switching arrangement SA2 controls which of the generated voltages is to be applied across the transformer winding as well as the polarity, where the first switch S1 connects the first upper wave-shaper arm 16A across the transformer winding with one polarity and the second switch S2 connects the second lower wave-shaper arm 16B across the winding with an opposite polarity.

15

20

25

The three rectified sine waves provided by the wave-shaper branches (upper and lower wave-shaper arms) of the converter blocks are separated or phase shifted from each other by a phase shift of 60 degrees, which through the control of the switching arrangement SA2 results in a non-rectified phase voltage separated from the other phase voltages by of 120 degrees.

30

In order to form the DC voltage, the rectified wave shapes are added to each other and thereby a DC voltage V_{dc_sum} with ripple as shown in fig.

9 is obtained. However, this ripple is avoided through the inactive wave-shaper arms being used to compensate the ripple, where inactive in this case means inactive in relation to the forming of an AC wave shape on the AC connection. The inactive or DC wave-shaper arms are thus used to
 5 form a compensating voltage V_{dc_comp} in order to counteract the ripple, resulting in the pole-to-pole DC voltage V_{pp} shown in fig. 9. This provides a significant relaxation of the filtering requirements. This compensation thus reduces the ripple on the DC voltage. However, it does not influence the AC voltage.

10

Put differently, for a 3-phase system, each AC arm will generate the rectified AC voltage as commanded by the corresponding control unit. The sum of the active arm voltages will have $6N$ harmonic ripple and the other three DC arms are utilized to compensate the ripple as shown in Figure 9.
 15 If each arm rating is considered one p.u., then the pole-to-pole voltage will be equal to two p.u.

Also in this second variation of the converter it is possible to use a trapezoidal wave shape, which is schematically shown in fig. 10. Fig. 10
 20 shows the operation of the upper and lower wave-shaper arms of three converter blocks together with the generation of the DC voltage and the three AC voltages. In this case a cycle of the wave-shaper branch comprises a first half made up of six time intervals. The cycle of the rectified wave shape thereby comprises twelve time intervals. As can be seen the upper
 25 wave-shaper arm of a converter block forms a voltage or rectified wave shape V_{dc_phU1} to be used for forming a positive half of a trapezoid AC wave shape, where during a first time interval the voltage of the shape rises to a maximum level $2V_{dc}/3$, which maximum level is then kept for a number of time intervals, in this example four, which are in a second,
 30 third, fourth and fifth time interval. Then the voltage decreases to zero during a further sixth time interval, whereupon the lower wave-shaper arm performs the same operation, i.e. forms a voltage V_{dc_phL1} comprising the same shape during an equal number of following time intervals to be

used for the negative half-cycle of the AC wave shape. It can also be seen that when the upper wave-shaper arm forms the positive half-cycle of the AC wave shape, the lower phase arm is used to compensate DC ripple. It can more particularly be seen that at the maximum level of the formed

5 wave shape, the voltage V_{dc_phL1} of the lower wave-shaper arm rises to half the maximum level in the second time interval and then goes back to zero in the third time interval, again rises to half the maximum level during the fourth time interval followed by a return to zero during the fifth time interval. The upper wave-shaper arm has the same type of operation

10 when the lower wave-shaper arm generates the rectified wave shape that is to be used for the negative half- cycle of the trapezoidal shape. Thereby in the second half of the cycle of the wave-shaper branch the rectified wave shape V_{dc_phU1} stays at zero in a seventh time interval, rises to half the maximum level in an eighth time interval, goes back to zero in a ninth time

15 interval, again rises to half the maximum level during a tenth time interval, returns to zero during an eleventh time interval and then stays at zero in the twelfth time interval.

This type of operation is performed by the three converter blocks with a

20 separation of 120 degrees in relation to each other, which as can be seen leads to the forming of trapezoidally shaped AC voltages separated by 120 degrees from each other and a smooth DC voltage V_{pp} . Thereby a smooth DC voltage is also here obtained requiring a minimum amount of filtering.

25 The submodule utilization is further improved by the use of the trapezoidal modulation scheme. It should be observed that the pole-to-pole voltage equal to $2V_{dc}$ is obtained with a valve rating of $2V_{dc}/3$ instead of V_{dc} with sinusoidal modulation scheme. Thereby it is possible to also lower the voltage rating and consequently also the number of components used,

30 compared with the sinusoidal scheme.

Also in the converter according to the second realization a fault may occur in the uppermost AC connection, i.e. in the connection between the first

converter block CBA1 of the first chain and the first converter block CBB1 of the second chain.

In order to handle such a fault and protect the submodules, it may be
5 necessary to bypass one or more of the wave-shaper arms. One variation of the second realization of the converter that achieves this is schematically shown in fig. 11. As can be seen in this figure there is a first string of bypass switches BPSS1 connected in parallel with the first or upper wave-shaper arm UA of the wave-shaper branch in the first converter block CBA1 in the
10 first chain and a second string of bypass switches BPSS2 connected in parallel with the first or upper wave-shaper arm UA of the wave-shaper branch in the first converter block CBB1 in the second chain. The bypass switches may also in this case be realized as thyristors, which are jointly being turned on in the case of a detection of a fault on the AC connection.

15 Also in this case the thyristors are oriented downwards. Their direction of conductivity is thus towards the second and fourth DC terminals, respectively.

20 The submodule capacitors of upper-most arms are thereby protected from getting over-charged during internal converter bus fault at the upper-most phases. The pole to pole voltage will appear across the upper-arm of the first converter block CBA1 if there is converter bus fault at the corresponding AC connection and thus in the case of a fault, the
25 submodule capacitors get charged via diodes up to 2Vdc. This is avoided through firing all the downward oriented thyristors in the director valve arm. If IGBTs are used in the switching arrangement (director valve arms), then the bypass thyristors may be required across upper-most arms as shown in Figure 11. It should be noted that, more thyristor bypass arms
30 may be required if the number of phases increase. Basically, the bypass thyristors are required for all those top-most arms whose sum of submodule voltages reach up to 2Vdc, i.e. the pole to pole or DC-link

voltage, to prevent the overcharging of submodule capacitors due to fault current.

As can be seen above, the invention has a number of advantages. It provides reduced cost, size, and losses compared to front-to-front converters with parallel wave-shaper arms. It also provides bypass of submodules during internal converter bus fault. If submodules with bipolar voltage contribution capability are used, then the converter also has a DC link fault blocking capability.

10

In table II there is a comparison between submodule rating of the topologies and a front to front DC /DC converter using parallel wave-shaper arms.

Topology	Overall submodule rating
Front-to-front	$24 \cdot V_{dc}$
First realization	$6 \cdot V_{dc}$
Second realization (sinusoidal)	$12 \cdot V_{dc}$
Second realization (trapezoidal)	$8 \cdot V_{dc}$

TABLE II

15

The invention can be varied in a multitude of ways. There are a number of ways in which switches may be implemented. They may be implemented as anti-parallel self-commutated components, such as two transistors, like Insulated Gate Bipolar Transistors (IGBTs) or Metal–Oxide–

20

Semiconductor Field-Effect Transistor (MOSFETs), or Integrated Gate-Commutated Thyristors (IGCTs), using a self-commutated circuits with anti-parallel circuit-commutated circuits, such as an IGBT or IGCT together with a diode or thyristor, or as anti-parallel circuit commutated components, such as two anti-parallel thyristors or a thyristor with anti-

parallel diode. A self-commutated component is here a component that may be directly turned off through receiving a control signal in order to stop conducting current, while a circuit-commutated component is a component needing an applied negative voltage to stop conducting
5 current, for instance through the use of a dedicated circuit. As a thyristor is an example of one type of circuit commutated component, it can be seen that this type of circuit commutated component also has the ability of being directly turned on through receiving a control signal in addition to requiring an applied external negative voltage for being turned off. Such
10 circuit-commutated turn off may with advantage be achieved through applying a negative voltage across the circuit-commutated component, for instance using a submodule with bipolar voltage contribution capability.

Moreover, a valve may be realized through a number of series-connected
15 component combinations of the types described above.

From the foregoing discussion it is evident that the present invention can be varied in a multitude of ways. It shall consequently be realized that the present invention is only to be limited by the following claims.

CLAIMS

1. A Direct Current to Direct Current, DC to DC, converter (10) comprising a first number n of converter blocks (CBA1, CBA2, CBA3, ...
5 CBA_n), each corresponding to an alternating current, AC, phase and connected in series in a first chain between a first and a second DC terminal (DC1, DC2) forming a first DC link, a second number n of converter blocks (CBB1, CBB2, CBB3, CBB_n), each corresponding to an alternating current, AC, phase and connected in series in a second chain
10 between a third and a fourth DC terminal (DC3, DC4) forming a second DC link and a number n of AC connections, each comprising a transformer (TR1, TR2, TR3, TR_n) interconnecting a converter block of the first chain with a corresponding converter block of the second chain, where $n \geq 3$, each converter block comprising:
15 a wave-shaper branch connected in parallel with a switching arrangement (SA1; SA2), said wave-shaper branch comprising a first wave-shaper arm (16; 16A) formed as a chain-link of submodules and said switching arrangement comprising a first string of series-connected switches (S1, S3; S1, S2) in parallel with the wave-shaper branch,
20 where in each converter block of a chain, the wave-shaper branch is configured to generate at least one rectified wave shape (V_{dc_ph}; V_{dc_phU}, V_{dc_phL}) and the switching arrangement (SA1; SA2) is configured to control the application of said at least one rectified wave shape across a winding of a corresponding transformer for generating an
25 AC phase wave shape (V_{ac}; V_{ac_ph}) and the sum (V_{dc_sum}) of the rectified wave shapes of the chain contribute to the DC voltage between the two DC terminals.
2. The converter (10) according to claim 1, wherein the number n >
- 30 3.
3. The voltage source converter according to claim 2, wherein the number n is an odd number.

4. The converter (10) according to any previous claim, further comprising a string of bypass switches (BPSS1, BPSS2) in parallel with the first wave-shaper arm of at least one converter block (CBA1, CBB1) of each chain, which converter block is the converter block of the chain closest to the DC terminal of the corresponding DC link with the highest potential.

5. The converter (10) according to claim 4, where the strings of bypass switches are provided for a number of wave-shaper arms, which number has a rating that is the difference between the DC link voltage and the rating of one wave-shaper arm.

6. The converter (10) according to previous claim, wherein the switching arrangement of each converter block comprises a second string of switches (S2, S4) in parallel with the first string (S1, S3), where the first wave-shaper arm (16) is configured to generate said rectified wave shape as a rectified AC wave shape (Vdc_ph) and the switching arrangement is configured to provide the AC wave shape (Vac) through changing the polarity of the rectified AC wave shape.

7. The converter (10) according to claim 6, wherein one end of said transformer winding is connected to a junction between the switches of the first string and the other end is connected to a junction between the switches of the second string.

8. The converter according to any of claims 1 - 5, wherein the wave-shaper branch of each converter block comprises a second wave-shaper arm (16B), where the first wave-shaper arm (16A) is configured to generate a first rectified wave shape (Vdc_phU), the second wave-shaper arm (16B) is configured to generate a second rectified wave shape (Vdc_phL), a first switch (S1) of the first string in the switching arrangement (SA2) is configured to apply the wave shape (Vdc_phU) of the first wave-shaper arm (16A) across the connected transformer winding for generating a

positive half cycle of the AC wave shape (Vac_ph) and a second switch (S2) of the first string in the switching arrangement (SA2) is configured to apply the wave shape (Vdc_phL) of the second wave-shaper arm (16B) across the connected transformer winding for generating the negative half
5 cycle of the AC wave shape (Vac_ph).

9. The converter (10) according to claim 8, wherein one end of the transformer winding is connected to a junction between the switches (S1, S2) of the first string and the other end is connected to a junction between
10 the two wave-shaper arms (16A, 16B).

10. The converter (10) according to claim 8 or 9, wherein each wave-shaper arm has a half cycle when it is active in contributing to a forming of an AC wave shape and a half cycle when it is inactive in such forming and
15 an inactive wave-shaper arm of at least one converter block in a chain is configured to counter voltage ripple in the DC link voltage caused by the rectified wave shapes of the active wave-shaper arms.

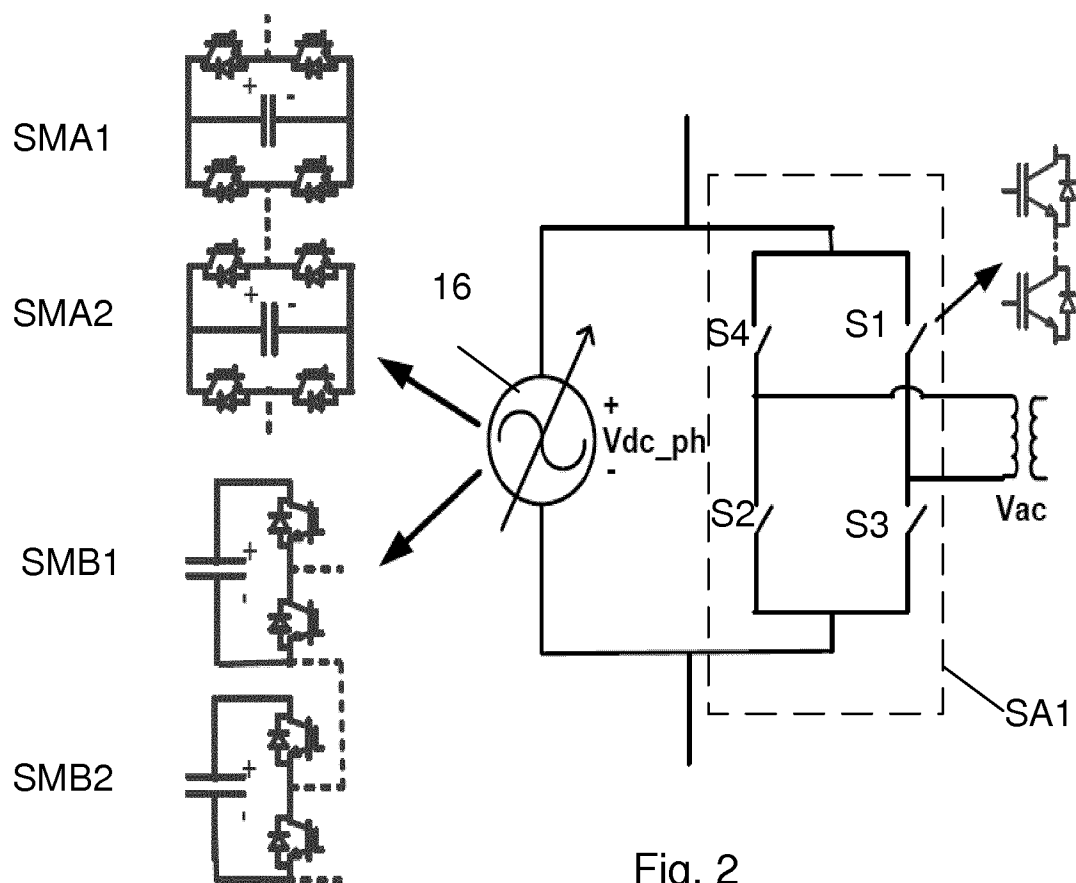
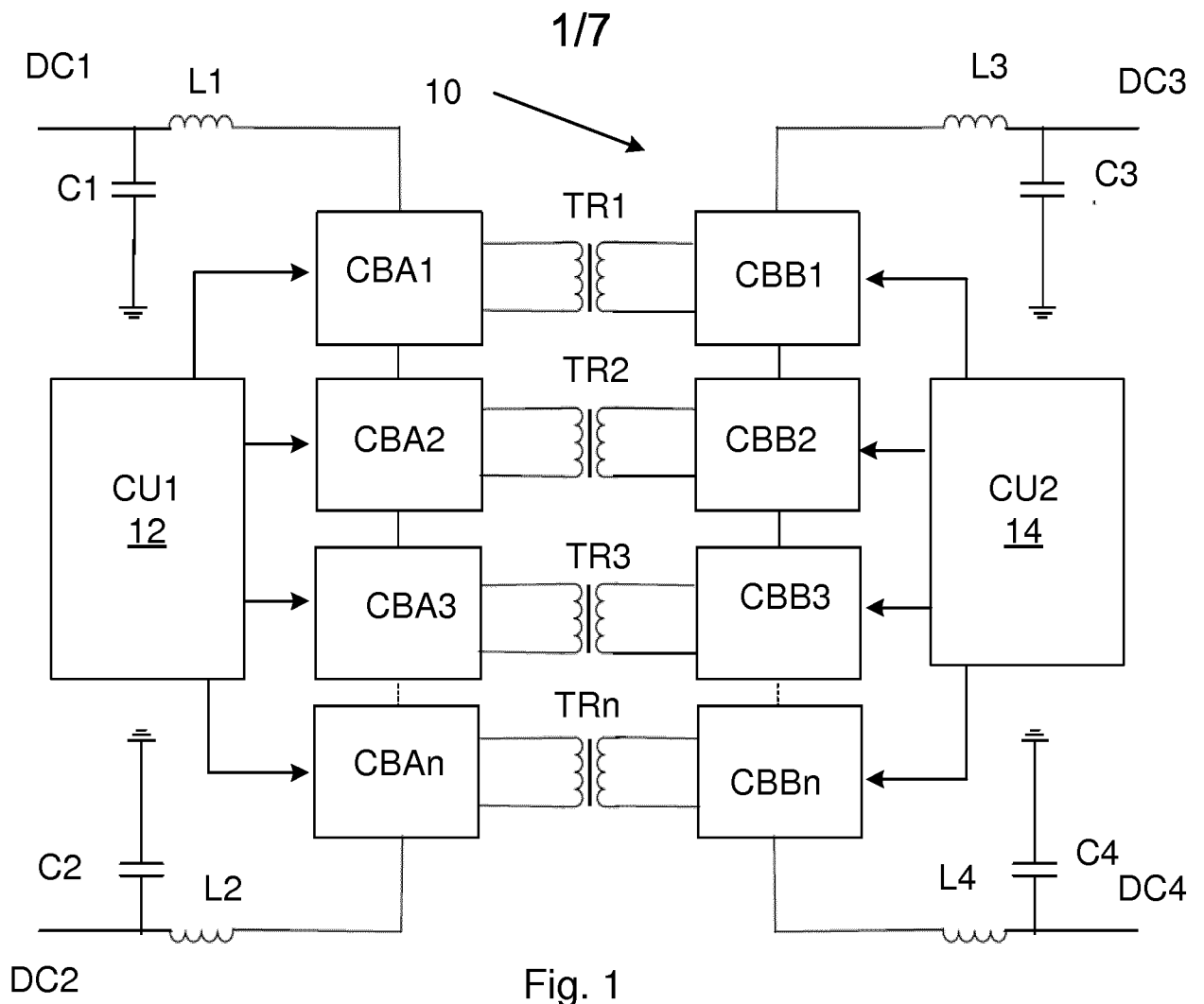
11. The converter according to any previous claim, wherein the AC wave shape is a sinusoidal wave shape.
20

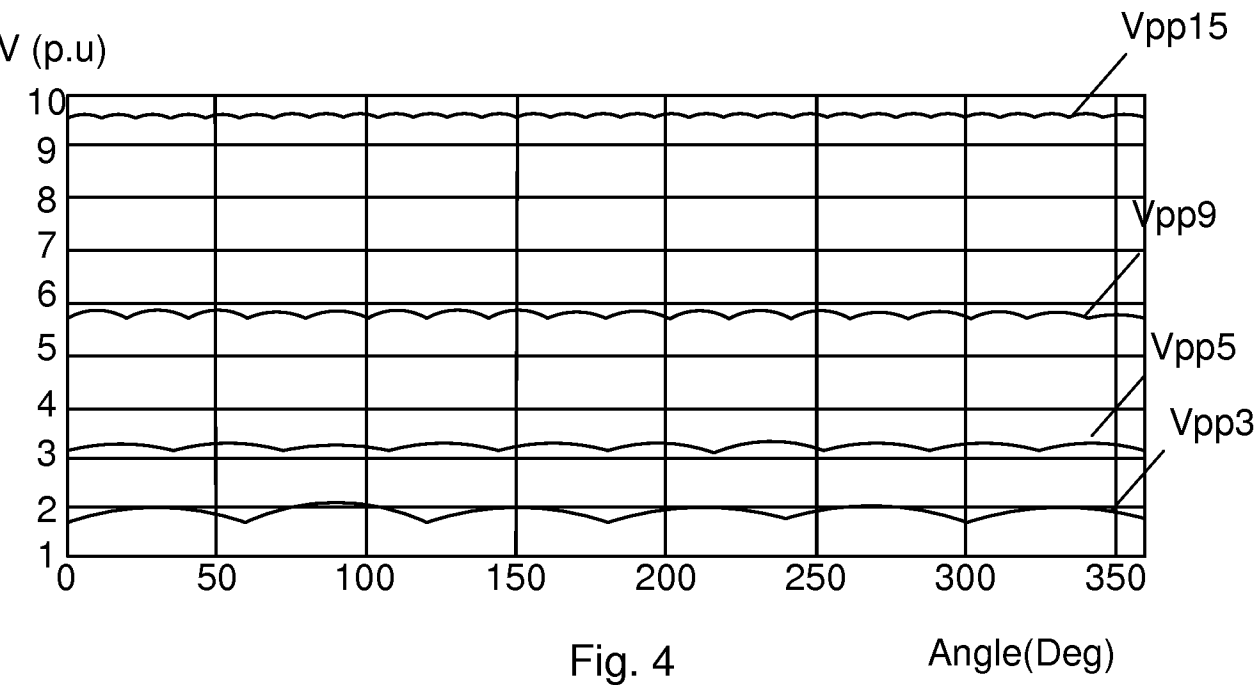
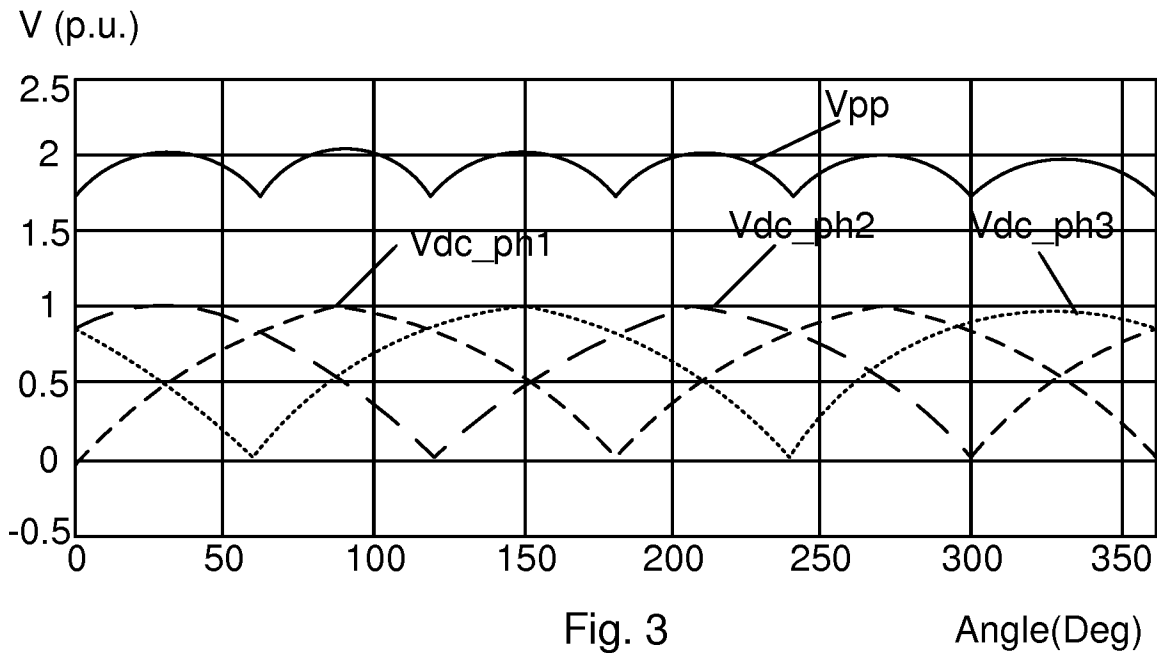
12. The converter according to any of claims 1 - 10, wherein the AC wave shape is a trapezoidal wave shape.

13. The converter according to claim 12, wherein the rectified wave
25 shape has a cycle where the voltage rises from zero to maximum value during a first time interval, decreases from the maximum value to zero during a last time interval and remains at the maximum value during intermediate time intervals.

30 14. The converter according to any previous claim, wherein a chain-link of submodules comprises submodules with a unipolar voltage contribution capability.

15. The converter according to any previous claim, wherein a chain-link of submodules comprises submodules with a bipolar voltage contribution capability.





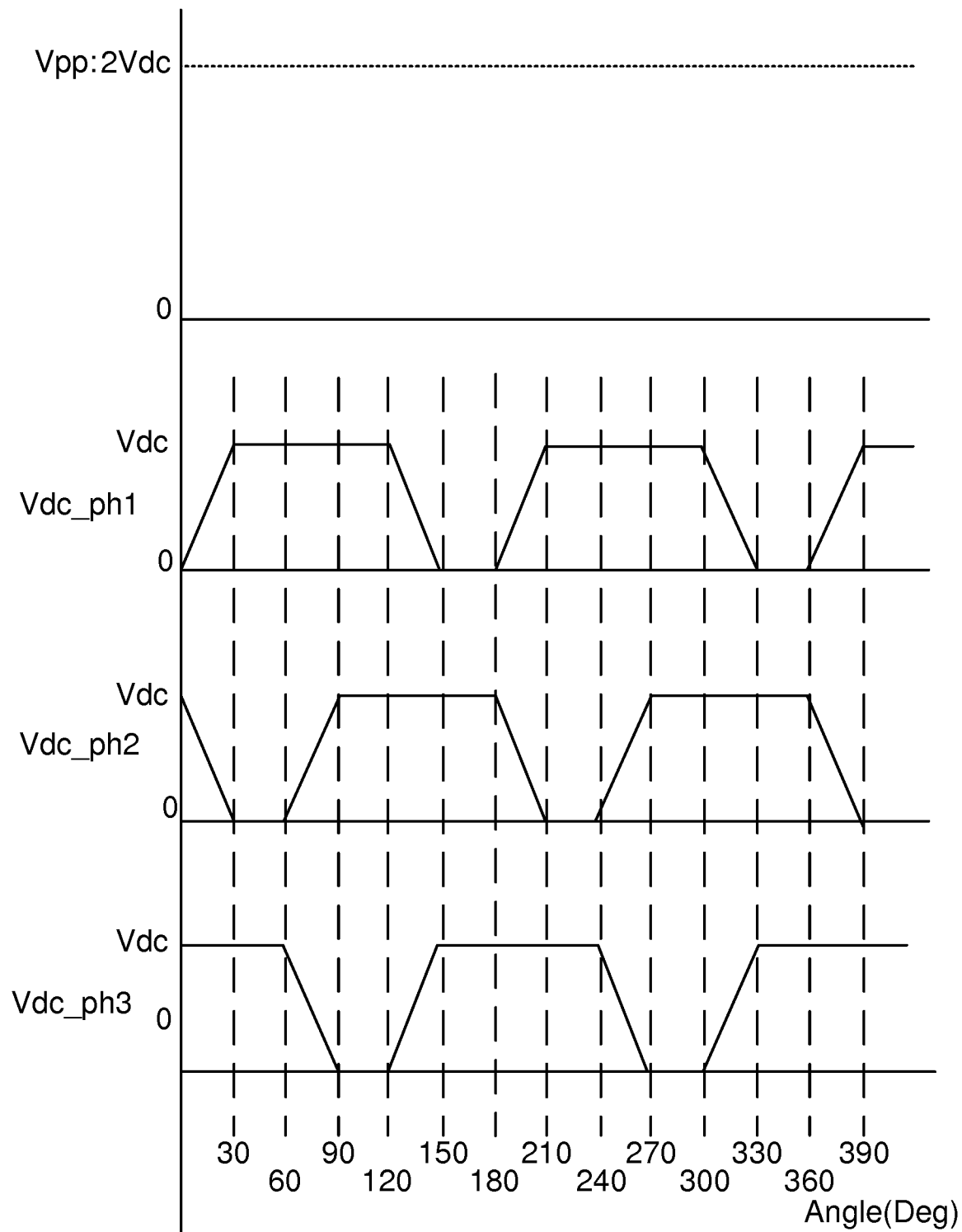
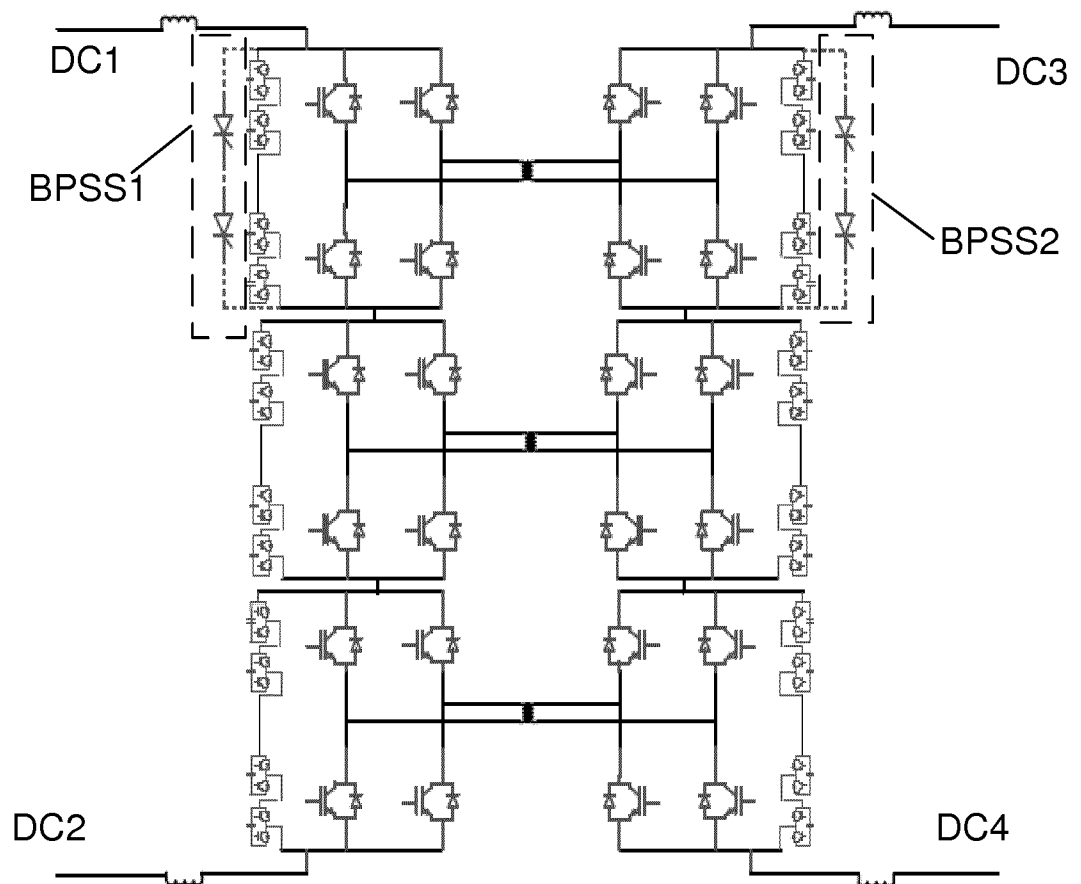
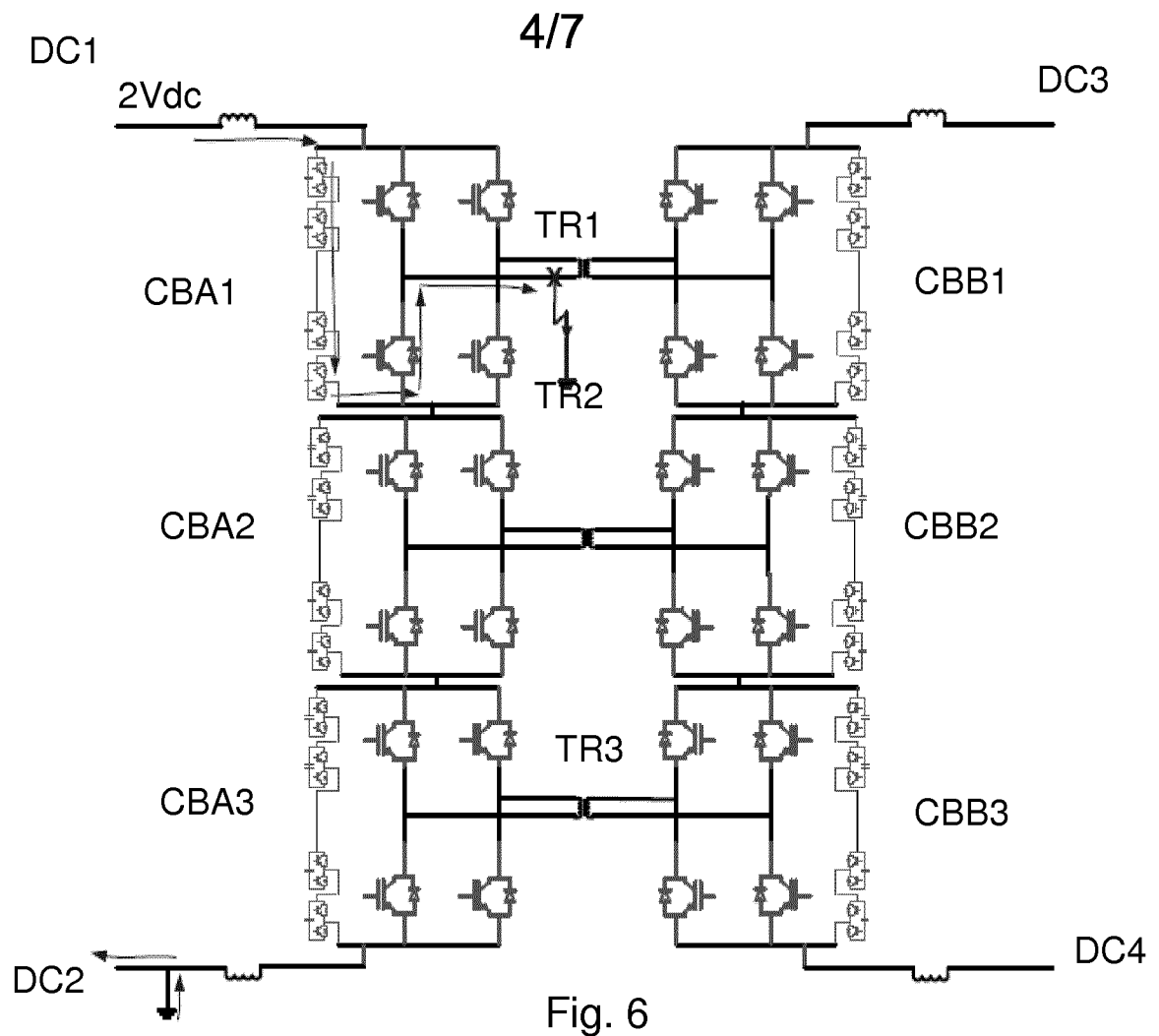


Fig. 5



5/7

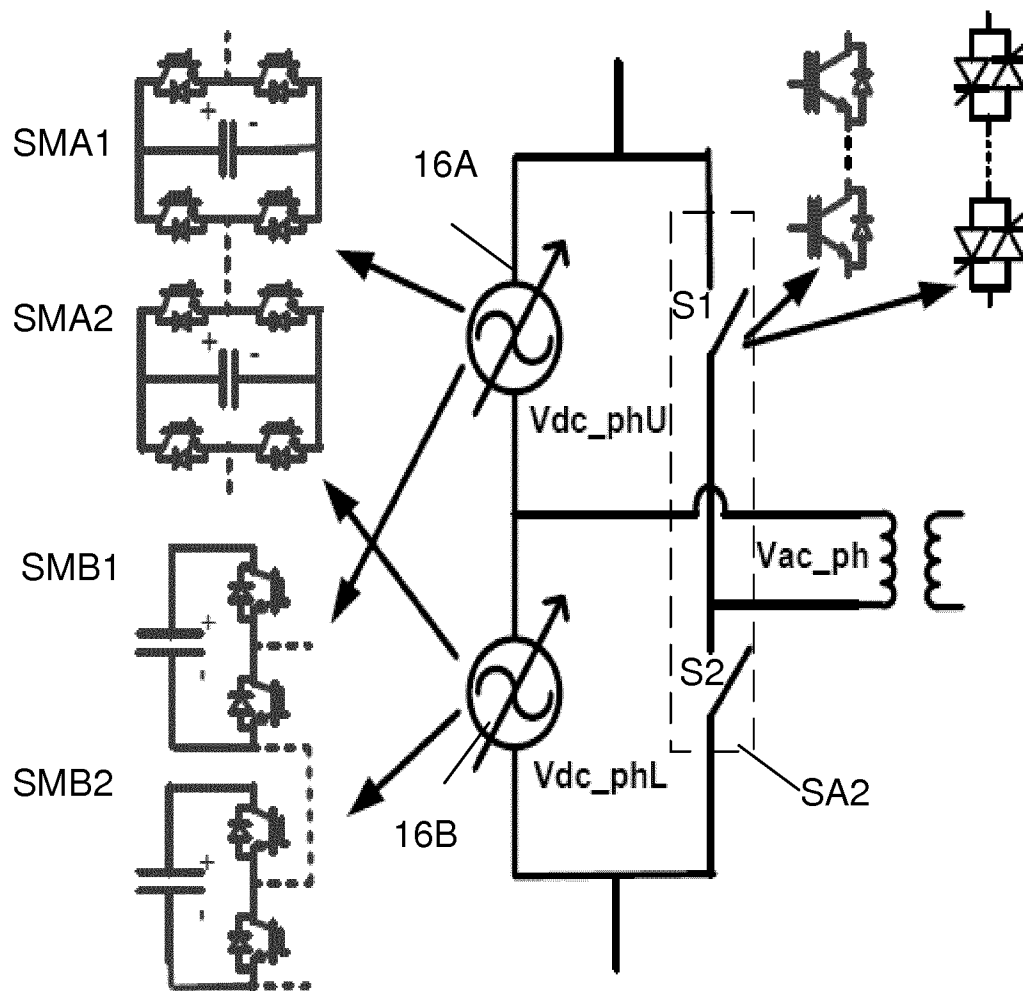


Fig. 8

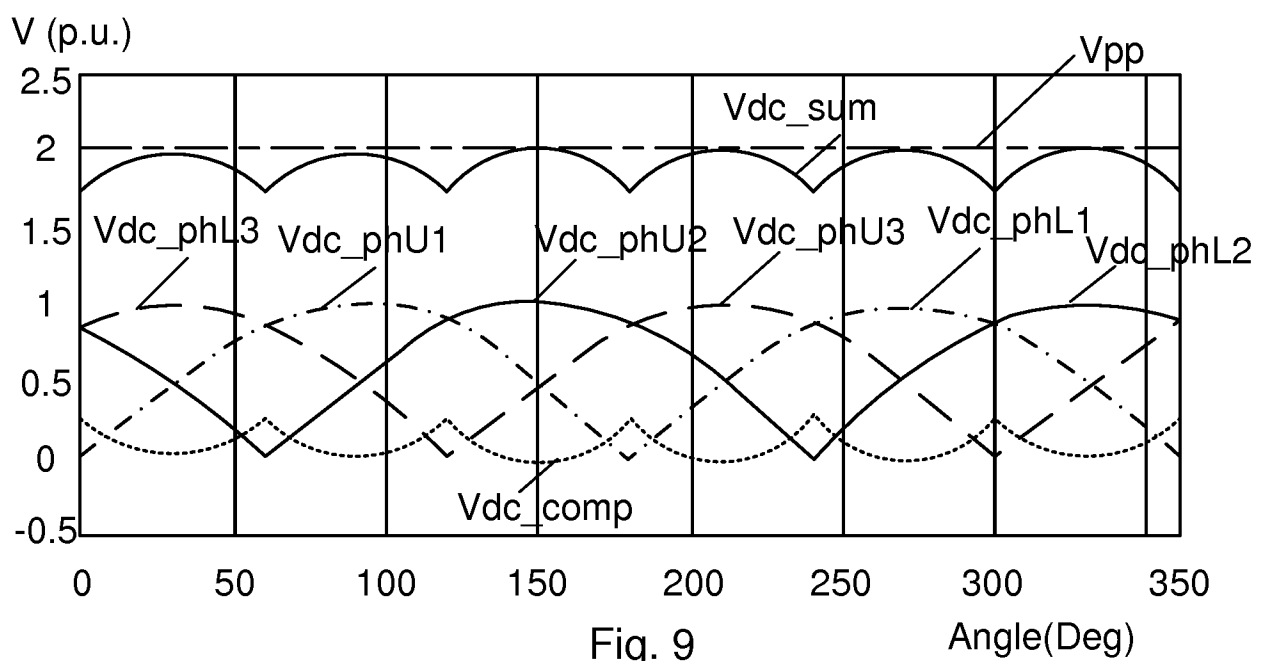


Fig. 9

6/7

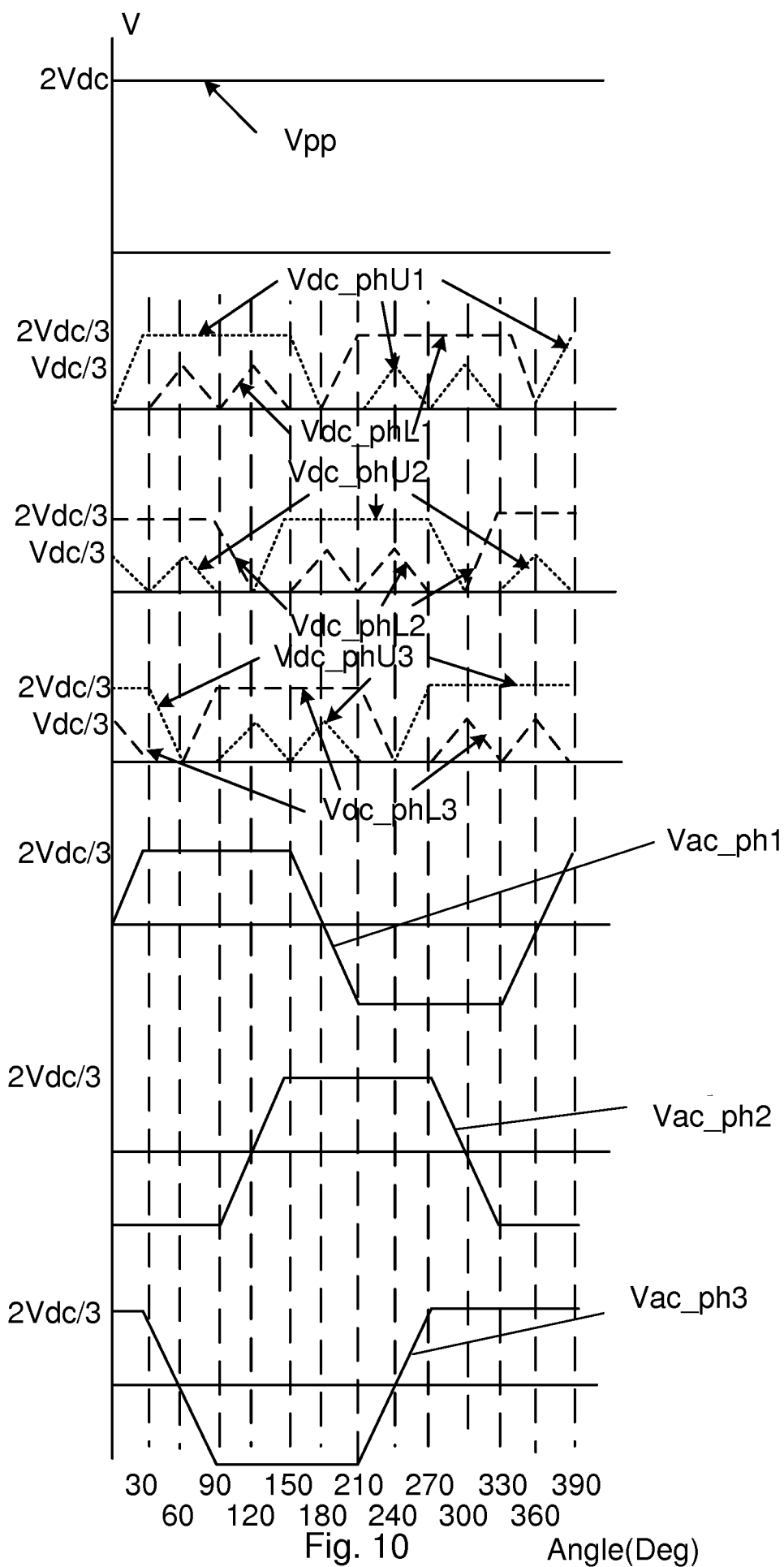


Fig. 10

Angle (Deg)

7/7

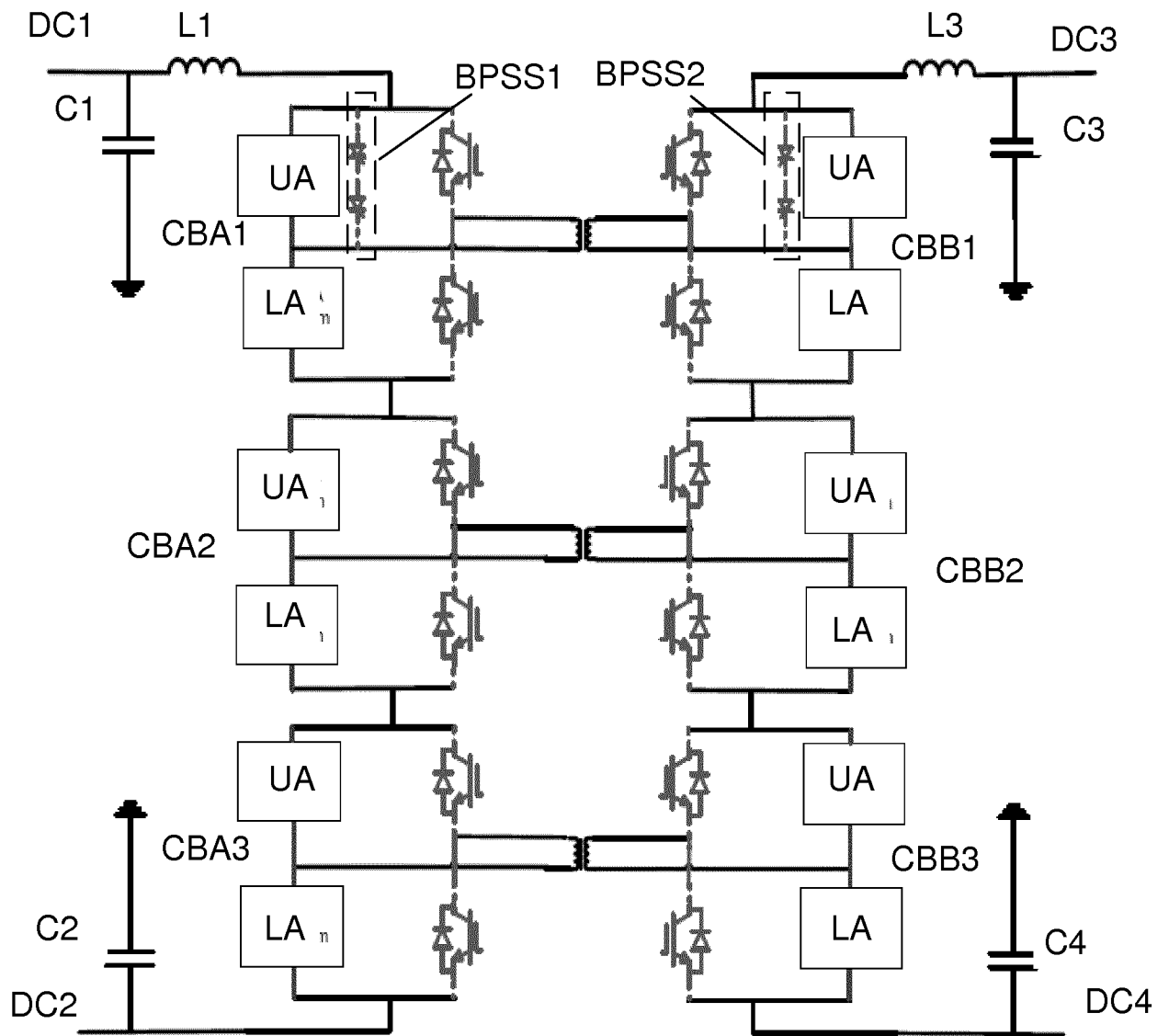


Fig. 11

INTERNATIONAL SEARCH REPORT

International application No

PCT/EP2018/065940

A. CLASSIFICATION OF SUBJECT MATTER

INV. H02M1/32 H02M3/335
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H02M

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	TRAINER D R ET AL: "B4-111 A new Hybrid Voltage-Sourced Converter for HVDC Power Transmission", CIGRE SESSION, CIGRE, PARIS, FR, 23 August 2010 (2010-08-23), pages 1-12, XP008134692,	1-3,6-15
A	figure 4 paragraph [0003]	4,5
Y	----- CN 104 283 436 A (UNIV HUAZHONG SCIENCE TECH) 14 January 2015 (2015-01-14) figure 1 ----- -/-	1-3,6,7, 11-15



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

12 March 2019

Date of mailing of the international search report

20/03/2019

Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040,
Fax: (+31-70) 340-3016

Authorized officer

Gotzig, Bernhard

INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2018/065940

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	M TOMASINI ET AL: "DC-Link voltage ripple minimization in a modular multilevel voltage source converter for HVDC power transmission", PROCEEDINGS OF THE 2011-14TH EUROPEAN CONFERENCE ON POWER ELECTRONICS AND APPLICATIONS (EPE 2011), 1 January 2011 (2011-01-01), pages 1-10, XP055212884, ISBN: 978-1-61-284167-0 figure 1 paragraph [0004] -----	1-3
Y	US 2018/091038 A1 (SUBRAMANIAN SASITHARAN [SE]) 29 March 2018 (2018-03-29) figure 9 paragraph [0088] -----	1-3,8-10

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/EP2018/065940

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
CN 104283436	A	14-01-2015	NONE	

US 2018091038	A1	29-03-2018	CN 107580751 A	12-01-2018
			EP 3292624 A1	14-03-2018
			US 2018091038 A1	29-03-2018
			WO 2016177398 A1	10-11-2016
