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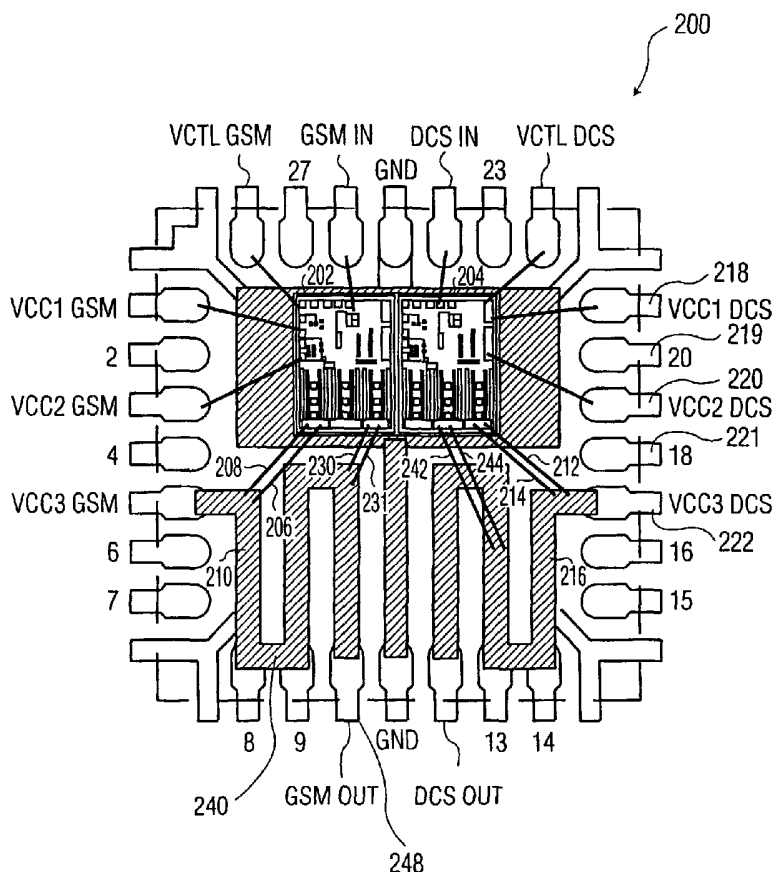
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(54) Title: PACKAGING OF AN INTEGRATED CIRCUIT WITH INTERNAL IMPEDANCE MATCHING



(57) Abstract: An integrated circuit package houses and connects to a die to form an integrated circuit with internal impedance matching. The package comprises a lead frame comprising at least one transmission line, a die paddle, and at least one input lead and at least one output lead. Bond wires connect select locations along the at least one transmission line to ground through impedance matching circuit components located within the integrated circuit to provide an impedance matching network associated with at least one of the output leads. The package may also substantially encapsulate the lead frame, while exposing the die paddle and the input/output leads.



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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H01L23/66

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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 00/75990 A (ERICSSON INC) 14 December 2000 (2000-12-14)	1-4, 6-8, 10-14, 16, 17, 19, 20, 22-27, 29, 32
A	abstract page 1, line 20 - line 27 page 3, line 3 - line 12 page 5, line 2 - line 24 page 7, line 2 - page 9, line 6; figures 7, 8 ----- -/--	5, 9, 15, 18, 28, 30

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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A		14,15, 21,25,31
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