



(43) International Publication Date
19 November 2009 (19.11.2009)

(10) International Publication Number
WO 2009/140631 A3

(51) International Patent Classification:
G06F 13/00 (2006.01)

(21) International Application Number:
PCT/US2009/044200

(22) International Filing Date:
15 May 2009 (15.05.2009)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
61/053,527 15 May 2008 (15.05.2008) US
61/053,522 15 May 2008 (15.05.2008) US

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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM,

AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

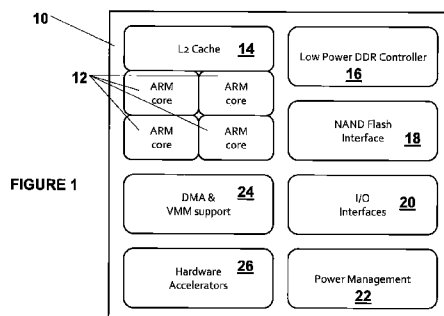
(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report (Art. 21(3))

(88) Date of publication of the international search report:
7 January 2010

(54) Title: DISTRIBUTED COMPUTING SYSTEM WITH UNIVERSAL ADDRESS SYSTEM AND METHOD



(57) Abstract: A distributed computing system that incorporates enhanced distributed storage and a universal address system and method are provided. The system and method are particularly applicable to a server on a chip processing unit. The universal address system and method has greater utility, such as to other computer systems and architectures that can utilize the universal address system and method. The universal address system and method can be used with various processing unit based systems such as single processor systems.



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INTERNATIONAL SEARCH REPORT

International application No.
PCT/US 09/44200

A. CLASSIFICATION OF SUBJECT MATTER

IPC(8) - G06F 13/00 (2009.01)

USPC - 711/119

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC(8): G06F 13/00 (2009.01)

USPC: 711/119

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

IPC(8): G06F 13/00 (2009.01) (Keyword limited - see search terms below)

USPC: 711/119,122,129,130; 710/65,74 (Keyword limited - see search terms below)

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

PubWEST (PGPB, USPT, EPAB, JPAB); Google Scholar

Search Terms Used: first second processors file system table walking nand nor flash virtual physical address space mapping

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2006/0259734 A1 (SHEU et al.), 16 November 2006 (16.11.2006), entire document, especially para [0002], [0023], [0038], [0059], [0072], [0075]-[0078], [0080]-[0082]	1-10
----- Y		11-25
Y	US 7,080,078 B1 (SLAUGHTER et al.), 18 July 2006 (18.07.2006), entire document, especially col 3 ln 27-28; col 7 ln 62-64; col 16 ln 14-16; col 42 ln 40 to col 43 ln 4; col 43 ln 23-38; col 63 ln 11-37	11-25
A	US 2006/0136570 A1 (PANDYA), 22 June 2006 (22.06.2006), entire document	1-25
A	US 2005/0015378 A1 (GAMMEL et al.), 20 January 2005 (20.01.2005), entire document	1-25

☐ Further documents are listed in the continuation of Box C.

* Special categories of cited documents:

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"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

22 June 2009 (22.06.2009)

Date of mailing of the international search report

01 JUL 2009

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P.O. Box 1450, Alexandria, Virginia 22313-1450

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