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Declaration under Rule 4.17:

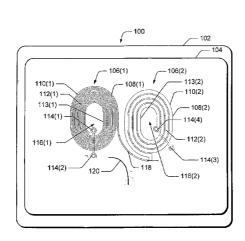
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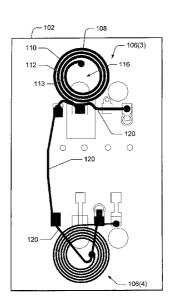
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(54) Title: INDUCTOR AND DECOUPLING STRUCTURES FOR FILTERS AND DESIGN METHOD THEREOF





(57) Abstract: Inductor topologies for filters used in broadband applications are described. In one implementation, an apparatus, such as an interface module includes a printed circuit board (102) and a filter (104). The filter includes a generally circular high Quality factor (Q) value spiral planar inductor (106) etched directly into the printed circuit board (102). The generally circular high Q value spiral planar inductor (106) is configured to operate in a broadband application. Other implementations involve the use of decoupling structures (120) with respect to the planar inductors as well as a design methodology for the filters.



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 before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

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US CL	: 336/180,199,200; 333/185,177,246; 703/14,2;	716/2,7 ; 455/307,26.1					
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C. DOC	UMENTS CONSIDERED TO BE RELEVANT						
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Further	documents are listed in the continuation of Box C.	See patent family annex.					
* S	pecial categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the					
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C. (Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT					
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Category * Y,P	Citation of document, with indication, where appropriate, of the relevant passages US 6,397,171 (BELK) 28 May 2002 (28.05.2002), abstract.	Relevant to claim No. 56-64 66-68 70 71			
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INTERNATIONAL SEARCH REPORT					
	Continuation of Item 4 of the first sheet: INDUCTOR AND DECOUPLING STRUCTURES FOR FILTERS AND DESIGN METHOD THEREOF				

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